Space Invaders Revamp Project Report



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Project Overview

In this project, we have recreated the classic "Space Invaders" arcade game from the 1970s on the De1-SoC FPGA. In this game, the player controls a defender spaceship that moves horizontally across the bottom of the screen and fires missiles at enemy alien ships. Every few seconds, the enemy alien ships shift down and approach the defender spaceship. Additionally, the enemy alien ships also drop bombs that the defender must avoid when moving around at the bottom of the screen. To proceed to the next level, the player must destroy all of the enemy alien ships before they reach the bottom of the screen.

To recreate this game on FPGA, we used a retro NES USB controller to get the user input. To handle this, we wrote a device driver in software that correctly forwards the user input to the top level software logic. Our software controls all of the Space Invaders logic and passes the data to byte-addressable VRAM in hardware, which is then displayed on a VGA monitor.



Top-Level Architecture



<u>Hardware</u>

- Our approach for displaying the graphics data involved utilizing a tile-and-sprites method. This process is done with four tables: a pattern name, pattern generation, sprite attribute, and sprite generation table.

#define PATTERN_NAME_TABLE 0
#define PATTERN_GENERATOR_TABLE 1
#define SPRITE_ATTRIBUTE_TABLE 2
#define SPRITE_GENERATOR_TABLE 3

- Tiles were employed to display the user interface and gameplay messages. To accomplish this, we used a pattern generator table, which is addressed by 12 bits and results in 4096 rows. Since every pattern tile requires 32 bytes, we had the capacity to store up to 128 distinct patterns. A pattern name table parses the generator table and obtains the corresponding pattern attributes. The pattern table does not require all 12 bits of addressing; this was kept in case new patterns and UI features wanted to be added.
- The sprites are displayed in a similar structure to the pattern tiles, except we assumed that all of the sprites are moving components unlike the tiles. Because Space Invaders has a lot of moving parts (20+ ships, missiles, bombs), we needed to create a very large sprite generator table. Each sprite requires 128 bytes and 32 address bits were required to create the rows in our sprite generator table. We used a sprite attribute table to store the addresses of each sprite. Each sprite attribute contains the y position, x position, and the sprite address from the generator table. A combinational block allows for colors to be prioritized and for sprites to be displayed in front of the tiles.
- Additionally, each sprite requires its own state machine. This is particularly tricky with Space Invaders, since there are many enemies and the horizontal count of a sprite must not overlap with another sprite. If a sprite needs to be displayed on the following line, the sprite generator table is accessed from the designated base address. The horizontal position of the sprite is then loaded into a down counter, while the sprite pixels are loaded into a shift register. When the next vertical line is reached, the down counter decreases, and a 4-bit pixel value is retrieved from the shift register, which corresponds to the 24-bit RGB color value. Since Space Invaders only needs green and white as colors, a color translation table was created, where a 4-bit pixel value maps to the 24-bit RGB value.

Avalon Bus: HW/SW Interface

- Our hardware interface accepts a 32 bit write packet from software that is structured as follows:
 - 1) Bits 0 1: Table Selector [pat_name, pat_gen, sprite_attr, sprite_gen]
 - 2) Bits 2 17: 16-bit Destination Address
 - 3) Bits 24 31: 8-bit Data to Write at Destination Address

AUDIO INTERFACE

Audio CODEC is interfaced with the Avalon bus using Avalon Stream Interface. Avalon-ST is an interface that supports the unidirectional flow of data, including multiplexed streams, packets, and DSP data. The audio streaming interface consists of 3 signals: data signal, read signal, and a valid signal. The data signal carries the actual audio data, while the valid signal indicates when the data is valid and should be processed, and the read signal is used to control the flow of the data.

Use	Connections	Name	Description	Export	Clock	Base	End
V		□ clk 0	Clock Source				
_	· · · · · · · · · · · · · · · · · · ·	- clk in	Clock Input	clk	exported		
	0 0 D	clk in reset	Reset Input	reset			
		clk	Clock Output	Double-click to export	clk 0		
		clk reset	Reset Output	Double-click to export	1		
P		🗆 🕮 hps 0	Arria V/Cyclone V Hard Proce				
-		h2f user1 clock	Clock Output	Double-click to export	hps 0 h2f userl clock		
	~~	memory	Conduit	hps_ddr3			
		hns io	Conduit	hps			
		h2f reset	Reset Output	Double-click to export			
		h2f avi clock	Clock Input	Double-click to export	clk 0		
		h2f avi master	AYI Master	Double-click to export	(b2f avi clock)		
		f2h ovi slock	Clock Input	Double-click to export	clk 0		
	T T L T T	folk and eleve	AVI Claure	Double-click to export	KOh aut ala ala)		
		h 26 ku sui slask	AAI SIAVE	Double-click to export	[IZN_AXI_CIOCK]		
		nzi_iw_axi_clock	Clock input	Double-click to export	CIK_0		
		n2r_w_axi_master	AXI Master	Double-click to export	[n2t_IW_axi_clock]	700	
		r zn_irqu	Interrupt Receiver	Double-click to export		IRQ 0	1 INU
-	· · · · · · · · · · · · · · · · · · ·	T2n_irq1	Interrupt Receiver	Double-click to export		THU C) THŮ
		euglaudio_pll_0	Audio Clock for DE-series Boa				
		ref_clk	Clock Input	Double-click to export	clk_0		
		ref_reset	Reset Input	Double-click to export			
		H audio_clk	Clock Output	audio_pll_0_audio_clk	audio_pll_0_audio_clk		
		reset_source	Reset Output	Double-click to export			
~		audio_and_video_config_0	Audio and Video Config				
		r clk	Clock Input	Double-click to export	clk_0		
		• reset	Reset Input	Double-click to export	[clk]		
		avalon_av_config_slave	Avalon Memory Mapped Slave	Double-click to export	[clk]	÷	
	0-C	← external_interface	Conduit	audio_and_video_config_0_external_interf			
~		⊡ audio_0	Audio				
	• • • • • · · · · · · · · · · · · · · ·	+ clk	Clock Input	Double-click to export	clk_0		
		reset	Reset Input	Double-click to export	[clk]		
		avalon_left_channel_source	Avalon Streaming Source	Double-click to export	[clk]		
		avalon_right_channel_source	Avalon Streaming Source	Double-click to export	[clk]		
		avalon_left_channel_sink	Avalon Streaming Sink	audio_0_avalon_left_channel_sink	[clk]		
		avalon_right_channel_sink	Avalon Streaming Sink	audio_0_avalon_right_channel_sink	[clk]		
		 external_interface 	Conduit	audio_0_external_interface			
~		⊡ vga_ball_0	VGA Ball				
		+ clock	Clock Input	Double-click to export	clk_0		
L		reset	Reset Input	Double-click to export	[clock]		
		avalon slave 0	Avalon Memory Mapped Slave	Double-click to export	[clock]	= 0x0000 0000	0x0000 0007
		≻ vga	Conduit	vga	[clock]	-	
L		avalon streaming source r	Avalon Streaming Source	Double-click to export	[clock]		
		avalon streaming source I	Avalon Streaming Source	Double-click to export	[clock]		
~		onchip_memory2_0	On-Chip Memory (RAM or ROM				
	♦ ♦	clk1	Clock Input	Double-click to export	clk_0		
	↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓	sl	Avalon Memory Mapped Slave	Double-click to export	[clk1]	= 0x0000 1000	0x0000 1fff
	↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓	reset1	Reset Input	Double-click to export	[clk1]	_	
V		onchip memory2 1	On-Chip Memory (RAM or ROM				
	l ♦ ♦ ♦ • • • • • • • • • • • • • •	clk1	Clock Input	Double-click to export	clk 0		
L	0- •	sl	Avalon Memory Mapped Slave	Double-click to export	[clk1]	= 0x0000 2000	0x0000 2fff
L	• • • •	reset1	Reset Input	Double-click to export	[clk1]	-	-

<u>Software</u>

Game Logic

- Game Stages: (STAGE_MENU, STAGE_IN_GAME, STAGE_END)

The game was set to refresh every 8ms from a counter that iterated through the MAX_INT and modulo 40 division. The game interface keeps track of the current level, current lives, current points, and different audio tracks when an event occurs. The levels increase once all the enemy ships have been defeated. The lives will decrease after the player ship is hit with an enemy bomb. The points will increase according to the enemy ship defeated by the player ship. The audio tracks will be determined by an event taking place - for example, an enemy is damaged, the game is over, the player takes damage, and background music.

- Game State:

The game state holds information related to the objects it must keep track of during updates and relevant inputs. The struct show below holds all the information:

typedef struct {		
<pre>pthread_mutex_t mu; defender_t defender; enemy_t aliens[4]; bullet_t bullets[2]; bomb_t bomb; game_stage_t stage; } game_state_t; int MAXBULLETS = 3; int MAXBULLETS = 3; int dropped = 0; int fired = 100; int lives = 3; int score = 0;</pre>	<pre>typedef enum { DIR_NONE, DIR_LEFT, DIR_RIGHT, DIR_UP, DIR_DOWN, } dir_t;</pre>	<pre>typedef struct { uint8_t i; uint16_t y; uint16_t x; uint8_t name; } sprite_attr_t;</pre>

The struct on the left shows the defender, the enemies, bullets, bombs, and game stage; along with settable parameters for bullets, bombs, lives, and score. The struct in the middle explains the direction attributes that are set within the different instances of the game state. The struct on the right explains the relevant attributes that sprite will take into consideration when creating the instances on screen.

- Defender Ship State & Function:

The player ship will be "defending" the Earth by firing shots at the incoming enemy ships. The player ship receives directional commands and firing commands from the joystick peripheral in a struct. The player ship can be harmed by the incoming bullets from enemy ships and a life will be taken from the game interface. The player ship does not have a limited number of bullets, but it does have a cooldown on how fast the player can fire. Another counter has a cooldown period of 200ms. There is only one reference to an instance of the defender in the game state, so it is a non-array object with the attributes shown below.



As the defender ship is a pretty simple instance, it only requires sprite attributes and direction. The defender movement function will also call a check function each update cycle in order to evaluate if any bomb has hit the ship.

- Enemy Ship State & Function:

The enemy ships will be "invading" the Earth by slowly moving down toward the defending ship and dropping periodic bombs too. During the process, they "bounce" back and forth across the screen and turn directions each time the end ships hit the screen edge. Once the lowest ship reaches the player or if all the enemy ships are eliminated, the game is over. Additionally, different levels of enemy ships can appear as the player progresses, which will require more shots to defeat the enemy ship. The enemy ship state will have to maintain these hitpoint values. The bombs will be dropped with a 10% chance for each iteration, meaning 10 movements will result in a bomb being dropped. A maximum of 3 bombs can be on the screen at once and can be programmed accordingly. The defender is allowed to move at a constant rate of 3 pixels every 8ms.

```
typedef struct {
   dir_t dir;
   sprite_attr_t attr;
   int alive;
} enemy_t;
```

The enemy instance is similar to the defender instance except for the lives being an attribute rather than an overall calculation. This is due to the fact that multiple instances of an enemy are produced and must be looped through during each update to check for events. The alive count will be increased as the levels get more difficult; when the alive count reaches 0, the enemy dies. Enemies are allowed to move at a starting rate of 2 pixels every 8ms.

- Bullet State & Function:

The bullet is instantiated once the controller button A has been pressed by the user. The function will check each bullet instance in the game state class and determine if a maximum number of bullets have already been called. If there is room for another bullet, the alive attribute is incremented on the bullet class to signal it has been instantiated. The bullet is fired directly from the cannon of the defender toward enemy ships and propagates at a rate of 8 pixels per 8ms.



Similarly, the sprite and direction of the bullet are updated every time the bullet is re/instantiated during updates and relevant events. Once a bullet has hit an enemy or gone off screen, the alive attribute is decremented and the sprite disappears. It is then when a new bullet can be queued to be fired. There can only be a maximum of 3 bombs on the screen at once.

- Bomb State & Function:

The bomb is instantiated once the probability that an enemy drops a bomb has been reached. The function will instantiate a bomb in the same place that the enemy is located. The bomb will propagate at a rate of 8 pixels per 8ms toward the defender.



Following suit with the previous structs, the direction and sprite attributes are also listed here. Once a bomb has hit the defender or gone off screen, the bomb sprite is reset and the count is decremented.

- Setup Game & Reset Game Functions:

The setup game and reset functions are one in the same once the game has been loaded in. The reset will iterate through all the class instances in the game state struct and assign appropriate starting attributes and locations to relevant characters. The sprite class uses a unique identifier that each unique ship/defender/bullet/bomb must be assigned before it is called for the first time. There is a series of for loops that assign these unique integers to each sprite struct. Each time the game is reset or started for the first time, this function is called and the ships are lined up on the top of the screen, with the defender on the bottom of the screen.

- Main State & Function:

The main state function is a constant loop that updates relevant game states based on the current stage of the game (START, IN_GAME, END). For the start, the patterns are assigned to explain directions to start the game. Additionally, the screen is cleared from all previous sprites, the score is cleared, and the lives are reset back to 3. Once the START button is pressed on the controller, the game stage is set to IN_GAME, where the loop continually calls tracking functions for the defender, enemy, bullets, and bombs.

Additionally, the score and lives are constantly updated with every call. Once the game is ended (either by win or lose), the relevant integer indicating whether the game was a win or loss is passed into the END stage. The "win" screen will print out congratulations and the score. A "lose" screen will print out a game over and the score. Both end states will print out the instructions to reset the game. Once the reset button is clicked, the screen is cleared and the initial state of START is set and the while loop restarts at the beginning.

Gamepad Controller

The joystick peripheral must have communication algorithms that will relay important information for each button. The following functions will be implemented:

- move_left() \rightarrow button movement will indicate left translation of pixels of player ship
- move_right() → button movement will indicate right translation of pixels of player ship
- fire_bullet() \rightarrow button press will launch bullet pixels from player ship
- start() \rightarrow start button will start game in beginning
- select() \rightarrow select button will reset game after lives are terminated or game is won

Kernel Space Driver

- The kernel driver follows the following struct: a uint8_t table, a uint16_t addr, and a uint8_t data field. These values are concatenated together to pass the 32 bits of write data to hardware.

```
typedef struct {
    uint8_t table;
    uint16_t addr;
    uint8_t data;
} vga ball arg t;
```

User Space Driver

- In vga_ball_write, an ioctl call is made similar to the vga_ball done in Lab 3. This is called in set_sprite and set_pattern to pass the hardware the 32-bit packet containing the table, addr, and data information.

```
void vga_ball_write(vga_ball_arg_t *arg)
{
   //fprintf(stderr, "vga_ball_write called\n");
   if(ioctl(vga_ball_fd, VGA_BALL_WRITE, arg))
   {
     perror("ioctl(VGA_BALL_SET_BACKGROUND) failed");
     return;
   }
}
```

```
void set sprite(sprite attr t attr)
{
  vga ball arg t arg;
  int start;
  start = 4 * attr.i;
  arg.table = SPRITE ATTRIBUTE TABLE;
  arg.addr = start;
  arg.data = (uint8_t)(attr.y / 2);
  vga ball write(&arg);
  arg.addr = start + 1;
  arg.data = (uint8_t)(attr.x / 2);
  vga ball write(&arg);
  arg.addr = start + 2;
  arg.data = attr.name;
  vga ball write(&arg);
}
```

Lessons Learned

- Test hardware in parallel with other work! Compiling Quartus and copying the .dts and .rbf files to the FPGA is extremely time consuming. It's also very easy to lose track of what changes to hardware were made and why. When working on hardware, write out a set plan of implementation changes to try and keep track of the changes. While Quartus is compiling, work on software in parallel.
- Get the HW/SW interface working as soon as possible. Coding and understanding how software passes information to the hardware is vital to any video game project that requires a display. Following the interface, sprites and bitmapping can be implemented to see how changes in software display on the actual VGA peripheral.
- Start with a strong basis on hardware. Once the hardware is correctly implemented with basic test cases in software, this will make the challenge of software testing an isolated experiment. During our programming, we progressed with the hardware at a level to comfortably test 5 sprites. We perfected the algorithms for 5 sprites assuming that adding more hardware to support more sprites would be intuitive. However, once the sprites would not perform as expected, the isolation of errors was now expanded to both the hardware and software. This made the process extremely time consuming and difficult.

Project Breakdown

Alan Hwang	Game Hardware & Basic Software
Zach Burpee	Game Software & Basic Hardware
Mili Sehgal	Audio Hardware & Basic Software

Code Screen Shots - Hardware

vga_ball.sv

vga_ball.sv Open 👻 🖪 Save = _ • × -hw/archive * Avalon memory-mapped peripheral that generates VGA * Stephen A. Edwards * Columbia University */ module vga_ball(input logic clk, input logic reset, input logic [31:0] writedata, input logic write, chipselect, input input logic [3:0] address, output logic [7:0] VGA_R, VGA_G, VGA_B, output logic VGA_CLK, VGA_HS, VGA_VS, VGA_BLANK_n, output logic VGA SYNC n); logic [10:0] hcount: logic [9:0] vcount; cogic [9:0] vcount; logic [3:0] out_pixel[32:0]; //output pixels values from each of 32 sprites + 1 pattern logic [3:0] final_out_pixel; //actual output pixel to display logic [7:0] background_r, background_g, background_b; logic [23:0] rgb_val; //final_RGB value to display //for pattern name table
logic [11:0] ra_n, wa_n; //12 bits logic we_n; logic [7:0] din_n; logic [7:0] dout_n; //for pattern generator table logic [10:0] ra_pg, wa_pg; //change later logic we_pg; logic [7:0] din_pg; logic [7:0] dout_pg; //for sprite attribute table logic [31:0] ra_a, wa_a; //32 simultaneous sprites logic [51:0] ru_u, logic we_a; logic [7:0] din_a; logic [7:0] dout_a; //for sprite generator table
logic [11:0] ra_g, wa_g; //32*128 sprite -> 12 bit addr logic we_g; logic [7:0] din_g; logic [7:0] dout_g; logic [31:0] sprite_base_addr[31:0]; //sprite attr table base address logic [11:0] h_start[31:0]; //hcount at which sprite_prep n starts logic [31:0] sprite_ra_a[31:0]; //requested read address for sprite attr table from sprite prep modules logic [11:0] sprite_ra_g[31:0]; //requested read address for sprite gen table from sprite prep modules //determines where each sprite prep instance will start reading the attr table from assign sprite_base_addr[0]=32'h0; assign sprite_base_addr[1]=32'h4; assign sprite_base_addr[1]=32'h4; assign sprite base addr[3]=32'hc; assign sprite base addr[4]=32'h10; assign sprite_base_addr[5]=32'h14; assign sprite base addr[6]=32'h18; assign sprite_base_addr[7]=32'hlc; assign sprite_base_addr[8]=32'h20; assign sprite_base_addr[9]=32'h24; assign sprite_base_addr[10]=32'h28; assign sprite_base_addr[11]=32'h22; Ln 11, Col 42 🔻 INS SystemVerilog - Tab Width: 8 -

Open 👻 🖪	vga_ball.sv	Save	≡	-		×
assign	spiile_base_adur[1]=32-114;					
assign	sprite_base_addr[2]=32'h8;					
assign	spille_base_add([5]=52 lic;					
assign	<pre>sprite_base_addr[4]=32'h10;</pre>					
assign	sprite_base_addr[5]=32'h14;					
assign	sprite base addr[7]=32'hlc;					
assign	sprite_base_addr[8]=32'h20; sprite_base_addr[8]=32'h24;					
assign	sprite base addr[10]=32 h28;					
assign	<pre>sprite_base_addr[11]=32'h2c;</pre>					
accion	corite bace addr[12]=32/b30.					
assign	sprite base addr[13]=32'h34;					
assign	<pre>sprite_base_addr[14]=32'h38;</pre>					
assign	sprite_base_addr[15]=32'h3c;					
assign	<pre>sprite_base_addr[16]=32'h40;</pre>					
assign	<pre>sprite_base_addr[17]=32'h44;</pre>					
assign	sprite_base_addr[18]=32'h48; sprite_base_addr[19]=32'h4c;					
doorgi	sprite_base_dat(ts) sprite;					
assign	sprite_base_addr[20]=32'h50;					
assign assign	sprite base addr[22]=32'h54;					
assign	sprite_base_addr[23]=32'h5c;					
	and the base of the fact and the second					
assign assign	sprite_base_addr[24]=32'h00; sprite_base_addr[25]=32'h64:					
assign	<pre>sprite_base_addr[26]=32'h68;</pre>					
assign	<pre>sprite_base_addr[27]=32'h6c;</pre>					
assign	<pre>sprite base addr[29]=32'h70;</pre>					
assign	<pre>sprite_base_addr[30]=32'h74;</pre>					
assign	<pre>sprite_base_addr[31]=32'h78;</pre>					
//dete	rmines when each sprite prep instance will start processing sprites					
assign	h_start[0]=12'b010100100000; //1312					
assign	h_start[1]=12'b010100111010; //1338 h_start[2]=12'b010101010100• //1364					
assign	h_start[3]=12'b0101011101110; //1390					
assign	h_start[4]=12'b010110001000; //1416					
assign	h start[5]=12'd1442: //1442					
assign	h_start[6]=12'd1468; //1468					
assign	h_start[7]=12'd1494; //1494					
assign	h start[9]=12'd1546; //1546					
assign	h_start[10]=12'd1572; //1572 h_start[11]=12'd1598; //1598					
assign	h start[12]=12'd1624; //1624					
assign	h_start[13]=12'd1650; //1650					
assign	h_start[14]=12'd16/6; //16/6					
assign	h_start[15]=12'd1702; //1702					
assign	h_start[16]=12'd1728; //1728					
assign	h start[18]=12'd1780; //1780					
assign	h_start[19]=12'd1806; //1806					
accion	h start[20]-12/d1832+ //1832					
assign	h start[21]=12'd1858; //1858					
assign	h_start[22]=12'd1884; //1884					
assign	h_start[23]=12'd1910; //1910 h_start[24]=12'd1936: //1936					
assign						
assign	h_start[25]=12'd1962; //1962					
assign	h_start[26]=12'd1988; //1988 h_start[27]=12'd2014: //2014					
assign	h_start[20]=12/d2014, //2014 h_start[20]=12/d2010+ //2014					
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Open	•	vga_ball.sv ~/Downloads/spaceinvaders-hw/archive	Save	≡	-	•	×
	assign	h_start[29]=12'd2066; //2066					
	assign assign //assig	h_start[30]=12'd2092; //2092 h_start[31]=12'd2118; //2118 n h_start[32]=12'd214; //2144					
	vga_cou patt_na patt_ge	nters counters(.clk50(clk), .*); me_table pn1(.clk(clk), .ra(ra_n), .wa(wa_n), .we(we_n), .din(din_n), .dout(dout_n)); n_table pg1(.clk(clk), .ra(ra_pg), .wa(wa_pg), .we(we_pg), .din(din_pg), .dout(dout_pg));				
	sprite_ sprite_ color_l	attr_table satl(.clk(clk), .ra(ra_a), .wa(wa_a), .we(we_a), .din(din_a), .dout(dout_a)) gen_table sgtl(.clk(clk), .ra(ra_g), .wa(wa_g), .we(we_g), .din(din_g), .dout(dout_g)); ut cll(.color_code(final_out_pixel), .rgb_val(rgb_val));	;				
	pattern .dout_n	_prep pp0(.clk(clk), .reset(reset), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA_ (dout_n), .dout_g (dout_pg), .ra_n (ra_n), .ra_g(ra_pg), .out_pixel(out_pixel[32]));	BLANK_n)	,			
sp0(.cl	sprite_ k(clk), .dout_a	prep .reset(reset), .h_start(h_start[0]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[0]), .ra_g(sprite_ra_g[0]), .out_pixel(_BLANK_r out_pixe	ı), .b el[<mark>0</mark>])	ase_ao);	dr(:	sprit
sp1(.cl	sprite_ k(clk), .dout_a	prep .reset(reset), .h_start(h_start[1]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[1]), .ra_g(sprite_ra_g[1]), .out_pixel(_BLANK_r out_pixe), .b el[<mark>1</mark>])	ase_ao);	dr(sprit
sp2(.cl	sprite_ k(clk), .dout_a	prep .reset(reset), .h_start(h_start[2]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[2]), .ra_g(sprite_ra_g[2]), .out_pixel(_BLANK_r out_pixe	1), .b el[<mark>2</mark>])	ase_ao);	dr(sprit
sp3(.cl	sprite_ k(clk), .dout_a	prep .reset(reset), .h_start(h_start[3]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[3]), .ra_g(sprite_ra_g[3]), .out_pixel(_BLANK_r out_pixe	ı), .b el[3])	ase_ac);	dr(sprit
sp4(.cl	sprite_ k(clk), .dout_a	prep .reset(reset), .h_start(h_start[4]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[4]), .ra_g(sprite_ra_g[4]), .out_pixel(_BLANK_r out_pixe	ı), .b el[4])	ase_ao);	dr(:	sprit
sp5(.cl	sprite_ k(clk), .dout_a	prep .reset(reset), .h_start(h_start[5]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[5]), .ra_g(sprite_ra_g[5]), .out_pixel(_BLANK_r out_pixe	ı), .b el[<mark>5</mark>])	ase_ac);	dr(:	sprit
sp6(.cl	sprite_ k(clk), .dout_a	prep .reset(reset), .h_start(h_start[6]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[6]), .ra_g(sprite_ra_g[6]), .out_pixel(nren	_BLANK_r out_pixe	ı), .b el[<mark>6</mark>])	ase_ao);	dr(؛	sprit
sp7(.cl	k(clk), .dout_a	.reset(reset), .h_start(h_start[7]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[7]), .ra_g(sprite_ra_g[7]), .out_pixel(_BLANK_r out_pixe	n), .b el[<mark>7</mark>])	ase_ao);	ldr(s	sprit
sp8(.cl	sprite_ k(clk), .dout_a	prep .reset(reset), .h_start(h_start[8]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[8]), .ra_g(sprite_ra_g[8]), .out_pixel(_BLANK_r out_pixe	n), .b el[<mark>8</mark>])	ase_ac);	dr(sprit
sp9(.cl	sprite_ k(clk), .dout_a	prep .reset(reset), .h_start(h_start[9]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[9]), .ra_g(sprite_ra_g[9]), .out_pixel(_BLANK_r out_pixe	ı), .b :l[<mark>9</mark>])	ase_ao);	dr(؛	sprit
sp10(.c	sprite_ lk(clk), .dout_a	prep .reset(reset), .h_start(h_start[10]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(V (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[10]), .ra_g(sprite_ra_g[10]), .out_pixe	GA_BLANM l(out_pi	(_n), .xel[1	.base_ 0]));	_add	r(spr
sp11(.c	sprite_ lk(clk), .dout_a	prep .reset(reset), .h_start(h_start[11]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(V (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[11]), .ra_g(sprite_ra_g[11]), .out_pixe	GA_BLANK l(out_pi	(_n), .xel[<mark>1</mark>	.base 1]));	_add	r(spr
sp12(.c	sprite_ lk(clk), .dout_a	prep .reset(reset), .h_start(h_start[12]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(V .(dout_a), .dout_g(dout_g), .ra_a (sprite_ra_a[12]), .ra_g(sprite_ra_g[12]), .out_pixe	GA_BLANM l(out_pi	(_n), .xel[<mark>1</mark>	.base_ 2]));	_add	r(spr
	sprite_	prep					
		SystemVerilog 👻 Tab Width: 8 👻	Ln 1	.1, Col 4	12	r	INS

<pre>sprite_prep spl7(.clk(clk), .reset(reset), .h_start(h_start[17]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA_B spl7(.clk(clk), .reset(reset), .h_start(h_start[17]), .hcount(hcount), .vcount(vcount), .vGA_BLANK_n(VGA_ANK_n(VGA_BLANK_n(VGA_ANK</pre>	LANK_ t_pix				
.uour_a (uour_a), .uour_g (uour_g), .ia_a (sprire_ra_a[i/j), .ra_g(sprire_ra_g[i/]), .Out_pixel(Ou		n), el[<mark>1</mark>	.base_ 7]));	addr	(sp
sprite_prep sp18(.clk(clk), .reset(reset), .h_start(h_start[18]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA_B .dout_a (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[18]), .ra_g(sprite_ra_g[18]), .out_pixel(ou	LANK_ t_pix	n), el[<mark>1</mark>	.base_ 8]));	addr	(sp
sprite_prep sp19(.clk(clk), .reset(reset), .h_start(h_start[19]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA_B .dout_a (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[19]), .ra_g(sprite_ra_g[19]), .out_pixel(ou	LANK_ t_pix	n), el[<mark>1</mark>	.base_ 9]));	addr	(sp
<pre>sprite_prep sp20(.clk(clk), .reset(reset), .h_start(h_start[20]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA_B .dout_a (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[20]), .ra_g(sprite_ra_a[20]), .out_pixel(ou</pre>	LANK_ t_pix	n), el[20	.base_ 0]));	addr	(sp
<pre>sprite_prep sp21(.clk(clk), .reset(reset), .h_start(h_start[21]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA_B .dout_a (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[21]), .ra_g(sprite_ra_g[21]), .out_pixel(ou</pre>	LANK_ t_pix	n), el[<mark>2</mark>	.base_ 1]));	addr	(sp
<pre>sprite_prep sp22(.clk(clk), .reset(reset), .h_start(h_start[22]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA_B .dout_a (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[22]), .ra_g(sprite_ra_g[22]), .out_pixel(ou</pre>	LANK_ t_pix	n), el[<mark>2</mark>	.base_ 2]));	addr	(sp
<pre>sprite_prep sp23(.clk(clk), .reset(reset), .h_start(h_start[23]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA_B .dout_a (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[23]), .ra_g(sprite_ra_g[23]), .out_pixel(ou</pre>	LANK_ t_pix	n), el[<mark>2</mark>	.base_ 3]));	addr	(spi
<pre>sprite_prep sp24(.clk(clk), .reset(reset), .h_start(h_start[24]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA_B .dout_a (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[24]), .ra_g(sprite_ra_g[24]), .out_pixel(ou</pre>	LANK_ t_pix	n), el[<mark>2</mark> 4	.base_ 4]));	addr	(sp
<pre>sprite_prep sp25(.clk(clk), .reset(reset), .h_start(h_start[25]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA_B .dout_a (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[25]), .ra_g(sprite_ra_g[25]), .out_pixel(ou</pre>	LANK_ t_pix	n), el[<mark>2</mark>	.base_ 5]));	addr	(sp
<pre>sprite_prep sp26(.clk(clk), .reset(reset), .h_start(h_start[26]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA_B .dout_a (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[26]), .ra_g(sprite_ra_g[26]), .out_pixel(ou</pre>	LANK_ t_pix	n), el[20	.base_ 6]));	addr	(sp
<pre>sprite_prep sp27(.clk(clk), .reset(reset), .h_start(h_start[27]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA_B .dout_a (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[27]), .ra_g(sprite_ra_g[27]), .out_pixel(ou</pre>	LANK_ t_pix	n), el[<mark>2</mark>	.base_ 7]));	addr	(sp
<pre>sprite_prep sp28(.clk(clk), .reset(reset), .h_start(h_start[28]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA_B .dout_a (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[28]), .ra_g(sprite_ra_g[28]), .out_pixel(ou</pre>	LANK_ t_pix	n), el[<mark>2</mark>	.base_ 8]));	addr	(sp
sprite_prep sp29(.clk(clk), .reset(reset), .h_start(h_start[29]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA_B .dout_a (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[29]), .ra_g(sprite_ra_g[29]), .out_pixel(ou	LANK_ t_pix	n), el[<mark>2</mark>	.base_ 9]));	addr	(sp
<pre>sprite_prep sp30(.clk(clk), .reset(reset), .h_start(h_start[30]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA_B .dout_a (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[30]), .ra_g(sprite_ra_g[30]), .out_pixel(ou</pre>	LANK_ t_pix	n), el[<mark>3</mark>	.base_ 0]));	addr	(spi
sprite_prep sp31(.clk(clk), .reset(reset), .h_start(h_start[31]), .hcount(hcount), .vcount(vcount), .VGA_BLANK_n(VGA_B .dout_a (dout_a), .dout_g (dout_g), .ra_a (sprite_ra_a[31]), .ra_g(sprite_ra_g[31]), .out_pixel(ou	LANK_ t_pix	n), el[<mark>3</mark>	.base_ 1]));	addr	(spi
<pre>always_ff@(posedge clk) begin //Writing to VRAM</pre>					
<pre>background_b <= 8'h00; end else if (chipselect && write) begin case (writedata[1:0]) 2'b0 : begin //pattern name table</pre>					
we_n<=1; SystemVerilog Tab Width: 8	Ln 11	, Col 4	2 -	- 11	NS



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	<pre>else if (out_pixel[24]!=4'b0) final_out_pixel=out_pixel[24]; else if (out_pixel[25]!=4'b0) final_out_pixel=out_pixel[25]; else if (out_pixel[26]!=4'b0) final_out_pixel=out_pixel[26]; else if (out_pixel[27]!=4'b0) final_out_pixel=out_pixel[27]; else if (out_pixel[28]!=4'b0) final_out_pixel=out_pixel[28]; else if (out_pixel[30]!=4'b0) final_out_pixel=out_pixel[29]; else if (out_pixel[30]!=4'b0) final_out_pixel=out_pixel[30]; else if (out_pixel[31]!=4'b0) final_out_pixel=out_pixel[31]; else if (out_pixel[32]!=4'b0) final_out_pixel=out_pixel[32]; //pattern has lowest pixe' else if inal_out_pixel=out_pixel[32]; //pattern has lowest pixe'</pre>	l prior:	ity			
end						
always	<pre>comb begin //VRAM read multiplexer //multiplex sprite attribute table reads if ((hcount>=h_start[0]) && (hcount<h_start[1])) begin<br="">ra a=sprite ra a[0];</h_start[1]))></pre>					
	<pre>ra_g=sprite_ra_g[0]; end else if ((hcount>=h_start[1]) && (hcount<h_start[2])) begin<br="">ra_a=sprite_ra_a[1];</h_start[2]))></pre>					
	<pre>ra_g=sprite_ra_g[1]; end else if ((hcount>=h_start[2]) && (hcount<h_start[3])) begin<br="">ra_a=sprite_ra_a[2]; ra_n=sprite_ra_a[2];</h_start[3]))></pre>					
	<pre>end else if (hcount>=h_start[3]) && (hcount<h_start[4])) begin<br="">ra_a=sprite_ra_a[3]; ra_q=sprite_ra_a[3];</h_start[4]))></pre>					
	<pre>end else if ((hcount>=h_start[4]) && (hcount<h_start[5])) begin="" ra_a="sprite_ra_a[4];" ra_g="sprite_ra_g[4];</pre"></h_start[5]))></pre>					
	<pre>end else if ((hcount>=h_start[5]) && (hcount<h_start[6])) ((hcount="h" (hcount="h" <="" [7]))="" begin="" ff="" herein="" pre="" ra_a="sprite_ra_a[5];" ra_g="sprite_ra_g[5];" ra_if="" test="" test)[6])=""></h_start[6]))></pre>					
	<pre>end else if ((hcount>=h_start[7]) && (hcount<h_start[7])) ((hcount="" begin="" else="" end="" if="" ra_a="sprite_ra_a[6];" ra_g="sprite_ra_g[6];">=h_start[7]) && (hcount<h_start[8])) begin<="" pre=""></h_start[8]))></h_start[7]))></pre>					
	<pre>ra_a=sprite_ra_a[7]; ra_g=sprite_ra_g[7]; end else if ((hcount>=h_start[8]) && (hcount<h_start[9])) begin<="" pre=""></h_start[9]))></pre>					
	<pre>ra_a=sprite_ra_a[8]; ra_g=sprite_ra_g[8]; end else if ((hcount>=h_start[9]) && (hcount<h_start[10])) begin<="" pre=""></h_start[10]))></pre>					
	<pre>ra_a=sprite_ra_a[9]; ra_g=sprite_ra_g[9]; end else if ((hcount>=h_start[10]) && (hcount<h_start[11])) begin="" pre="" ra_arsorite_ra_ation;<=""></h_start[11]))></pre>					
	<pre>ra_=sprite_ra_g[10]; end else if ((hcount>=h_start[11]) && (hcount<h_start[12])) begin<br="">ra a=sprite ra a[11];</h_start[12]))></pre>					
	<pre>ra_g=sprite_ra_g[11]; end else if ((hcount>=h_start[12]) && (hcount<h_start[13])) begin<br="">ra_a=sprite_ra_a[12];</h_start[13]))></pre>					
	<pre>ra g=sprite_ra_g[12]; end else if ((hcount>=h_start[13]) && (hcount<h_start[14])) begin<br="">ra_a=sprite_ra_e[13]; ra_a=sprite_ra_e[13];</h_start[14]))></pre>					
	<pre>ra_g=sprite_ra_g[13]; end else if ((hcount>h_start[14]) && (hcount<h_start[15])) begin<br="">ra_a=sprite_ra_a[14]; ra_g=sprite_ra_a[14];</h_start[15]))></pre>					
	<pre>end else if (hcount>=h_start[15]) && (hcount<h_start[16])) begin="" ra_a="sprite_ra_a[15];" ra_g="sprite_ra_g[15];</pre"></h_start[16]))></pre>					
	<pre>end else if ((hcount>=h_start[16]) && (hcount<h_start[17])) begin<="" td=""><td></td><td></td><td></td><td></td><td></td></h_start[17]))></pre>					
	<pre>end else if ((hcount>=h_start[17]) && (hcount<h_start[18])) begin<br="">ra_a=sprite_ra_a[17]; ra_g=sprite_ra_g[17];</h_start[18]))></pre>					
	<pre>end else if ((hcount>=h_start[18]) && (hcount<h_start[19])) a="sprite_ra_[[18];" begin="" pre="" ra="" ra_mssnite_ra_0[18];<=""></h_start[19]))></pre>					
	SystemVerilog Tab Width: 8	Ln .	11, Col 4	12 -	11	NS

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ra_a=sprite_ra_a[21];					
ra_g=sprite_ra_g[21]; end else if ((hcount>=h start[22]) && (hcount <h b<="" start[23]))="" td=""><td>egin</td><td></td><td></td><td></td><td></td></h>	egin				
ra_a=sprite_ra_a[22];					
end else if ((hcount>=h_start[23]) && (hcount <h_start[24])) b<="" td=""><td>egin</td><td></td><td></td><td></td><td>-</td></h_start[24]))>	egin				-
ra_a=sprite_ra_a[23]; ra g=sprite ra g[23];					-
<pre>end else if ((hcount>=h_start[24]) && (hcount<h_start[25])) a="sprite" a[24];<="" b="" pre="" ra=""></h_start[25]))></pre>	egin				-
ra_g=sprite_ra_g[24];					1
<pre>end else if ((hcount>=h_start[25]) && (hcount<h_start[26])) b<="" td=""><td>egin</td><td></td><td></td><td></td><td></td></h_start[26]))></pre>	egin				
ra_g=sprite_ra_g[25]; end else if ((hcount>=h start[26]) && (hcount <h b<="" start[27]))="" td=""><td>eain</td><td></td><td></td><td></td><td></td></h>	eain				
ra_a=sprite_ra_a[26];					
end else if ((hcount>=h_start[27]) && (hcount <h_start[28])) b<="" td=""><td>egin</td><td></td><td></td><td></td><td></td></h_start[28]))>	egin				
ra_a=sprite_ra_a[27]; ra_g=sprite_ra_g[27];					
<pre>end else if ((hcount>=h_start[28]) && (hcount<h_start[29])) a="sprite" a[28];<="" b="" pre="" ra=""></h_start[29]))></pre>	egin				1
ra_g=sprite_ra_g[28];					
end else if ((ncount>=n_start[29]) && (ncount <n_start[30])) b<br="">ra_a=sprite_ra_a[29];</n_start[30]))>	egin				1
ra_g=sprite_ra_g[29]; end else if ((hcount>=h start[30]) که (hcount <h b<="" start[31]))="" td=""><td>eain</td><td></td><td></td><td></td><td>1</td></h>	eain				1
ra_a=sprite_ra_a[30];	- 5				1
end else if (hcount>=h_start[31]) begin					1
ra_a=sprite_ra_a[31]; ra_g=sprite_ra_g[31];					
end else begin //below should never run here					
ra_g=11'b0;					
end					
endmodule					ĺ
<pre>module sprite prep (input logic clk, reset,</pre>					
input logic [10:0] h_start,					
input logic [9:0] vcount,					
input logic VGA_BLANK_n, input logic [31:0] base_addr, //base address in sprite attr table					
input logic [7:0] dout_a, input logic [7:0] dout q,					1
output logic [31:0] ra_a,					
output logic [3:0] out_pixel);					1
<pre>logic [8:0] down_counter; //8 bit wide down counter</pre>					
<pre>logic [63:0] shift_reg; //64 bit wide shift register logic [7:0] shift pos: //position in shift reg to read pixel value fr</pre>	om				
<pre>logic [10:0] sprite offset; //which row of a given sprite to display logic [63:0] display pixel:// determines whether sprite or background</pre>	nivel is shown				
logic [7:0] shift_reg_shift; //bit position in shift reg to write to	(0-63, steps of 8)				-
assign out_pixet=display_pixet[3:0];					1
<pre>enum {IDLE, READ_VERT_POS,READ_VERT_POS_WAIT, READ_VERT_POS_WAIT2, RE READ_SPRITE_ADDR, READ_SPRITE_ADDR_WAIT, READ_SPRITE_PIXELS_BASE, REA</pre>	AD_HORT_POS, READ_HORT_POS_W D_SPRITE_PIXELS_BASE_WAIT,	AIT,			
LOAD_SHIFT_REG, LOAD_SHIFT_REG_WAIT, SPRITES_LOADED, COUNT_DOWN, PREP. state, state_next;	ARE_PIXELS }				
always ff @(nosedge clk) begin					
state=state_next;					
<pre>if (reset) begin state<=IDLE;</pre>					
ra_g<=0; ra a<=0;					
2	SystemVerilog 👻 Tab Width: 8 👻	Ln 11, Col 4	42 👻	-	INS

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                                                                                                                                                                                                                                            ×
               enum {IDLE, READ_VERT_POS,READ_VERT_POS_WAIT, READ_VERT_POS_WAIT2, READ_HORT_POS, READ_HORT_POS_WAIT,
               READ SPRITE ADDR, READ SPRITE ADDR WAIT, READ SPRITE PIXELS BASE, READ SPRITE PIXELS BASE WAIT, LOAD SHIFT REG, LOAD SHIFT REG WAIT, SPRITES LOADED, COUNT DOWN, PREPARE PIXELS }
               state, state next;
               always_ff @(posedge clk) begin
                              state<=state next:
                              if (reset) begin
                                             state<=IDLE;</pre>
                                             ra q<=0;
                                             ra_a<=0;
                              end
                              case (state)
                                             IDLE: begin
                                                             display pixel<=64'b0;
                                                             shift_reg<=0
                                                             Shift_reg<=04'00;
shift_reg_shift=r8'h40; //dec=64 (actual value used is 8 less)
shift_pos<=8'h40; //dec=64 set shift position to start of shift regs (MSB) (actual value used</pre>
is 4 less)
                                             end
                                             READ_VERT_POS: begin
                                                             ra_a<=base_addr; //address of (starting) vertical position of sprite</pre>
                                             end
                                             READ_HORT_POS: begin
                                                             ra_a<=base_addr+32'b1; //address of horizontal position of sprite
sprite_offset<={2'b0, vcount[8:0]-{dout_a, 1'b0}}; //which of 16 rows of sprite to display //</pre>
e.g. vcount=11, v_pos=5 -> 11-5=6th row
                                             end
                                             READ_SPRITE_ADDR: begin //base address need right shift of 3 bits
                                                             ra_a<=base_addr+32'b10; //address of base address of sprite pixels in the generator table //</pre>
test using 0
                                                             down counter<={dout a, 1'b0}; //copy horizontal position into down counter</pre>
                                             end
                                             READ SPRITE PIXELS BASE: begin //!!note: address no longer >> shifted by 3!!
                                                             ra_g<={dout_a[3:0], 7'b0} + (sprite_offset<<3); //read left-most 8 pixels in gen table, 8x</pre>
offset since 8 table rows needed per pixel line
                                             end
                                             LOAD_SHIFT_REG: begin
                                                             shift_reg<= ({56'b0, dout_g}<<(shift_reg_shift-8'h8)) | shift_reg; //store left-most 8 pixels</pre>
of sprite line
                                                             shift_reg_shift<=shift_reg_shift-8'h8; //minus 8
ra_g<=ra_g+1; //increment gen table address by one to read upcoming pixels</pre>
                                              end
                                             COUNT_DOWN: begin
                                                             //only down count every 2 hcounts
                                                             if (down_counter>9'b0 && VGA_BLANK_n && !hcount[0]) down_counter<=down_counter-1;</pre>
                                             end
                                             PREPARE PIXELS: begin
                                                             if (VGA_BLANK_n && !hcount[0]) begin
display_pixel<=(shift_reg>>(shift_pos-8'h4)); //Only 4 LSB of display_pixel matter
shift_pos<=shift_pos-8'h4; //minus 4</pre>
                                                             end
                                             end
                              endcase
               end
               always comb begin
               case (state)
IDLE: state_next = (hcount==h_start) ? READ_VERT_POS: IDLE;
READ_VERT_POS: state_next = READ_VERT_POS_WAIT; //extra cycle for reading vertical position in attr table
READ_VERT_POS_WAIT: state_next = READ_VERT_POS_WAIT2; //ra_a update needs 2 cycles for some reason
READ_VERT_POS_WAIT2: state_next = (kcount[8:0]>={dout_a, 1b0}) && (vcount[8:0]<({dout_a,
1'b0}+8'b10000)))? READ_HORT_POS: IDLE; //check if any part of sprite is showing (don't need last 4 LSB)
READ_HORT_POS: state_next = READ_HORT_POS_WAIT; //extra cycle for mem read
DEAD_HORT_POS_WAIT
                                             READ_HORT_POS: STATE_IEXT = READ_SPRITE_ADDR;
READ_SPRITE_ADDR: state_next = READ_SPRITE_ADDR WAIT; //extra cycle for mem read
READ_SPRITE_ADDR.WAIT: state_next = READ_SPRITE_PIXELS_BASE;
READ_SPRITE_PIXELS_BASE: state_next = READ_SPRITE_PIXELS_BASE WAIT; //extra cycle for mem read
DEAD_CODITE_DIXELS_BASE: state_next = READ_SPRITE_PIXELS_BASE WAIT; //extra cycle for mem read
DEAD_CODITE_DIXELS_BASE: state_next = READ_SPRITE_PIXELS_BASE WAIT; //extra cycle for mem read
DEAD_CODITE_DIXELS_BASE: state_next = READ_SPRITE_PIXELS_BASE WAIT; //extra cycle for mem read
DEAD_CODITE_DIXELS_BASE: state_next = READ_SPRITE_DIXELS_BASE WAIT; //extra cycle for mem read
DEAD_CODITE_DIXELS_BASE: state_next = READ_SPRITE_DIXELS_BASE WAIT; //extra cycle for mem read
DEAD_CODITE_DIXELS_BASE: state_next = READ_SPRITE_DIXELS_BASE WAIT; //extra cycle for mem read
DEAD_CODITE_DIXELS_BASE: state_next = READ_SPRITE_DIXELS_BASE WAIT; //extra cycle for mem read
DEAD_CODITE_DIXELS_BASE: state_next = READ_SPRITE_DIXELS_BASE WAIT; //extra cycle for mem read
                                                                                                                                                 SystemVerilog - Tab Width: 8 -
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```

```
vga_ball.sv
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                                                                                                                                                                                                      ×
             always_comb begin
case (state)
                                       LOAD_SHIFT_REG:state_next= LOAD_SHIFT_REG WAIT;
LOAD_SHIFT_REG_WAIT: state_next= (shift_rEg_shift==8'b0) ? SPRITES_LOADED: LOAD_SHIFT_REG;
                                       //if new vertical line started, begin down counting
SPRITES_LOADED: state_next= (hcount==11'bl11111) ? COUNT_DOWN : SPRITES_LOADED; //start at 127
COUNT_DOWN: state_next= (down_counter==9'b0) ? PREPARE_PIXELS: COUNT_DOWN;

                                       PREPARE_PIXELS: state_next= (shift_pos==8'b0) ? IDLE : PREPARE_PIXELS;
                                       state_next = IDLE;
                   default:
             endcase
      end
endmodule
input logic VGA_BLANK_n,
input logic [7:0] dout n,
             input logic [7:0] dout_g,
             output logic [11:0] ra_n,
output logic [11:0] ra g,
             output logic [3:0] out_pixel);
              logic [2047:0] shift reg; //8*64*4 bit wide shift register
             logic [1207/0] shift_neg, //0/04/4 bit wide shift regits read pixel value from
logic [10:0] shift_pos; //position in shift reg to read pixel value from
logic [2047:0] display_pixel;// determines whether sprite or background pixel is shown
logic [11:0] shift_reg_shift; //bit position in shift reg to write to (0-63, steps of 8)
logic [7:0] tile_total_counter; //counts the total number of tiles that has been loaded into shift reg
logic [7:0] tile_pixel counter; //counts the number of tile pixel rows that has been loaded
             assign out_pixel=display_pixel[3:0];
             parameter [11:0] v_start=12'h0; //vertical position where first pattern begins
parameter [7:0] tiles_per_row=8'd64; //number of tiles per row
parameter [11:0] name_table_addr_mask={6'b11111, 6'b0};
             enum {IDLE, READ_TILE_ADDR_BASE, READ_TILE_ADDR_BASE_WAIT, READ_PATT_PIXELS_BASE, READ_PATT_PIXELS_BASE_WAIT,
LOAD_SHIFT_REG, LOAD_SHIFT_REG_WAIT, READ_TILE_NEXT, READ_TILE_NEXT_WAIT, PATT_LOADED, PREPARE_PIXELS }
             state, state_next;
             always_ff @(posedge clk) begin
                          state<=state next;
                          if (reset) begin
                                       state<=IDLE:
                                       ra_n<=0;
                                       ra_g<=0;
                          end
                          case (state)
                                       IDLE: begin
                                                    tile_total_counter<=8'b0;
tile_pixel_counter<=8'b0;
display_pixel<=2048'b0;</pre>
                                                    shift_reg<=2048'b0;
shift_reg_shift<=12'b1000000000000; //dec=2048 (actual value used is 8 less)
shift_pos<=12'b100000000000; // dec=2048 set shift position to start of shift regs (MSB)</pre>
 (actual value used is 4 less)
                                       end
                                                                                                                             SystemVerilog - Tab Width: 8 -
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```

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                                         shift_reg<=2048'b0;
shift_reg_shift<=12'b100000000000; //dec=2048 (actual value used is 8 less)</pre>
                                         shift_pos<=12'b100000000000; // dec=2048 set shift position to start of shift regs (MSB)</pre>
(actual value used is 4 less)
                               READ TILE ADDR BASE: begin
                                         ra_n<=(({2'b0, vcount}-v_start)<<3) & name table addr_mask; //get address of (starting) tile</pre>
pixel address in name table
                                         pattern row offset<={8'b0, vcount[2:0]-v start[2:0]}: //which of 8 pixel rows to access
                               end
                              READ_PATT_PIXELS_BASE: begin //!!note: address no longer >> shifted by 3!!
ra_g<={dout_n[5:0], 5'b0} + (pattern_row_offset<<2); //read base 8 pixels in gen table,4x</pre>
offset since 4 table rows needed per pixel line
                              end
                              READ PATT PIXELS BASE WAIT: begin //!!note: address no longer >> shifted by 3!!
                                         ra g<=ra g+1;
                               end
                              LOAD_SHIFT_REG: begin //first time: gets ra_g pixels_base stage and not base_wait stage
shift_reg<= ({2040'b0, dout_g}<<(shift_reg_shift-12'h8)) | shift_reg; //store left-most 8</pre>
pixels of sprite line
                                         shift_reg_shift<=shift_reg_shift-12'h8; //minus 8</pre>
                                         ra_g<=ra_g+1; //increment gen table address by one to read upcoming pixels
tile pixel counter<=tile pixel counter+8'b1;</pre>
                               end
                               READ TILE NEXT: begin
                                         ra n<=ra n+1; //increment name table address</pre>
                                         tile_pixel_counter<=8'b0;
tile_total_counter<=tile_total_counter+8'b1;</pre>
                               end
                               PREPARE PIXELS: begin
                                         if (VGA_BLANK_n && !hcount[0]) begin
                                                   display_pixel<(shift_reg>(shift_pos-12'h4)); //Only 4 LSB of display_pixel matter
shift_pos<=shift_pos-12'h4; //minus 4</pre>
                                         end
                              end
                    endcase
          end
          always_comb begin
          case (state)
               IDLE:
                               state_next = ((hcount==11'd1152) && (vcount>=v_start[9:0]) && (vcount<10'd480)) ? READ_TILE_ADDR_BASE:</pre>
IDLE; //start at h=1152 and vcount=0
               READ TILE ADDR BASE:
                                                state next = READ TILE ADDR BASE WAIT; //extra cycle for reading vertical position in
attr table
                               READ_TILE_ADDR_BASE_WAIT: state next = READ_PATT_PIXELS_BASE; //check if true: ra_a update needs 2
cycles for some reason
                              READ_PATT_PIXELS_BASE: state_next = READ_PATT_PIXELS_BASE_WAIT;
READ_PATT_PIXELS_BASE_WAIT: state_next= LOAD_SHIFT_REG;
LOAD_SHIFT_REG: state_next=(tile_pixel_counter==8<sup>1</sup>h3) ? READ_TILE_NEXT: LOAD_SHIFT_REG;
READ_TILE_NEXT: state_next=READ_TILE_NEXT_WAIT;
READ_TILE_NEXT_WAIT: state_next=(tile_total_counter==tiles_per_row)? PATT_LOADED: READ_PATT_PIXELS_BASE;
                              //if new vertical line started, begin down counting
PATT LOADED: state next= (hcount==11'd127) ? PREPARE PIXELS : PATT LOADED;
                               PREPARE_PIXELS: state_next= (shift_pos==12'b0 || vcount>10'd480) ? IDLE : PREPARE_PIXELS;
               default:
                               state next = IDLE:
          endcase
     end
endmodule
input logic clk,
          input logic [31:0] ra, wa, //change later
input logic we,
          input logic [7:0] din,
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               IDLE:
                             state next = ((hcount==11'd1152) && (vcount>=v start[9:0]) && (vcount<10'd480)) ? READ TILE ADDR BASE:</pre>
IDLE; //start at h=1152 and vcount=0
              READ_TILE_ADDR_BASE:
                                               state next = READ TILE ADDR BASE WAIT; //extra cycle for reading vertical position in
attr table
                              READ_TILE_ADDR_BASE_WAIT: state_next = READ_PATT_PIXELS_BASE; //check if true: ra_a update needs 2
cycles for some reason
                              READ_PATT_PIXELS_BASE:
                                                                     state next = READ PATT PIXELS BASE WAIT;
                             READ_PATT_PIXELS_BASE_WAIT: state_next= LoAD_SHIFT_REG;
LOAD_SHIFT_REG: state_next= (tile_pixel_counter==8*h3) ? READ_TILE_NEXT: LOAD_SHIFT_REG;
READ_TILE_NEXT: state_next=READ_TILE_NEXT_WAIT;
                              READ_TILE_NEXT_WAIT: state_next=(tile_total_counter==tiles_per_row)? PATT_LOADED: READ_PATT_PIXELS_BASE;
                             //if new vertical line started, begin down counting
PATT_LOADED: state_next= (hcount==11'd127) ? PREPARE_PIXELS : PATT_LOADED;
PREPARE_PIXELS: state_next= (shift_pos==12'b0 || vcount>10'd480) ? IDLE : PREPARE_PIXELS;
               default:
                              state_next = IDLE;
         endcase
     end
endmodule
module sprite_attr_table( //stores sprite information (x, y, name, color)
          //x and y position has to be a multiple (2x) of hcount/vcount since only 8 bits
input logic clk,
          input logic [31:0] ra, wa, //change later
         input logic we,
input logic [7:0] din,
         output logic [7:0] dout);
         logic[7:0] mem[31:0];
         always_ff @(posedge clk) begin
        if (we) mem[wa] <= din;</pre>
       dout <= mem[ra];</pre>
         end
endmodule
input logic [11:0] ra, wa, //change later
         input logic [11:0] fa, wa
input logic we,
input logic [7:0] din,
output logic [7:0] dout);
         logic[7:0] mem[4095:0]; //128 8 bit words need per sprite:
          always_ff @(posedge clk) begin
       if (we) mem[wa] <= din;
dout <= mem[ra];</pre>
         end
endmodule
module patt_name_table( //stores 8 bit address of tiles on each row
         input logic clk,
input logic [11:0] ra, wa, //12 bit addr
         input logic we,
input logic [7:0] din,
          output logic [7:0] dout);
         logic[7:0] mem[4095:0];
          always_ff @(posedge clk) begin
        if (we) mem[wa] <= din;</pre>
       dout <= mem[ra];</pre>
         end
endmodule
module patt_gen_table( //stores 8x8 patterns
          input logic clk,
         input logic [10:0] ra, wa,
input logic we,
input logic [7:0] din,
          output logic [7:0] dout);
                                                                                               SystemVerilog - Tab Width: 8 -
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```

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vga_ball.sv
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module patt_gen_table( //stores 8x8 patterns
         input logic clk,
input logic [10:0] ra, wa,
         input logic [10:0] fu, wa
input logic we,
input logic [7:0] din,
output logic [7:0] dout);
         logic[7:0] mem[2047:0]; //32 8 bit words need per pattern: 4 table rows (32 bits) per pixel row
       always_ff @(posedge clk) begin
if (we) mem[wa] <= din;
dout <= mem[ra];</pre>
         end
endmodule
always_comb
                   case(color code)
                            d'hl: rgb_val=24'h00ff00; //green
4'h9: rgb_val=24'hffffff; //white text
default: rgb_val=24'hffffff; //if something goes wrong, use white to make it obvious
                   endcase
endmodule
* 640 X 480 VGA timing for a 50 MHz clock: one pixel every other cycle
 * HCOUNT 1599 0
                                  1279
                                             1599 0
          _____
                                   Video
                      Video
 *
 * |SYNC| BP |<-- HACTIVE -->|FP|SYNC| BP |<-- HACTIVE
 *
                  VGA HS
                                    _____i
   1
        - T
 */
   // Parameters for hcount
   parameter HACTIVE
                               = 11'd 1280,
               HFRONT_PORCH = 11'd 32,
HFRONT_PORCH = 11'd 32,
HBACK_PORCH = 11'd 192,
HBACK_PORCH = 11'd 96,
HTOTAL = HACTIVE + HFRONT_PORCH + HSYNC +
                                 HBACK PORCH; // 1600
   // Parameters for vcount
parameter VACTIVE =
                               = 10'd 480,
               VFRONT_PORCH = 10'd 480,
VFRONT_PORCH = 10'd 10,
VSYNC = 10'd 2,
VBACK_PORCH = 10'd 33,
                               = VACTIVE + VFRONT PORCH + VSYNC +
               VTOTAL
                                 VBACK PORCH; // 525
   logic endOfLine;
   always_ff @(posedge clk50 or posedge reset)
      if (reset) hcount <= 0;
else if (endOfLine) hcount <= 0;</pre>
      else
                             hcount <= hcount + 11'd 1:
   assign endOfLine = hcount == HTOTAL - 1;
   logic endOfField;
                                                                                            SystemVerilog - Tab Width: 8 -
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```

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vga_ball.sv
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                             VGA_CLK, VGA_HS, VGA_VS, VGA_BLANK_n, VGA_SYNC_n);
output logic
* 640 X 480 VGA timing for a 50 MHz clock: one pixel every other cycle
* HCOUNT 1599 0
                                        1279
                                                      1599 0
                                        ______Video
                         Video
                 1
* |SYNC| BP |<-- HACTIVE -->|FP|SYNC| BP |<-- HACTIVE
                     VGA_HS
                                            1
         - 1
*/
   // Parameters for hcount
   parameter HACTIVE
                                   = 11'd 1280,
                 HACITVE = 11 0 1280,

HFRONT_PORCH = 11'd 320,

HSYNC = 11'd 192,

HBACK_PORCH = 11'd 96,

HTOTAL = HACTIVE + HFRONT_PORCH + HSYNC +

HBACK_PORCH; // 1600
   // Parameters for vcount
parameter VACTIVE =
                                   = 10'd 480,
                 VACIIVE = 10 d 300,
VFRONT_PORCH = 10'd 10,
VSYNC = 10'd 2,
VBACK_PORCH = 10'd 33,
                 VTOTAL
                                    = VACTIVE + VFRONT_PORCH + VSYNC +
                                      VBACK PORCH; // 525
   logic endOfLine;
   always_ff @(posedge clk50 or posedge reset)
      if (reset) hcount <= 0;
else if (endOfLine) hcount <= 0;</pre>
                                  hcount <= hcount + 11'd 1;</pre>
       else
   assign endOfLine = hcount == HTOTAL - 1;
   logic endOfField;
   always_ff @(posedge clk50 or posedge reset)
      else if (endOfLine)
if (endOfLine)
        if (endOfField) vcount <= 0;
else vcount <= vcount + 10'd 1;</pre>
   assign endOfField = vcount == VTOTAL - 1;
   // Horizontal sync: from 0x520 to 0x5DF (0x57F)
// 101 0010 0000 to 101 1101 1111 (active LOW during 1312-1503) (192 cycles)
assign VGA_HS = !( (hcount[10:8] == 3'b101) & !(hcount[7:5] == 3'b111));
assign VGA_VS = !( vcount[9:1] == (VACTIVE + VFRONT_PORCH) / 2);
   assign VGA_SYNC_n = 1'b0; // For putting sync on the green signal; unused
   // Horizontal active: 0 to 1279 Vertical active: 0 to 479
   // Horizontal active: 0 to 12/9 Vertical active: 0 to 4/9
// 10 0000 0000 1280 01 1110 0000 480
// 110 0011 1111 1599 10 0000 1100 524
assign VGA_BLANK_n = !( hcount[10] & (hcount[9] | hcount[8]) ) &
        !( vcount[9] | (vcount[8:5] == 4'b111) );
   /* VGA CLK is 25 MHz
     * hcount[0]__|___|____
   assign VGA CLK = hcount[0]; // 25 MHz clock: rising edge sensitive
endmodule
                                                                                                           SystemVerilog - Tab Width: 8 -
                                                                                                                                                      Ln 11, Col 42 👻 INS
```

Code Screenshots - Software

map.h





```
#include "sprite.h"
      static uint8_t map[PATTERN_NROW][PATTERN_NCOL];
      void clear_screen()
      {
         for (r = 0; r < PATTERN_NROW; r++)</pre>
11 🔻
         {
           for(c = 0; c < PATTERN_NCOL; c++)</pre>
13 🔻
           Ł
             set_map_at(r, c, PAT_BACKGROUND);
           `}
        }
17
20 🔻
      {
21
         set_local_map_at(35, 0, PAT_L);
        set_local_map_at(35, 1, PAT_I);
set_local_map_at(35, 2, PAT_V);
set_local_map_at(35, 3, PAT_E);
        set_local_map_at(35, 4, PAT_S);
26
27
28
        set_local_map_at(35, 6, PAT_0 + lives);
      `}
      void set_map_at(int r, int c, uint8_t name)
31 🔻
      {
         map[r][c] = name;
         set_pattern_at(r, c, name);
      }
      void set_local_map_at(int r, int c, uint8_t name)
      {
         set_map_at(MAP_ROW_OFFSET + r, MAP_COL_OFFSET + c, name);
      }
```

pattern.h

	#ifndef _PATTERN_H
	#define _PATTERN_H
	<pre>#include <stdint.h></stdint.h></pre>
	HALFTERN RITHAR CITE 22
	#define PATTERN_BITMAP_SIZE 32
	#define PATTERN_BITMAP_NROW 8
	#define PATTERN_BITMAP_NCOL 8
10	#define PATTERN NROW 60
11	#define PATTERN NCOL 64
10	
12	
13	#define pattern_pixel(x) ((x)&0xT)
14	
15	<pre>void load_pattern_bitmaps();</pre>
16	<pre>void set_pattern_bitmap(int i, const uint8_t *pat);</pre>
17	void set pattern at(uint8 t r. uint8 t c. uint8 t name):
18	
10	
19	timedat anim t
20	typeaer enum 1
21	
22	PAT_BACKGROUND = 0,
23	PAT_BORDER_TOP,
24	PAT BORDER BOTTOM.
25	PAT BORDER RIGHT.
26	DAT BODDED LEET
20	PAT_DURDER_EELT,
27	PA1_0,
28	PA1_1,
29	PAT_2,
30	PAT_3,
31	PAT_4,
32	PAT 5.
22	PAT 6
24	
34	PA1_7,
35	PA1_8,
36	PAT_9,
37	PAT_A,
38	PAT_B,
39	PAT C.
40	ΡΑΤ Π
41	PAT E
41	
42	
43	PA1_6,
44	PAT_H,
45	PAT_I,
46	PAT_J,
47	PAT_K,
48	PAT L.
49	ΡΑΤ Μ.
50	PAT N
51	
51	
52	
53	PAI_Q,
54	PAT_R,
55	PAT_S,
56	PAT_T,
57	PAT U,
58	PAT V.
59	PAT W
55	
61	
01	PAI_T,
62	PAI_Z,
63	<pre>} pattern_name_t;</pre>
64	
65	
66	#endif

pattern.c

```
#include "pattern.h'
#include "color.h"
              #include "vga_ball_user.h"
              #include <stdio.h>
              #include <stdlib.h>
              const uint8_t pat_background[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL] = {
                        {Transp, Transp, Transp, Transp, Transp, Transp, Transp, 
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, 
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, 
},
                        {Transp, Transp, Transp, Transp, Transp, Transp, Transp},
                        {Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
18
19
                        {Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp}, 
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
                        {Transp, Transp, Transp, Transp, Transp, Transp, Transp},
21
22
23
24
25
26
              };
              const uint8_t pat_border_top[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL] = {
                        {White, White, White, White, White, White, White},
                        {White, White, White, White, White, White, White, White},
{White, White, White, White, White, White, White, White, White},
{White, White, White, White, White, White, White, White},
27
28
29
30
                        {Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
                        {Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
32
33
34
                        {Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, 
{Transp, Transp, Tra
              };
              const uint8_t pat_border_bottom[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL] = {
                        {Transp, Transp, Transp, Transp, Transp, Transp, Transp}, 
{Transp, Transp, Transp, Transp, Transp, Transp, Transp},
                        {Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, 
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
                        {White, White, White, White, White, White, White}, {White, White, White, White, White, White, White, White},
43
44
                        {White, White, White, White, White, White, White, White},
                        {White, White, White, White, White, White, White},
              }:
              const uint8_t pat_border_right[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL] = {
                        {Transp, Transp, Transp, Transp, White, White, White}, 
{Transp, Transp, Transp, Transp, White, White, White}, 
{Transp, Transp, Transp, Transp, White, White, White},
                        {Transp, Transp, Transp, Transp, White, White, White, White},
                        {Transp, Transp, Transp, Transp, White, White, White, White},
57
58
                        {Transp, Transp, Transp, Transp, White, White, White, White},
                        {Transp, Transp, Transp, Transp, White, White, White, White},
                        {Transp, Transp, Transp, Transp, White, White, White, White},
              }:
```

62	<pre>const uint8_t pat_border_left[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]</pre>
63	(White White White Transp Transp Transp Transp)
04 65	{White, White, White, White, Transp, Transp, Transp, Transp},
66	{White, White, White, White, Transp, Transp, Transp, Transp,
67	{White White White White Transp, Transp, Transp, Transp,
68	(white, white, white, white, hansp, hansp, hansp, hansp, hansp),
	{White, White, White, White, Transp, Transp, Transp, Transp},
70	{White, White, White, White, Transp, Transp, Transp, Transp},
71	{White, White, White, White, Transp, Transp, Transp, Transp},
72	{White, White, White, White, Transp, Transp, Transp, Transp},
73	};
74	ACCENT WATTER A PATTERN RITHAR NERVIL PATTERN RITHAR NERVIL
75 76	CONST UINT8_t PAT_0[PATTERN_BITMAP_NRUW][PATTERN_BITMAP_NCUL]={
70 77	{Iransp, Iransp, Iransp, Iransp, Iransp, Iransp, Iransp, Iransp, Iransp, I
78	{Transp.Transp.Transp.White.White.Transp.Transp}.
79	{Transp, Transp, White, Transp, Transp, White, White, Transp},
80	{Transp,White,White,Transp,Transp,White,White},
81	{Transp,White,White,Transp,Transp,Transp,White,White},
82	{Transp,White,White,Transp,Transp,Transp,White,White},
83	<pre>{Transp,Transp,White,White,Transp,Transp,White,Transp},</pre>
84	<pre>{Transp,Transp,Transp,White,White,White,Transp,Transp},</pre>
85	};
86	
87	<pre>const uint8_t pat_1[PATTERN_BITMAP_NROW] [PATTERN_BITMAP_NCOL]={</pre>
88	{Transp, Transp, Transp, Transp, Transp, Transp, Transp},
89	{Transp, Transp, Transp, White, White, Transp, Transp},
90	{Iransp, Iransp, Iransp, white, white, white, Iransp, Iransp},
91	{Iransp, Iransp, Iransp, Iransp, white, white, Iransp, Iransp},
92	{Transp, Transp, Transp, Transp, White, White, Transp, Transp},
95	Transp, Transp, Transp, Transp, White, White, Transp, Transp},
94	{Transp, Transp, Transp, Transp, White White White White}
96	}:
97	• /
98	<pre>const uint8 t pat 2[PATTERN BITMAP NROW][PATTERN BITMAP NCOL]={</pre>
99	{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
100	{Transp,Transp,White,White,White,White,Transp},
101	<pre>{Transp,White,White,Transp,Transp,White,White},</pre>
102	<pre>{Transp,Transp,Transp,Transp,Transp,White,White,White},</pre>
103	<pre>{Transp,Transp,Transp,White,White,White,Transp},</pre>
104	<pre>{Transp,Transp,White,White,White,Transp,Transp},</pre>
105	<pre>{Transp,White,White,White,Transp,Transp,Transp,Transp},</pre>
106	{Transp,White,White,White,White,White,White},
107	<i>};</i>
108	
109	CONST UINT8_T PAT_3[PATIERN_BIIMAP_NROW][PATIERN_BIIMAP_NCOL]={
110	{ ransp, ransp, ransp, ransp, ransp, ransp, ransp, ransp},
112	<pre>{Transp, Transp, While, While, While, While, While}, /Transp, Transp, Transp, Transp, White White Transp)</pre>
112	{Transp, Transp, Transp, Transp, Transp, White, White, Transp Transp},
114	{Transp, Transp, Transp, Transp, Mite, White, Mite, Transp, Transp},
115	{Transp, Transp, Transp, Transp, Transp, Transp, White, White}
116	{Transp,White,White,Transp,Transp,Transp,White,White}
117	{Transp, Transp, White, White, White, White, White, Transp},
118	}:

ł

```
const uint8_t pat_4[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={
{Transp, Transp, Transp, Transp, White, White, Transp},
{Transp, Transp, Transp, Transp, White, White, White, Transp},
{Transp,Transp,Transp,White,White,White,White,Transp},
{Transp,Transp,White,White,Transp,White,Transp},
{Transp,White,White,Transp,Transp,White,White,Transp},
{Transp,White,White,White,White,White,White,White},
{Transp, Transp, Transp, Transp, White, White, Transp},
{Transp, Transp, Transp, Transp, Transp, White, White, Transp},
}:
const uint8_t pat_5[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
{Transp,White,White,White,White,White,Transp},
{Transp,White,White,Transp,Transp,Transp,Transp,Transp},
{Transp,White,White,White,White,White,Transp},
{Transp, Transp, Transp, Transp, Transp, White, White},
{Transp, Transp, Transp, Transp, Transp, White, White},
{Transp,White,White,Transp,Transp,Transp,White,White},
{Transp, Transp, White, White, White, White, Transp},
1:
const uint8_t pat_6[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
{Transp, Transp, Transp, White, White, White, White, Transp},
{Transp, Transp, White, White, Transp, Transp, Transp, Transp},
{Transp,White,White,Transp,Transp,Transp,Transp,Transp},
{Transp,White,White,White,White,White,Transp},
{Transp,White,White,Transp,Transp,Transp,White,White},
{Transp,White,White,Transp,Transp,Transp,White,White},
{Transp,Transp,White,White,White,White,Transp},
}:
const uint8 t pat_7[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
{Transp,White,White,White,White,White,White,White},
{Transp,White,White,Transp,Transp,White,White},
{Transp, Transp, Transp, Transp, Transp, White, White, Transp},
{Transp, Transp, Transp, Transp, White, White, Transp, Transp},
{Transp,Transp,Transp,White,White,Transp,Transp,Transp},
{Transp, Transp, Transp, White, White, Transp, Transp, Transp},
{Transp, Transp, Transp, White, White, Transp, Transp, Transp},
}:
const uint8 t pat_8[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
{Transp, Transp, White, White, White, White, Transp, Transp},
{Transp, White, White, Transp, Transp, White, Transp},
{Transp,White,White,White,Transp,Transp,White,Transp},
{Transp, Transp, White, White, White, White, Transp, Transp},
{Transp,White,Transp,Transp,White,White,White,White},
{Transp,White,Transp,Transp,Transp,White,White},
{Transp,Transp,White,White,White,White,Transp},
};
const uint8 t pat_9[PATTERN_BITMAP_NROW] [PATTERN_BITMAP_NCOL]={
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
{Transp, Transp, White, White, White, White, Transp},
{Transp,White,White,Transp,Transp,Transp,White,White},
{Transp,White,White,Transp,Transp,Transp,White,White},
{Transp,Transp,White,White,White,White,White,White},
{Transp, Transp, Transp, Transp, Transp, White, White},
{Transp, Transp, Transp, Transp, White, White, Transp},
{Transp, Transp, White, White, White, White, Transp, Transp},
```

```
const uint8_t pat_A[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={
{Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp,Tra
{Transp,Transp,White,White,Transp,White,White,Transp},
{Transp,White,White,Transp,Transp,Transp,White,White},
{Transp,White,White,Transp,Transp,Transp,White,White},
{Transp,White,White,White,White,White,White,White},
{Transp,White,White,Transp,Transp,White,White},
{Transp,White,White,Transp,Transp,White,White},
}:
const uint8_t pat_B[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
{Transp,White,White,White,White,White,Transp},
{Transp,White,White,Transp,Transp,Transp,White,White},
{Transp,White,White,Transp,Transp,Transp,White,White},
{Transp,White,White,White,White,White,Transp},
{Transp,White,White,Transp,Transp,White,White},
{Transp,White,White,Transp,Transp,Transp,White,White},
{Transp,White,White,White,White,White,Transp},
1:
const uint8_t pat_C[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
{Transp, Transp, Transp, White, White, White, White, Transp},
{Transp, Transp, White, White, Transp, Transp, White, White},
{Transp,White,White,Transp,Transp,Transp,Transp,Transp},
{Transp,White,White,Transp,Transp,Transp,Transp},
{Transp,White,White,Transp,Transp,Transp,Transp,Transp},
{Transp,Transp,White,White,Transp,Transp,White,White},
{Transp,Transp,Transp,White,White,White,Transp},
}:
const uint8_t pat_D[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
{Transp,White,White,White,White,Transp,Transp},
{Transp,White,White,Transp,Transp,White,White,Transp},
{Transp,White,White,Transp,Transp,White,White},
{Transp,White,White,Transp,Transp,Transp,White,White},
{Transp,White,White,Transp,Transp,Transp,White,White},
{Transp,White,White,Transp,Transp,White,White,Transp},
{Transp,White,White,White,White,Transp,Transp},
};
const uint8_t pat_E[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
{Transp,Transp,White,White,White,White,White,White},
{Transp,Transp,White,White,Transp,Transp,Transp,Transp},
{Transp,Transp,White,White,Transp,Transp,Transp,Transp},
{Transp,Transp,White,White,White,White,Transp},
{Transp, Transp, White, White, Transp, Transp, Transp, Transp},
{Transp,Transp,White,White,Transp,Transp,Transp,Transp},
{Transp,Transp,White,White,White,White,White},
}:
const uint8_t pat_F[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
{Transp,White,White,White,White,White,White,White},
{Transp,White,White,Transp,Transp,Transp,Transp,Transp},
{Transp,White,White,Transp,Transp,Transp,Transp,Transp},
{Transp,White,White,White,White,White,Transp},
{Transp,White,White,Transp,Transp,Transp,Transp,Transp},
{Transp,White,White,Transp,Transp,Transp,Transp},
{Transp,White,White,Transp,Transp,Transp,Transp,Transp},
}:
```

```
const uint8 t pat_G[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
{Transp, Transp, Transp, White, White, White, White, White},
{Transp,Transp,White,White,Transp,Transp,Transp,Transp},
{Transp,White,White,Transp,Transp,Transp,Transp,,
{Transp,White,White,Transp,Transp,White,White,White},
{Transp,White,White,Transp,Transp,Transp,White,White},
{Transp,Transp,White,White,Transp,Transp,White,White},
{Transp, Transp, Transp, White, White, White, White, White},
}:
const uint8_t pat_H[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
{Transp,White,White,Transp,Transp,Transp,White,White},
{Transp,White,White,Transp,Transp,Transp,White,White},
{Transp,White,White,Transp,Transp,White,White},
{Transp,White,White,White,White,White,White,White},
{Transp,White,White,Transp,Transp,Transp,White,White},
{Transp,White,White,Transp,Transp,Transp,White,White},
{Transp,White,White,Transp,Transp,White,White},
}:
const uint8_t pat_I[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
{Transp, Transp, White, White, White, White, White, White},
{Transp,Transp,Transp,Transp,White,White,Transp,Transp},
{Transp, Transp, Transp, Transp, White, White, Transp, Transp},
{Transp,Transp,Transp,Transp,White,White,Transp,Transp},
{Transp,Transp,Transp,Transp,White,White,Transp,Transp},
{Transp,Transp,Transp,Transp,White,White,Transp,Transp},
{Transp,Transp,White,White,White,White,White,White},
3:
const uint8_t pat_J[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={
{Transp, Transp, Trans
{Transp,Transp,Transp,Transp,Transp,White,White},
{Transp,Transp,Transp,Transp,Transp,White,White},
{Transp, Transp, Transp, Transp, Transp, White, White},
{Transp, Transp, Transp, Transp, Transp, White, White},
{Transp, Transp, Transp, Transp, Transp, White, White},
{Transp,White,White,Transp,Transp,White,White},
{Transp,Transp,White,White,White,White,Transp},
}:
const uint8 t pat K[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
{Transp,White,White,Transp,Transp,White,White},
{Transp,White,White,Transp,Transp,White,White,Transp},
{Transp,White,White,Transp,White,White,Transp,Transp},
{Transp,White,White,White,White,Transp,Transp,Transp},
{Transp,White,White,White,White,Transp,Transp},
{Transp,White,White,Transp,Transp,White,White,Transp},
{Transp,White,White,Transp,Transp,Transp,White,White},
}:
const uint8_t pat_L[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={
{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
{Transp,White,White,Transp,Transp,Transp,Transp,Transp},
{Transp,White,White,Transp,Transp,Transp,Transp,Transp},
{Transp,White,White,Transp,Transp,Transp,Transp,Transp},
{Transp,White,White,Transp,Transp,Transp,Transp,Transp},
{Transp,White,White,Transp,Transp,Transp,Transp,Transp},
{Transp,White,White,Transp,Transp,Transp,Transp},
{Transp,White,White,White,White,White,White},
```

320	<pre>const uint8 t pat M[PATTERN BITMAP NROW][PATTERN BITMAP NCOL]={</pre>
321	Transp Transp Transp Transp Transp Transp Transp}
222	(Transp, Hunsp, Hunsp, Hunsp, Hunsp, Hunsp, Hunsp, Hunsp, J
222	(Transp, White, White, Transp, Transp, Transp, White, White, White,
222	(Transp, while, while, while, it ansp, while, while, while, it, it, it, it, it, it, it, it, it, it
324	{ ransp, white, white, white, white, white, white, white},
325	{ ransp, white, white, white, white, white, white, white},
326	<pre>{Transp,White,White,Transp,White,Transp,White,White},</pre>
327	<pre>{Transp,White,White,Transp,Transp,Transp,White,White},</pre>
328	<pre>{Transp,White,White,Transp,Transp,Transp,White,White},</pre>
329	} ;
330	
331	<pre>const uint8 t pat N[PATTERN BITMAP NROW][PATTERN BITMAP NCOL]={</pre>
332	{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp}.
333	{Transp White White Transp Transp Transp White White}
221	Transp White White White Transp Transp White White)
225	JTransp, White White White White Transp, White White
222	Transp, white, white, white, white, white, white, white,
330	{iransp,white,white,white,white,white,white},
337	{Transp,White,White,Transp,White,White,White,White},
338	{Transp,White,White,Transp,Transp,White,White,White},
339	<pre>{Transp,White,White,Transp,Transp,Transp,White,White},</pre>
340	};
341	
342	<pre>const uint8 t pat O[PATTERN BITMAP NROW][PATTERN BITMAP NCOL]={</pre>
343	{Transn Transn Transn Transn Transn Transn Transn Transn}
211	(Transp, Transp, White White White White Transp)
245	(Transp, Hansp, white, white, white, white, white, the sport
343	(Transp, while, while, Transp, Transp, Transp, while, while, t
346	{ Iransp, white, white, Iransp, Iransp, Iransp, white, white},
347	{Transp,White,White,Transp,Transp,Transp,White,White},
348	{Transp,White,White,Transp,Transp,Transp,White,White},
349	<pre>{Transp,White,White,Transp,Transp,Transp,White,White},</pre>
350	<pre>{Transp,Transp,White,White,White,White,Transp},</pre>
351	};
352	
353	<pre>const uint8 t pat P[PATTERN BITMAP NROW][PATTERN BITMAP NCOL]={</pre>
354	Transn Transn Transn Transn Transn Transn Transn Transn}
255	Transp, Hunsp, Hunsp, Hunsp, Hunsp, Hunsp, Hunsp, Hunsp, J
222	Transp, white, white, white, white, white, white, hite, white, white,
300	Transp, while, while, Transp, Transp, Transp, while, while,
357	(Transp, while, while, Transp, Transp, Transp, while, while,
358	{Transp,White,White,Transp,Transp,White,White},
359	{Transp,White,White,White,White,White,Transp},
360	<pre>{Transp,White,White,Transp,Transp,Transp,Transp,Transp},</pre>
361	<pre>{Transp,White,White,Transp,Transp,Transp,Transp,Transp},</pre>
362	};
363	
364	
365	<pre>const uint8 t pat O[PATTERN BITMAP NROW][PATTERN BITMAP NCOL]={</pre>
366	{Transp. Transp. Transp. Transp. Transp. Transp. Transp. Transp.
367	{Transp, Transp, White White White White Transp}
368	Transp, White White Transp Transp Transp White White}
260	(Transp, White, White, Transp, Transp, Transp, White, White},
209	(Transp, White, White, Transp, Transp, Transp, White, White, S
370	<pre>{iransp,white,white,iransp,iransp,iransp,white,white;</pre>
3/1	{ ransp, white, white, ransp, white, white, white, white},
372	{Transp,White,White,Transp,Transp,White,White,Transp},
373	<pre>{Transp,Transp,White,White,White,Transp,White},</pre>
374	};
375	
376	<pre>const uint8_t pat_R[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={</pre>
377	{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp}.
378	{Transp,White,White,White,White,White,Transp}.
379	{Transp, White, White, Transp, Transp, Transp, White, White}
380	{Transp White White Transp Transp Transp White White}
300	Transp White White Transp Transp White White White
202	(Transp, White, White, White, White, White, White, White, White,
382	{Transp, white, white, white, white, Iransp, Transp},
383	<pre>{iransp,white,white,iransp,white,white,White,Transp},</pre>
384	<pre>{Iransp,White,White,Transp,Transp,White,White,White},</pre>
385	};

387 388 390 391 392 393 394 395 396	<pre>const uint8_t pat_S[PATTERN_BITMAP_NROW] [PATTERN_BITMAP_NCOL]={ {Transp,Transp,Transp,Transp,Transp,Transp,Transp}, {Transp,Transp,White,White,White,White,Transp,Transp}, {Transp,White,White,Transp,Transp,Transp,Transp}, {Transp,White,White,Transp,Transp,Transp,Transp}, {Transp,Transp,Transp,Transp,Transp,Transp}, {Transp,Transp,Transp,Transp,Transp,White,White}, {Transp,White,White,Transp,Transp,Transp,White,White}, {Transp,Transp,Transp,Transp,Transp,White,White}, {Transp,White,White,White,White,White,White}, {Transp,Transp,Transp,Transp,Transp,White,White}, {Transp,Transp,White,White,White,White,White,Transp}, }; </pre>
397 398 399 400 401 402 403 404 404 405 406 407	<pre>const uint8_t pat_T[PATTERN_BITMAP_NROW] [PATTERN_BITMAP_NCOL]={ {Transp,Transp,Transp,Transp,Transp,Transp,Transp}, {Transp,Transp,White,White,White,White,White}, {Transp,Transp,Transp,Transp,White,White,Transp,Transp}, {Transp,Transp,Transp,Transp,White,White,Transp,Transp}, {Transp,Transp,Transp,Transp,White,White,Transp,Transp}, {Transp,Transp,Transp,Transp,White,White,Transp,Transp}, {Transp,Transp,Transp,Transp,White,White,Transp,Transp}, {Transp,Transp,Transp,Transp,White,White,Transp,Transp}, {Transp,Transp,Transp,Transp,White,White,Transp,Transp}, {Transp,Transp,Transp,Transp,White,White,Transp,Transp}, {Transp,Transp,Transp,White,White,Transp,Transp}, {Transp,Transp,Transp,White,White,Transp,Transp}, };</pre>
408 409 410 411 412 413 414 415 416 417 418 419	<pre>const uint8_t pat_U[PATTERN_BITMAP_NROW] [PATTERN_BITMAP_NCOL]={ {Transp,Transp,Transp,Transp,Transp,Transp,Transp}, {Transp,White,White,Transp,Transp,Transp,White,White}, {Transp,White,White,Transp,Transp,Transp,White,White}, {Transp,White,White,Transp,Transp,Transp,White,White}, {Transp,White,White,Transp,Transp,Transp,White,White}, {Transp,White,White,Transp,Transp,Transp,White,White}, {Transp,White,White,Transp,Transp,Transp,White,White}, {Transp,White,White,Transp,Transp,Transp,White,White}, {Transp,White,White,Transp,Transp,Transp,White,White}, {Transp,White,White,White,White,White,White}, {Transp,White,White,White,White,White,White}, {Transp,White,White,White,White,White,White,White}, {Transp,White,White,White,White,White,White,White}, {Transp,White,White,White,White,White,White,White}, {Transp,White,White,White,White,White,White,White}, {Transp,White,White,White,White,White,White,White}, {Transp,White,White,White,White,White,White,White}, {Transp,White,White,White,White,White,White}, {Transp,White,White,White,White,White,White}, {Transp,White,White,White,White,White,White,White}, {Transp,White,White,White,White,White,White,White,White}, {Transp,White,White,White,White,White,White,White,White}, }; </pre>
429 421 422 423 424 425 426 427 428 429	<pre>const uint8_t pat_V[PATTERN_BITMAP_NROW] [PATTERN_BITMAP_NCOL]={ {Transp,Transp,Transp,Transp,Transp,Transp,Transp,, {Transp,White,White,Transp,Transp,Transp,White,White}, {Transp,White,White,Transp,Transp,Transp,White,White}, {Transp,White,White,Transp,Transp,White,White}, {Transp,White,White,White,Transp,White,White}, {Transp,Transp,White,White,White,White,Transp}, {Transp,Transp,Transp,White,White,Transp}, {Transp,Transp,Transp,Transp,Transp}, {Transp,Transp,Transp,Transp,Transp,Transp}, {Transp,Transp,Transp,Transp,White,Transp,Transp}, {Transp,Transp,Transp,Transp,White,Transp,Transp}, }; </pre>
430 431 432 433 434 435 436	<pre>const uint8_t pat_W[PATTERN_BITMAP_NROW][PATTERN_BITMAP_NCOL]={ {Transp,Transp,Transp,Transp,Transp,Transp,Transp,Transp}, {Transp,White,White,Transp,Transp,Transp,White,White}, {Transp,White,White,Transp,White,Transp,White,White}, {Transp,White,White,Transp,White,Transp,White,White},</pre>
437 438 439 440 441 442	<pre>{Transp,White,White,White,White,White,White}, {Transp,White,White,White,White,White,White}, {Transp,White,White,White,Transp,White,White}, {Transp,White,White,Transp,Transp,White,White}, };</pre>
443 444 445 446 447 448 449 450 451 452	<pre>const uint8_t pat_X[PATTERN_BITMAP_NROW] [PATTERN_BITMAP_NCOL]={ {Transp,Transp,Transp,Transp,Transp,Transp,Transp}, {Transp,White,White,Transp,Transp,White,White}, {Transp,White,White,White,Transp,White,White}, {Transp,Transp,White,White,White,White,Transp}, {Transp,Transp,Transp,White,White,White,Transp,Transp}, {Transp,Transp,White,White,White,White,White,Transp}, {Transp,Transp,White,White,Transp,White,White,White}, {Transp,White,White,Transp,White,White,White},White,White}, {Transp,White,White,Transp,White,White}, {Transp,White,White,Transp,Transp,White,White}, {Transp,White,White,Transp,White,White}, {Transp,White,White,Transp,Transp,White,White}, {Transp,White,White,Transp,Transp,White,White}, {Transp,White,White,Transp,Transp,White,White}, {Transp,White,White,Transp,Transp,White,White}, {Transp,White,White,Transp,Transp,White,White}, {Transp,White,White,Transp,White,White}, {Transp,White,White,Transp,Transp,White,White}, {Transp,White,White}, {Transp,White,White}, {Transp,White,White}, {Transp,White,White}, {Transp,White,White}, {Transp,White}, {Transp,White}, {Transp,White}, {Transp,White}, {Transp,White}, {Transp,White}, {Transp,White}, {Transp}, {Transp,White}, {Transp}, {Transp}, {Transp},</pre>
102	



voluseLpartern_at(units_t r, units_t c, units_t name)
{
 fif (r >= PATTERN_NROW)
 {
 fprintf(stderr, "Row %d is too large!\n", r);
 exit(-1);
 }
 if (c >= PATTERN_NCOL)
 {
 fprintf(stderr, "Column %d is too large!\n", c);
 exit(-1);
 }
 vga_ball_arg_t arg;
 arg.table = PATTERN_NAME_TABLE;
 arg.addr = r * PATTERN_NCOL + c;
 arg.data = name;
 vga_ball_write(&arg);
}

color.h

	#ifndef	_COLOR_H
	#define	_COLOR_H
	#define	Transp 0x0
5	#define	Green 0x1
	#define	Blue 0x7
	#define	White 0x9
8	#endif	



1	#ifndef _SPRITE_H
	#define _SPRITE_H
	<pre>#include <stdint.h></stdint.h></pre>
	typedef struct {
	uint8_t i;
	uint16_t y;
	uint16_t x;
10	uint8_t name;
11	<pre>} sprite_attr_t;</pre>
12	
13	<pre>#define SPRITE_BITMAP_SIZE 128</pre>
14	#define SPRITE_BITMAP_NROW 16
15	<pre>#define SPRITE_BITMAP_NCOL 16</pre>
16	#define sprite_pixel(x) ((x)&0xf)
17	
18	<pre>void load_sprite_bitmaps();</pre>
19	<pre>void set_sprite_bitmap(int spriti, const uint8_t *sprite);</pre>
20	<pre>void set_sprite(sprite_attr_t attr);</pre>
21	
22	typedef enum {
23	SPRITE_DEFENDER,
24	SPRITE_BULLET,
25	SPRITE_ENEMY_ONE,
26	SPRITE_EXPLOSION,
27	SPRITE_TRANSPARENT,
28	SPRITE_BOMB,
29	<pre>} sprite_name_t;</pre>
30	
31	#endif

sprite.c	

	<pre>#include "sprite.h'</pre>	н					
	<pre>#include "color.h"</pre>						
	<pre>#include "vga_ball_</pre>	_user.h"					
	#include actdint by						
	#include <stdint.n< td=""><td></td><td></td><td></td><td></td><td></td><td></td></stdint.n<>						
	<pre>#include <stdlib.h;< pre=""></stdlib.h;<></pre>						
	nanotado locacabin						
	<pre>const uint8_t sprid</pre>	te_defender[SPRI	TE_BITMAP_NROW][SP	RITE_BITMAP_NCOL]] = {		
11	{Transp, Transp,	Transp. Transp.	Transp, Transp, T	ransp. Green, Gi	reen, Transp, Transp,	Transp, Transp, Tra	ansp. Transp. Transp}.
	{Transp, Transp,	Transp, Transp,	Transp, Transp, T	ransp, Green, Gi	reen, Transp, Transp,	Transp, Transp, Tra	ansp, Transp, Transp},
	{Transp, Transp,	Transp, Transp,	Transp, Transp, T	ransp, Green, G	reen, Transp, Transp,	Transp, Transp, Tra	ansp, Transp, Transp},
14							
	{Transp, Transp,	Transp, Transp,	Transp, Transp, G	reen, Green, Gre	een, Green, Transp,	Transp, Transp, Tra	nsp, Transp, Transp},
	{Transp, Transp,	Transp, Transp,	Transp, Transp, G	reen, Green, Gre	een, Green, Transp,	Transp, Transp, Tra Transp, Transp, Tra	nsp, Transp, Transp},
	triansp, rransp,	riansp, riansp,	iransp, iransp, di	reen, dreen, dre	een, oreen, rransp,	iralisp, iralisp, iral	iisp, iraiisp, iraiisp,
19	{Transp, Transp,	Green, Green, G	reen, Green, Green,	. Green. Green	. Green. Green. Green	, Green, Green, Tra	nsp, Transp},
	{Transp, Transp,	Green, Green, G	reen, Green, Green	, Green, Green	, Green, Green, Green	, Green, Green, Tra	nsp, Transp},
22	{Green, Green, G	reen, Green, Gre	en, Green, Green, (Green, Green, G	Green, Green, Green,	Green, Green, Green	, Green},
	{Green, Green, G	reen, Green, Gre	en, Green, Green, G	Green, Green, G	Green, Green, Green,	Green, Green, Green	, Green},
	toreen, Green, G	reen, Green, Gre	en, Green, Green, G	Green, Green, G	Green, Green, Green,	Green, Green, Green	, Green},
	{Green, Green, G	reen. Green. Gre	en. Green. Green. (Green. Green. (Green, Green, Green,	Green, Green, Green	. Green}.
	{Green, Green, G	reen, Green, Gre	en, Green, Green, (Green, Green, (Green, Green, Green,	Green, Green, Green	, Green},
	{Green, Green, G	reen, Green, Gre	en, Green, Green, (Green, Green, (Green, Green, Green,	Green, Green, Green	, Green},
	{Green, Green, G	reen, Green, Gre	en, Green, Green, (Green, Green, (Green, Green, Green,	Green, Green, Green	, Green}
31 32	<i>}</i> ;						
33			BITMAP NROW] [SPRI]	TE BITMAD NCOL1 -	r		
	const uint8 t spri	te bullet[SPRITE			= {		
34	const uint8_t spri	te_bullet[SPRITE			= 1		
	<pre>{Transp, Transp,</pre>	te_bullet[SPRITE Transp, Transp,	Transp, Transp, T	ransp, Green, (= 1 Green, Transp, Transp	, Transp, Transp, T	ransp, Transp, Transp},
	<pre>{Transp, Transp, {Transp, Transp, {Transp, Transp,</pre>	te_bullet[SPRITE Transp, Transp, Transp, Transp,	Transp, Transp, Ti Transp, Transp, Ti	ransp, Green, (ransp, Green, (= 1 Green, Transp, Transp Green, Transp, Transp	, Transp, Transp, T , Transp, Transp, T	ransp, Transp, Transp}, ransp, Transp, Transp},
	Const uint8_t sprin {Transp, Transp, {Transp, Transp, (Transp, Transp,	te_bullet[SPRITE Transp, Transp, Transp, Transp,	Transp, Transp, Ti Transp, Transp, Ti	ransp, Green, (ransp, Green, (= 1 Green, Transp, Transp Green, Transp, Transp	, Transp, Transp, T , Transp, Transp, T	ransp, Transp, Transp}, ransp, Transp, Transp},
34 35 36 37 38	<pre>Const uint8_t sprit {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, }</pre>	te_bullet[SPRITE Transp, Transp, Transp, Transp, Transp, Transp,	Transp, Transp, Ti Transp, Transp, Ti Transp, Transp, Ti	ransp, Green, (ransp, Green, (ransp, Green, (= 1 Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp	, Transp, Transp, T , Transp, Transp, T , Transp, Transp, T	ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp},
34 35 36 37 38 39 40	<pre>{Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp,</pre>	te_bullet[SPRITE Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp,	Transp, Transp, Ti Transp, Transp, Ti Transp, Transp, Ti Transp, Transp, Ti	ransp, Green, (ransp, Green, (ransp, Green, (ransp, Green, (= { Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp	, Transp, Transp, T , Transp, Transp, T , Transp, Transp, T , Transp, Transp, T , Transp, Transp,	ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp},
34 35 36 37 38 39 40 41	<pre>{Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp,</pre>	te_bullet[SPRITE Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp,	Transp, Transp, T Transp, Transp, T Transp, Transp, T Transp, Transp, T Transp, Transp, T	ransp, Green, (ransp, Green, (ransp, Green, (ransp, Green, (ransp, Green, (= { Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp Green, Transp, T <u>rans</u> p Green, Transp, T <u>rans</u> p	, Transp, Transp, T , Transp, Transp, T , Transp, Transp, T , Transp, Transp, T , Transp, Tran <u>sp, T</u>	ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp},
34 35 36 37 38 39 40 41 42	<pre>{Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp,</pre>	te_bullet[SPRITE Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp,	Transp, Transp, Tr Transp, Transp, Ti Transp, Transp, Ti Transp, Transp, Ti Transp, Transp, Ti Transp, Transp, Ti	ransp, Green, (ransp, Green, (ransp, Green, (ransp, Green, (ransp, Green, (ransp, Green, (= { Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp Green, Transp, T <u>rans</u> p	, Transp, Transp, T , Transp, Trans <u>p, T</u>	ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp <u>}</u> ,
34 35 36 37 38 39 40 41 42 43	<pre>{Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp,</pre>	te_bullet[SPRITE Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp,	Transp, Transp, Tı Transp, Transp, Tı Transp, Transp, Tı Transp, Transp, Tı Transp, Transp, Tı Transp, Transp, Tı	ransp, Green, (ransp, Green, (ransp, Green, (ransp, Green, (ransp, Green, (ransp, Green, (= { Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp	, Transp, Transp, T , Transp, Transp, T	ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp,
34 35 36 37 38 39 40 41 42 43 44	<pre>{Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp,</pre>	te_bullet[SPRITE Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp,	Transp, Transp, Tı Transp, Transp, Tı Transp, Transp, Tı Transp, Transp, Tı Transp, Transp, Tı Transp, Transp, Tı Transp, Transp, Tı	ransp, Green, (ransp, Green, (ransp, Green, (ransp, Green, (ransp, Green, (ransp, Green, (ransp, Green, (= { Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp	, Transp, Transp, T , Transp, Transp, T	ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp}, ransp, Transp, Transp},
34 35 36 37 38 39 40 41 42 43 44 45 46	<pre>{Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp, {Transp, Transp,</pre>	te_bullet[SPRITE Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp,	Transp, Transp, Tr Transp, Transp, Tr Transp, Transp, Tr Transp, Transp, Tr Transp, Transp, Tr Transp, Transp, Tr Transp, Transp, Tr	ransp, Green, (ransp, Green, (= { Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp	, Transp, Transp, T , Transp, Transp, T	ransp, Transp, Transp}, ransp, Transp, Transp},
34 35 36 37 38 39 40 41 42 43 44 45 46 47	Const dint8_t sprin {Transp, Transp, {Transp, Transp,	te_bullet[SPRITE Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp,	Transp, Transp, Ti Transp, Transp, T	ransp, Green, (ransp, Green, (= { Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp Green, Transp, Transp	, Transp, Transp, T , Transp, Transp, T	ransp, Transp, Transp}, ransp, Transp, Transp},
34 35 36 37 38 39 40 41 42 43 44 45 46 45 46 47 48	<pre>const dint8_t spri {Transp, Transp, }</pre>	te_bullet[SPRITE Transp, Transp, Transp, Transp,	Transp, Transp, Tr Transp, Transp, Transp, Tr	ransp, Green, (ransp, Green, (= { Green, Transp, Transp Green, Transp, Transp	, Transp, Transp, T , Transp, Transp, T	ransp, Transp, Transp}, ransp, Transp, Transp},
34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49	<pre>{Transp, Transp, {Transp, Transp,</pre>	te_bullet[SPRITE Transp,	Transp, Transp, Tr Transp, Transp, Tr	ransp, Green, (ransp, Green, (= { Green, Transp, Transp Green, Transp, Transp	, Transp, Transp, T , Transp, Transp, T	ransp, Transp, Transp}, ransp, Transp, Transp},
34 35 36 37 38 39 40 41 42 43 44 45 447 48 49 50	<pre>{Transp, Transp, {Transp, Transp, Transp, {Transp, Transp, Transp, {Transp, Transp, Transp, {Transp, Transp, Transp,</pre>	te_bullet[SPRITE Transp, Transp, Transp, Transp,	Transp, Transp, Tr Transp, Transp, Tr	ransp, Green, (ransp, Green, ()	= { Green, Transp, Transp Green, Transp, Transp	, Transp, Transp, T , Transp, Transp, T	ransp, Transp, Transp}, ransp, Transp, Transp},
34 35 36 37 38 39 40 42 43 44 45 46 47 49 50 51	<pre>Const dint8_t sprif {Transp, Transp, }Transp, Transp, }</pre>	te_bullet[SPRITE Transp, Transp, Transp, Transp,	Transp, Transp, Tr Transp, Transp, Tr	ransp, Green, (ransp, Green, (= { Green, Transp, Transp Green, Transp, Transp	, Transp, Transp, T , Transp, Transp, T	ransp, Transp, Transp}, ransp, Transp, Transp},
34 35 36 37 38 39 40 42 43 44 45 46 47 48 49 50 51 52	<pre>{Transp, Transp, {Transp, Transp, {Transp, {Transp, Transp, {Transp, {Transp,</pre>	te_bullet[SPRITE Transp, Transp, Transp, Transp,	Transp, Transp, Tr Transp, Transp, Tr	ransp, Green, (ransp, Green, (= { Green, Transp, Transp Green, Transp, Transp	 Transp, Transp, T 	ransp, Transp, Transp}, ransp, Transp, Transp},
34 35 35 37 38 340 41 42 43 445 46 47 48 90 551 551 551 551	<pre>const dint8_t sprif {Transp, Transp, }Transp, }</pre>	te_bullet[SPRITE Transp, Transp, Transp, Transp,	Transp, Transp, Tr Transp, Transp, Tr	ransp, Green, (ransp, Green, (= { Green, Transp, Transp Green, Transp, Transp	 Transp, Transp, T 	ransp, Transp, Transp}, ransp, Transp, Transp},
34 35 35 36 37 39 40 41 44 44 44 44 44 44 50 55 23 55 55 55 55 55 55	<pre>Const dint8_t sprif {Transp, Transp, }Transp, }</pre>	te_bullet[SPRITE Transp, Transp, Transp, Transp,	Transp, Transp, Tr Transp, Transp, Tr	ransp, Green, (ransp, Green, ()	F { Green, Transp, Transp Green, Transp, Transp	 Transp, Transp, T 	ransp, Transp, Transp}, ransp, Transp, Transp, ransp, Transp, Transp, ransp, Transp, Transp,
34 35 35 37 33 33 33 33 40 41 43 44 45 46 48 49 55 55 55 55 55 55 55 55 55 55 55	Const dint8_t sprif {Transp, Transp, {Transp, Transp, Transp, {Transp, Transp, Transp, {Transp, Transp, Transp, {Transp, Transp, Transp, {Transp, Transp, Transp, {Transp, {Transp, Transp, {Transp, {Transp, Transp, {Transp, {Transp, {Transp, Transp, {Transp, {Transp, {Transp, Transp, {Transp, {Tra	te_bullet[SPRITE Transp, Transp, Transp, Transp,	Transp, Transp, Tr Transp, Transp, Tr	ransp, Green, (ransp, Green, ()	Transp, Transp Green, Transp, Transp	 Transp, Transp, T 	ransp, Transp, Transp}, ransp, Transp, Transp},
34 335 367 389 401 42 445 447 489 551 553 555 557	<pre>const dint8_t spri {Transp, Transp, }Transp, Transp, }</pre>	te_bullet[SPRITE Transp, Transp, Transp, Transp,	Transp, Transp, Tr Transp, Transp, Transp, Tr	ransp, Green, (ransp, Green, (Tansp, Transp, Transp Green, Transp, Transp	 Transp, Transp, T 	ransp, Transp, Transp}, ransp, Transp, Transp, ransp, Transp, Transp, ransp, Transp, Transp, ransp, Transp, Transp}, ransp, Transp, Transp},
33 333 333 333 333 40 44 44 44 44 44 44 44 44 44 44 45 51 55 55 55 55 55 55 55 55 55	<pre>const dint8_t sprif {Transp, Transp, }} } } </pre>	te_bullet[SPRITE Transp, Transp, Transp, Transp,	Transp, Transp, Tr Transp, Transp, Tr	ransp, Green, (ransp, Green, (E Contemporation of the system of the sys	 Transp, Transp, T 	ransp, Transp, Transp}, ransp, Transp, Transp},

61 c	<pre>onst uint8_t sprite_enemy_one[SPRITE_BITMAP_NROW][SPRITE_BITMAP_N</pre>	ICOL] = {
63 64	{Transp, Transp, Transp, Transp, Transp, Transp, White, White, {Transp, Transp, Transp, Transp, Transp, Transp, White, White,	White, White, Transp, Transp, Transp, Transp, Transp, Transp}, White, White, Transp, Transp, Transp, Transp, Transp, Transp},
66 67	{Transp, Transp, Transp, Transp, White, White, White, White, {Transp, Transp, Transp, Transp, White, White, White, White,	White, White, White, White, Transp, Transp, Transp, Transp}, White, White, White, White, Transp, Transp, Transp, Transp},
68 69 70	{Transp, Transp, White, White, White, White, White, White, {Transp, Transp, White, White, White, White, White, White,	White, White, White, White, White, Transp, Transp}, White, White, White, White, White, Transp, Transp},
71 72 73	{White, White, White, White, Transp, Transp, White, White, {White, White, White, White, Transp, Transp, White, White,	White, White, Transp, Transp, White, White, White, White}, White, White, Transp, Transp, White, White, White, White},
74 75 76	{White, White, White, White, White, White, White, White, {White, White, White, White, White, White, White, White,	White, White, White, White, White, White, White}, White}, White, White, White, White, White, White, White},
77 78 79	{Transp, Transp, Transp, Transp, White, White, Transp, Transp, {Transp, Transp, Transp, Transp, White, White, Transp, Transp,	Transp, Transp, White, White, Transp, Transp, Transp, Transp}, Transp, Transp, White, White, Transp, Transp, Transp, Transp},
80 81 82	{Transp, Transp, White, White, Transp, Transp, Transp, Transp, {Transp, Transp, White, White, Transp, Transp, Transp, Transp,	Transp, Transp, Transp, Transp, White, White, Transp, Transp}, Transp, Transp, Transp, Transp, White, White, Transp, Transp},
83 84 85	{Transp, Transp, Transp, Transp, White, White, Transp, Transp, {Transp, Transp, Transp, Transp, White, White, Transp, Transp,	Transp, Transp, White, White, Transp, Transp, Transp, Transp}, Transp, Transp, White, White, Transp, Transp, Transp, Transp},
86 87 }		
89 c	<pre>onst uint8_t sprite_explosion[SPRITE_BITMAP_NROW][SPRITE_BITMAP_N</pre>	$ICOL] = \underline{I}$
91 92 02	{White, White, Transp, Transp, Transp, Transp, Transp, White, {White, White, Transp, Transp, Transp, Transp, Transp, White,	White, Transp, Transp, Transp, Transp, Transp, White, White}, White, Transp, Transp, Transp, Transp, Transp, White, White},
94 95 96	{Transp, Transp, White, White, Transp, Transp, White, White, {Transp, Transp, White, White, Transp, Transp, White, White,	White, White, Transp, Transp, White, White, Transp, Transp}, White, White, Transp, Transp, White, White, Transp, Transp},
97 98 90	{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, {Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp,	Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp}, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
100 101 102	{White, White, White, White, Transp, Transp, Transp, Transp, {White, White, White, White, Transp, Transp, Transp, Transp,	Transp, Transp, Transp, Transp, White, White, White, White}, Transp, Transp, Transp, Transp, White, White, White, White},
102 103 104	{White, White, Transp, Transp, Transp, Transp, Transp, White, {White, White, Transp, Transp, Transp, Transp, Transp, White,	White, Transp, Transp, Transp, Transp, Transp, White, White}, White, Transp, Transp, Transp, Transp, Transp, White, White},
105 106 107	{Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp, {Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp,	Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp}, Transp, Transp, Transp, Transp, Transp, Transp, Transp, Transp},
109 110 111	{Transp, Transp, White, White, Transp, Transp, White, White, {Transp, Transp, White, White, Transp, Transp, White, White,	White, White, Transp, Transp, White, White, Transp, Transp}, White, White, Transp, Transp, White, White, Transp, Transp},
112 113 114	{White, White, Transp, Transp, Transp, Transp, White, {White, White, Transp, Transp, Transp, Transp, White,	White, Transp, Transp, Transp, Transp, Transp, White, White}, White, Transp, Transp, Transp, Transp, Transp, White, White},
115 } 116	3	

118	const uint	:8_t spr	ite_tran:	sparent[SPRITE_B	TMAP_NR	OW][SPRI	TE_BITMAP_	_NCOL] =	{						
119	{Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
120 121	{Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
122	{Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
123 124	{Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
125	{Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
126 127	{Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
128	{Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
129 130	{Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
131	{Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
132 133	{Iransp,	Iransp,	Iransp,	Iransp,	Iransp,	Iransp,	Iransp,	Iransp,	Iransp,	Iransp,	Iransp,	Iransp,	Iransp,	Iransp,	Iransp,	Iransp},
134	{Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
135 136	{Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
137	{Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
138	{Iransp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
140	{Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
141 142	triansp,	rransp,	rransp,	rransp,	rransp,	rransp,	rransp,	rransp,	rransp,	rransp,	rransp,	rransp,	rransp,	rransp,	rransp,	iransp <i>y</i> ,
142	1.															
144	, , , , , , , , , , , , , , , , , , ,															
145 146	const uint	8_t spr	ite_bomb	[SPRITE_	BITMAP_NF	ROW] [SPR:	ITE_BITM	AP_NCOL] =	= {							
	{Transp	Transp	, Transp	, Transp	, Transp,	, Transp	, Transp	, White,	White,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
148 149	{Transp,	Transp	, Transp	, Transp	, Transp,	, Transp	, Transp	, White,	White,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
150	{Transp,	Transp	, Transp	, Transp	, Transp,	, White,	White,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
151 152	{Transp	Transp	, Transp	, Transp	, Transp,	, White,	White,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
153	{Transp	Transp	, <u>T</u> ransp	, Transp	, Transp,	, Transp	, Transp	, White,	White,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
154 155	{Transp,	Transp	, Transp _	, Transp	, Transp, _	, Transp	, Transp	, White,	White,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
156	{Transp	Transp	, Transp	, Transp	, Transp,	, Transp	, Transp	, Transp,	Transp,	White,	White, T	ransp, T	ransp, T	ransp, T	ransp, T	ransp},
157 158	{Iransp,	Iransp	, Iransp _	, Iransp	, Iransp,	, Iransp	, Iransp	, Iransp,	Iransp,	White,	White, I	ransp, I	ransp, I	ransp, I _	ransp, I	ransp},
159	{Transp,	Transp	, Transp	, Transp	, Transp,	, Transp	, Transp	, White,	White,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
160 161	{Transp,	Transp	, Transp	, Transp	, Transp,	, Transp	, Transp	, White,	White,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
162	{Transp,	Transp	, Transp	, Transp	, Transp,	, White,	White,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
163 164	{Transp,	Transp	, Transp	, Transp	, Transp,	, White,	White,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
165	{Transp	Transp	, Transp	, Transp	, Transp,	, Transp	, Transp	, White,	White,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
166 167	{Transp,	Transp	, Transp	, Transp	, Transp,	, Transp	, Transp	, White,	White,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp,	Transp},
	{Transp	Transp	, Transp	, Transp	, Transp,	, Transp	, Transp	, Transp,	Transp,	White,	White, T	ransp, T	ransp, T	ransp, T	ransp, T	ransp},
169 170	{Transp	Transp	, Transp	, Transp	, Transp,	, Transp	, Transp	, Transp,	Transp,	White,	White, T	ransp, T	ransp, T	ransp, T	ransp, T	ransp},
171	};															

```
const uint8_t *sprites[] = {
                  (uint8_t *)sprite_defender,
(uint8_t *)sprite_bullet,
(uint8_t *)sprite_enemy_one,
                  (uint8_t *)sprite_explosion,
(uint8_t *)sprite_transparent,
(uint8_t *)sprite_bomb,
                 for (int i = 0; i < sizeof(sprites) / sizeof(const uint8_t *); i++)</pre>
                   const uint8_t *pat = sprites[i];
set_sprite_bitmap(i, pat);
          {
             vga_ball_arg_t arg;
             arg.table = SPRITE_GENERATOR_TABLE;
             start = spritei * SPRITE_BITMAP_SIZE;
              for (int i = 0; i < SPRITE_BITMAP_SIZE; i++)</pre>
                arg.addr = start + i;
arg.data = sprite_pixel(pat[2 * i]) << 4 | sprite_pixel(pat[2 * i + 1]);</pre>
208
209
210
211
212
213
214
215
216
217
218
219
220
                vga_ball_write(&arg);
          void set_sprite(sprite_attr_t attr)
          {
             vga_ball_arg_t arg;
             int start;
            start = 4 * attr.i;
arg.table = SPRITE_ATTRIBUTE_TABLE;
            arg.addr = start;
arg.data = (uint8_t)(attr.y / 2);
vga_ball_write(&arg);
            arg.addr = start + 1;
arg.data = (uint8_t)(attr.x / 2);
vga_ball_write(&arg);
             arg.addr = start + 2;
            arg.data = attr.name;
vga_ball_write(&arg);
```

joystick.h

```
#ifndef _JOYSTICK_H
#define _JOYSTICK_H
1
    #define USB_HID_KEYBOARD_PROTOCOL 1
    struct joystick_packet {
      uint8_t reserved0;
      uint8_t reserved1;
      uint8_t reserved2;
      uint8_t dir_x;
uint8_t dir_y;
      uint8_t primary;
      uint8_t secondary;
    };
    //Joystick Modifiers
    typedef uint16_t joystick_button_t;
                                (((joystick_button_t)1) << 0)</pre>
    #define JOYSTICK_LEFT
    #define J0YSTICK_RIGHT
                                (((joystick_button_t)1) << 1)</pre>
                                (((joystick_button_t)1) << 2)</pre>
    #define JOYSTICK_UP
    #define JOYSTICK_DOWN
                                (((joystick_button_t)1) << 3)</pre>
    #define JOYSTICK_A
                                (((joystick_button_t)1) << 4)</pre>
                                (((joystick_button_t)1) << 5)
    #define JOYSTICK_B
     #define JOYSTICK_SELECT (((joystick_button_t)1) << 6)</pre>
    #define J0YSTICK_START
                                (((joystick_button_t)1) << 7)</pre>
    #define JOYSTICK_DEFAULT ((joystick_button_t)0)
    //typedef enum { JOYSTICK_KEY } joystick_button_event_t;
    void joystick_init();
    void joystick_destroy();
    void joystick_set_listener(void (*listener)(joystick_button_t));
```

joystick.c

```
#include "joystick.h"
#include <libusb-1.0/libusb.h>
#include <stdbool.h>
#include <stdlib.h>
#include <unistd.h>
#include <stdio.h>
/* ----- Joystick USB Information ----- */
#define JOYSTICK_ID_VENDOR 0x79
#define JOYSTICK_ID_PRODUCT 0x11
void *joystick_worker(void *arg);
void joystick_generate_events(joystick_button_t next);
void joystick_set_buttons(joystick_button_t buttons);
struct libusb_device_handle *joystick_open(uint8_t *endpoint_address);
joystick_button_t joystick_decode_packet(struct joystick_packet packet);
  pthread_mutex_t mu;
pthread_t tid;
   bool dead;
   joystick_button_t buttons;
   uint8_t endpoint;
   struct libusb_device_handle *joystick_handle;
   // called by joystick_worker()
void (*listener)(joystick_button_t bs);
} joystick_state_t;
static joystick_state_t js;
```

```
int error;
  pthread_mutex_init(&js.mu, NULL);
  pthread_mutex_lock(&js.mu);
 js.dead = false;
//js.listener = NULL;
js.buttons = JOYSTICK_DEFAULT;
  if ((js.joystick_handle = joystick_open(&js.endpoint)) == NULL)
  {
    fprintf( stderr, "Did not find a joystick!\n");
  if ((error = pthread_create(&js.tid, NULL, &joystick_worker, NULL)) != 0)
    fprintf(stderr, "Joystick worker could not be created: %s\n", strerror(error));
 pthread_mutex_unlock(&js.mu);
printf("Joystick initialized \n");
{
  pthread_mutex_lock(&js.mu);
  js.dead = true;
  pthread_mutex_unlock(&js.mu);
  pthread_join(js.tid, NULL);
pthread_mutex_destroy(&js.mu);
  printf("Joystick destroyed\n");
void joystick_set_listener(void (*listener)(joystick_button_t))
{
 pthread_mutex_lock(&js.mu);
js.listener = listener;
pthread_mutex_unlock(&js.mu);
  printf("Set joystick listener\n");
<u>}</u>
```

```
void *joystick_worker(void *arg) {
  struct joystick_morker(roug)
struct joystick_packet packet;
joystick_button_t buttons;
  int transferred;
  while (true) {
   pthread_mutex_lock(&js.mu);
    if (js.dead) {
     pthread_mutex_unlock(&js.mu);
    libusb_interrupt_transfer(js.joystick_handle, js.endpoint, (unsigned char *)&packet,
   buttons = joystick_decode_packet(packet);
    joystick_generate_events(buttons);
   pthread_mutex_unlock(&js.mu);
 printf("Joystick worker exited\n");
return NULL;
void joystick_generate_events(joystick_button_t next) {
  if (js.listener == NULL)
  if (next == J0YSTICK_DEFAULT)
    js.listener(JOYSTICK_DEFAULT);
  if (next == J0YSTICK_LEFT)
   js.listener(JOYSTICK_LEFT);
  if (next == JOYSTICK_RIGHT)
   js.listener(JOYSTICK_RIGHT);
  if (next == J0YSTICK_UP)
   js.listener(JOYSTICK_UP);
  if (next == JOYSTICK_DOWN)
    js.listener(JOYSTICK_DOWN);
  if (next == J0YSTICK_A)
   js.listener(JOYSTICK_A);
  if (next == JOYSTICK_B)
js.listener(JOYSTICK_B);
  if (next == J0YSTICK_SELECT)
  js.listener(JOYSTICK_SELECT);
if (next == JOYSTICK_START)
    js.listener(JOYSTICK_START);
```

```
Joystick Device Handler ----
         struct libusb_device_handle *joystick_open(uint8_t *endpoint_address) {
            libusb_device **devs;
            struct libusb_device_handle *joystick_handle = NULL;
            struct libusb_device_descriptor desc;
            ssize_t num_devs, d;
uint8_t i, k;
            /* Start the library */
if ( libusb_init(NULL) < 0 ) {
    fprintf(stderr, "Error: libusb_init failed\n");</pre>
             /* Enumerate all the attached USB devices */
            inf ( (num_devs = libusb_get_device_list(NULL, &devs)) < 0 ) {
    fprintf(stderr, "Error: libusb_get_device_list failed\n");</pre>
183
184
            /* Look at each device, remembering the first HID device that speaks |\ | the keyboard protocol */
             for (d = 0; d < num_devs; d++) {
               libusb_device *dev = devs[d];
if ( libusb_get_device_descriptor(dev, &desc) < 0 ) {
    fprintf(stderr, "Error: libusb_get_device_descriptor failed\n");
               // Find the joystick vendor and product ID
if (desc.idVendor == JOYSTICK_ID_VENDOR && desc.idProduct == JOYSTICK_ID_PRODUCT)
                   struct libusb_config_descriptor *config;
                   libusb_get_config_descriptor(dev, 0, &config);
             for (i = 0 ; i < config->bNumInterfaces ; i++) {
for ( k = 0 ; k < config->interface[i].num_altsetting ; k++ ) {
    const struct libusb_interface_descriptor *inter = config->interface[i].altsetting + k ;
               int r:
               if ((r = libusb_open(dev, &joystick_handle)) != 0) {
    fprintf(stderr, "Error: libusb_open failed: %d\n", r);
               if (libusb_kernel_driver_active(joystick_handle, i)) {
    libusb_detach_kernel_driver(joystick_handle, i);
                libusb_set_auto_detach_kernel_driver(joystick_handle, i);
               if ((r = libusb_claim_interface(joystick_handle, i)) != 0) {
    fprintf(stderr, "Error: libusb_claim_interface failed: %d\n", r);
                         *endpoint_address = inter->endpoint[0].bEndpointAddress;
               goto found;
                  }
```

```
226 found:
227 libusb_free_device_list(devs, 1);
228 return joystick_handle;
230 }
231 joystick_button_t joystick_decode_packet(struct joystick_packet packet) {
232 joystick_button_t buttons = 0;
233 if (packet.dir_x == 0x11)
234 buttons = JOYSTICK_DEFAULT;
235 if (packet.dir_x == 0x00)
237 buttons = JOYSTICK_LEFT;
238 if (packet.dir_x == 0xff)
240 buttons = JOYSTICK_LEFT;
241 if (packet.dir_y == 0xff)
242 buttons = JOYSTICK_UP;
243 if (packet.dir_y == 0x00)
245 buttons = JOYSTICK_UP;
246 if (packet.dir_y == 0xff)
247 buttons = JOYSTICK_DOWN;
248 if (packet.primary & (1 << 6))
250 buttons = JOYSTICK_S;
251 if (packet.secondary & (1 << 5))
252 if (packet.secondary & (1 << 5))
253 if (packet.secondary & (1 << 4))
254 if (packet.secondary & (1 << 4))
255 buttons = JOYSTICK_SELECT;
256 if (packet.secondary & (1 << 4))
257 buttons = JOYSTICK_SELECT;
259 return buttons;
250 }

251 if (packet.secondary & (1 << 4))
252 buttons = JOYSTICK_SELECT;
253 return buttons;
254 if (packet.secondary & (1 << 4))</pre>
```

gameplay.h

1	#itadof CAMEDIAY H
2	#dofino CAMEDIAY
	#deline _GAMEPLAT_H
	#include "sprite.h"
	#include <stdbool.h></stdbool.h>
	typedef enum {
	STAGE_MENU,
10	STAGE IN GAME
1	STAGE END GAME
12	} name stage t
 २	, game_5 tage_t,
11	typedef enum 1
15	
10	DIR_NUNL,
	DIR_LEFT,
L/	DIR_RIGHT,
18	DIR_UP,
19	DIR_DOWN,
20	} dir_t;
21	
22	typedef struct {
23	dir_t dir0;
24	<pre>sprite_attr_t attr;</pre>
25	<pre>} defender_t;</pre>
26	
27	typedef struct {
28	dir_t dir;
29	sprite attr t attr:
30	int alive:
31	} enemy t:
32	
33	typedef struct {
34	dir t dir:
35	sprite attr t attr:
36	} bomb t
37	,
38	typedef struct {
39	dir t dir:
10	sprite attr t attr:
. e 11	int alive:
12	} hullet t
13	j buccec_c,
14	name stage t get game stage():
15	gume_stuge_t get_gume_stuge())
16	void set defender dir(dir t dir)
17	void set_derender_dir(dir_t dir);
†/ 10	void show with
+0 10	void show_ui(),
+9	weid action news () a
00	void setup_game();
51	
2	void reset_characters();
53	
54	<pre>void press_start_game();</pre>
5	
56	<pre>void move_defender();</pre>
57	
58	<pre>bool defender_move_timer();</pre>
»9	
00	void move_enemy();
)1 	
52	<pre>void move_enemies();</pre>
53	
o4	<pre>bool enemy_move_timer();</pre>

	<pre>void fire_bullet();</pre>
67	
68	<pre>void track_bullet();</pre>
69	
70	<pre>void drop_bomb();</pre>
71	
72	<pre>void track_bomb();</pre>
73	
74	<pre>void check_collision();</pre>
75	
	<pre>void check_bombs();</pre>
78	<pre>int check_enemies();</pre>
/9	ter ter al la la la constante de la constante d
80	<pre>int check_lives();</pre>
81	fort sharely share () -
82	<pre>int cneck_score();</pre>
83	
84	#~~~
80	#endit

gameplay.c

1	#include "gameplay.h"
	#include "map.h"
	#include <limits.h></limits.h>
	#include <ptdio b=""></ptdio>
	<pre>#include <stdlib.h></stdlib.h></pre>
	<pre>#include <sys gueue.h=""></sys></pre>
	<pre>#include <unistd.h></unistd.h></pre>
10	
11	typedef struct {
12	athread mutau to mu
13 14	pinread_mulex_i mu; defender t defender:
15	enemy t aliens[4]:
16	<pre>bullet_t bullets[2];</pre>
17	bomb_t bomb;
18	game_stage_t stage;
19	<pre>} game_state_t;</pre>
20	int MAXBULLEIS = 3;
∠⊥ 22	int dropped = 0:
23	int dropped = 0; int fired = 100;
24	int lives = 3;
25	<pre>int score = 0;</pre>
26	
27	<pre>static game_state_t game;</pre>
20 29	<pre>game stage t get game stage() { return game.stage: }</pre>
30	
31	<pre>void set_defender_dir(dir_t dir)</pre>
32	
33 34	pthread_mutex_lock(&game.mu);
35	game.defender.dir0 = dir;
36	J,
37	<pre>pthread_mutex_unlock(&game.mu);</pre>
38	}
39	unid nature mana()
40 11	vola setup_game()
42	
43	<pre>pthread_mutex_init(&game.mu, NULL);</pre>
44	<pre>pthread_mutex_lock(&game.mu);</pre>
45	
46	game.stage = STAGE_MENU;
4/	reset_characters();
40	<pre>printf("Game is ready!\n"):</pre>
50	
51	<pre>pthread_mutex_unlock(&game.mu);</pre>

```
int identifier = 0;
  lives = 3;
  score = 0;
  fired = 100;
  // Reset defender
  game.defender.dir0 = DIR_NONE;
  game.defender.attr.i = identifier;
  game.defender.attr.y = (MAP_ROW_OFFSET + 15) * 8 + 220;
game.defender.attr.x = (MAP_COL_OFFSET + 12) * 8;
  game.defender.attr.name = SPRITE_DEFENDER;
  set_sprite(game.defender.attr);
  identifier = identifier + 1;
  for (int i=0; i<sizeof(game.aliens)/sizeof(game.aliens[0]); i++) {
  game.aliens[i].dir = DIR_NONE;</pre>
    game.aliens[i].attr.i = identifier;
    game.aliens[i].attr.y = (7 + r) * 8;
game.aliens[i].attr.x = (8 + c) * 8;
    game.aliens[i].attr.name = SPRITE_ENEMY_ONE;
game.aliens[i].alive = 1;
    set_sprite(game.aliens[i].attr);
    // end of col
    if (c >= 38) {
    }
    identifier = identifier + 1;
  }
  for (int i=0; i<sizeof(game.bullets)/sizeof(game.bullets[0]); i++) {</pre>
    game.bullets[i].attr.x = 0;
game.bullets[i].attr.y = 0;
    game.bullets[i].attr.name = SPRITE_TRANSPARENT;
    game.bullets[i].dir = DIR_NONE;
    game.bullets[i].attr.i = identifier;
    game.bullets[i].alive = 0;
    set_sprite(game.bullets[i].attr);
    identifier = identifier + 1;
  game.bomb.dir = DIR_NONE;
  game.bomb.attr.i = identifier;
  game.bomb.attr.x = 0;
  game.bomb.attr.x = 480;
  game.bomb.attr.name = SPRITE_TRANSPARENT;
  set_sprite(game.bomb.attr);
}
void press_start_game()
Ł
  pthread_mutex_lock(&game.mu);
game.stage = STAGE_IN_GAME;
  pthread_mutex_unlock(&game.mu);
3
```

```
void move_defender()
{
   // Check for bombs
check_bombs();
   // Movement
switch (game.defender.dir0)
{
   case DIR_NONE:
   case DIR_LEFT:
      game.defender.attr.x = game.defender.attr.x - 2;
// Check boundaries
if (game.defender.attr.x <= 50) { game.defender.attr.x = 50; }</pre>
   break;
case DIR_RIGHT:
      game.defender.attr.x = game.defender.attr.x + 2;
       if (game.defender.attr.x >= 400) { game.defender.attr.x = 400; }
   set_sprite(game.defender.attr);
//pthread_mutex_unlock(&game.mu);
{
    // pthread_mutex_lock(&game.mu);
    int right_wall = 0;
    int left_wall = 0;
    int chance;
   // Check for bullet collision
check_collision();
// Check for remaining enemies
   // Check for remaining enemies
check_enemies();
// Check wall movement
for (int i=0; i<sizeof(game.aliens)/sizeof(game.aliens[0]); i++) {
    if ((game.aliens[i].alive == 1) && (game.aliens[i].attr.x + 2 >= 400)) {
          // Hit right wall
right_wall = 1;
for (int j=0; j<sizeof(game.aliens)/sizeof(game.aliens[0]); j++) { game.aliens[j].dir = DIR_LEFT; } // All left now</pre>
       else if ((game.aliens[i].alive == 1) && (game.aliens[i].attr.x - 2 <= 50)) {
          // Hit Left wall = 1;
for (int j=0; j<sizeof(game.aliens)/sizeof(game.aliens[0]); j++) { game.aliens[j].dir = DIR_RIGHT; } // ALL right now</pre>
   // Now move all enemies
//pthread_mutex_lock(&game.mu);
for (int i=0; i<sizeof(game.aliens)/sizeof(game.aliens[0]); i++) {</pre>
       // For dropping bomb
if (game.aliens[i].alive == 1) {
         chance = rand() % 100;
if (chance == 1 && dropped == 0) {
    drop_bomb(&game.aliens[i]);
    dropped = 1;
       ì
```

```
if (game.aliens[i].alive == 1 && right_wall == 1) {
    // Move alive aliens down and start left
    game.aliens[i].attr.y = game.aliens[i].attr.y + 8;
    game.aliens[i].attr.x = game.aliens[i].attr.x - 1;
       set_sprite(game.aliens[i].attr);
    }
    else if (game.aliens[i].alive == 1 && left_wall == 1) {
       game.aliens[i].attr.x = game.aliens[i].attr.x + 1;
game.aliens[i].attr.y = game.aliens[i].attr.y + 8;
       set_sprite(game.aliens[i].attr);
    }
    else if (game.aliens[i].alive == 1) {
       // move aliens across based on direction
switch(game.aliens[i].dir) {
         case DIR_NONE:
            // Starting case - move right
game.aliens[i].attr.x = game.aliens[i].attr.x + 1;
            break;
         case DIR_LEFT:
            game.aliens[i].attr.x = game.aliens[i].attr.x - 1;
            break;
         case DIR_RIGHT:
            game.aliens[i].attr.x = game.aliens[i].attr.x + 1;
       }
       set_sprite(game.aliens[i].attr);
    }
  //pthread_mutex_unlock(&game.mu);
  right_wall = 0;
  left_wall = 0;
  //pthread_mutex_unlock(&game.mu);
}
void fire_bullet() {
  //pthread_mutex_lock(&game.mu);
// Can make new bullet
     for (int i=0; i<sizeof(game.bullets)/sizeof(game.bullets[0]); i++) {</pre>
       if (game.bullets[i].alive == 0 && fired > 100) {
         game.bullets[i].attr.name = SPRITE_BULLET;
          game.bullets[i].dir = DIR_UP;
          game.bullets[i].attr.x = game.defender.attr.x;
          game.bullets[i].attr.y = game.defender.attr.y - 24;
          game.bullets[i].alive = 1
          set_sprite(game.bullets[i].attr);
          fired = 0;
       fired = fired + 1;
    3
  //pthread_mutex_unlock(&game.mu);
`}
```

```
void track_bullet() {
   for (int i=0; i<sizeof(game.bullets)/sizeof(game.bullets[0]); i++) {</pre>
     if (game.bullets[i].alive == 1) {
  game.bullets[i].attr.x = game.bullets[i].attr.x;
  game.bullets[i].attr.y = game.bullets[i].attr.y - 4;
  set_sprite(game.bullets[i].attr);
     if (game.bullets[i].attr.y < 25) {
   game.bullets[i].attr.name = SPRITE_TRANSPARENT;</pre>
        game.bullets[i].attr.x = 0;
game.bullets[i].attr.y = 0;
game.bullets[i].alive = 0;
        set_sprite(game.bullets[i].attr);
void drop_bomb(enemy_t *enemy) {
     game.bomb.attr.name = SPRITE_BOMB;
     game.bomb.dir = DIR_DOWN;
     game.bomb.attr.x = enemy->attr.x;
game.bomb.attr.y = enemy->attr.y + 8;
      set_sprite(game.bomb.attr);
void track_bomb() {
  if (dropped == 1) {
     game.bomb.attr.name = SPRITE_BOMB;
     game.bomb.attr.x = game.bomb.attr.x;
game.bomb.attr.y = game.bomb.attr.y + 2;
set_sprite(game.bomb.attr);
   if (game.bomb.attr.y >= 440) {
     game.bomb.attr.name = SPRITE_TRANSPARENT;
      game.bomb.attr.x = 0;
     game.bomb.attr.y = 0;
      set_sprite(game.bomb.attr);
     dropped = 0;
     game.bomb.attr.name = SPRITE_BOMB;
```



```
void check_bombs() {
  int right_defender, left_defender, top_defender, bottom_bomb, right_bomb, left_bomb;
  right_defender = game.defender.attr.x + 16;
  left_defender = game.defender.attr.x - 16;
  top_defender = game.defender.attr.y - 16;
  bottom_bomb = game.bomb.attr.y + 8;
right_bomb = game.bomb.attr.x + 2;
left_bomb = game.bomb.attr.x - 2;
  if ((right_bomb < right_defender && right_bomb > left_defender) &&
     (bottom_bomb > top_defender)) {
    game.bomb.attr.name = SPRITE_TRANSPARENT;
    game.bomb.attr.x = 0;
    game.bomb.attr.y = 480;
    game.defender.attr.name = SPRITE_EXPLOSION;
    set_sprite(game.defender.attr);
    usleep(100000);
    set_sprite(game.bomb.attr);
    if (lives > 1) {
       lives = lives - 1;
        game.defender.dir0 = DIR_NONE;
       game.defender.attr.i = 0;
       game.defender.attr.y = (MAP_ROW_OFFSET + 13) * 8 + 220;
game.defender.attr.x = (MAP_COL_OFFSET + 12) * 8;
        game.defender.attr.name = SPRITE_DEFENDER;
        set_sprite(game.defender.attr);
    }
    else { game.stage = STAGE_END_GAME; } // Game over
```

int check_enemies() { int count = 0; for (int i=0; i<sizeof(game.aliens)/sizeof(game.aliens[0]); i++) { if (game.aliens[i].alive == 1) { count = count + 1; } if (count == 0) {
 game.bomb.attr.name = SPRITE_TRANSPARENT; game.bomb.attr.x = 0; game.bomb.attr.y = 480; set_sprite(game.bomb.attr); game.stage = STAGE_END_GAME; patture 1 int check_lives() {
 return lives; } 376 return score; } int check_score() { bool enemy_move_timer() static int counter = 0; counter = (counter + 1) % 100; return counter == 0; bool defender_move_timer() static int counter = 0; counter = (counter + 1) % 100; return counter == 0; }

spaceinvaders.c

	<pre>#include "jovstick.h"</pre>
	#include "vga ball user.h"
	"#include "gamenlav h"
	#include Ucprite b
	#include Sprite.n
	#include "map.n"
	#include "pattern.h"
	<pre>#include <stdio.h></stdio.h></pre>
	<pre>#include <stdlib.h></stdlib.h></pre>
10	<pre>#include <time.h></time.h></pre>
 11	#include <unistd h=""></unistd>
10 1	
12	
13	#include <sys loctl.n=""></sys>
14	#include <sys types.h=""></sys>
15	#include <sys stat.h=""></sys>
16	<pre>#include <fcntl.h></fcntl.h></pre>
17	<pre>#include <string.h></string.h></pre>
18	
10	// GLOBALS
20	
20	void componing lighton of investick by the th
21	void gameptay_tistemer(joystick_button_t b)
22	
23	<pre>switch(get_game_stage()){</pre>
24	case STAGE_MENU:
25	if(b == JOYSTICK_START)
26	-
27	press start game():
 28	}
	hroaki
29	Dieak,
30	
31	
32	case STAGE_IN_GAME:
33	<pre>if (b == JOYSTICK_DEFAULT){</pre>
34	<pre>set defender_dir(DIR_NONE);</pre>
35	<pre>//printf("set defender dir(DIR NONE) called\n"):</pre>
	1
20 27	
20	
38	else if (D == JUTSIICK_LEFT){
	<pre>set_defender_dir(DIR_LEFT);</pre>
40	<pre>//printf("set_defender_dir(DIR_LEFT) called\n");</pre>
41	}
42	
43	else if (b == JOYSTICK RIGHT){
.с лл	set defender dir(DTR RTGHT):
77 15	//nrintf("cot_defender_dir(DIR_DICHT)_colled)").
40	//princi(sec_derender_dif(Dik_kidni) catted(n);
46	
47	}
48	} else if (b == JOYSTICK_A) {
	<pre>} else if (b == JOYSTICK_A) { fire_bullet();</pre>
	<pre>} else if (b == JOYSTICK_A) { fire_bullet(); //printf("FIRE BULLET");</pre>
49 50	<pre>} else if (b == JOYSTICK_A) { fire_bullet(); //printf("FIRE BULLET"); }</pre>
49 50 51	<pre>} else if (b == JOYSTICK_A) { fire_bullet(); //printf("FIRE BULLET"); } break:</pre>
49 50 51 52	<pre>} else if (b == JOYSTICK_A) { fire_bullet(); //printf("FIRE BULLET"); } break;</pre>
49 50 51 52	<pre>} else if (b == JOYSTICK_A) { fire_bullet(); //printf("FIRE BULLET"); } break; case STAGE END GAME:</pre>
49 50 51 52 53	<pre>else if (b == JOYSTICK_A) { fire_bullet(); //printf("FIRE BULLET"); } break; case STAGE_END_GAME: if (b == JOYETTEX CELECT) { }</pre>
49 50 51 52 53 54	<pre>} else if (b == JOYSTICK_A) { fire_bullet(); //printf("FIRE BULLET"); } break; case STAGE_END_GAME: if (b == JOYSTICK_SELECT) { }</pre>
49 50 51 52 53 54 55	<pre>} else if (b == JOYSTICK_A) { fire_bullet(); //printf("FIRE BULLET"); } break; case STAGE_END_GAME: if (b == JOYSTICK_SELECT) { clear_screen(); } }</pre>
49 50 51 52 53 54 55 56	<pre>} else if (b == JOYSTICK_A) { fire_bullet(); //printf("FIRE BULLET"); break; case STAGE_END_GAME: if (b == JOYSTICK_SELECT) { clear_screen(); setup_game(); } }</pre>
49 50 51 52 53 54 55 56 57	<pre>else if (b == JOYSTICK_A) { fire_bullet(); //printf("FIRE BULLET"); break; case STAGE_END_GAME: if (b == JOYSTICK_SELECT) { clear_screen(); setup_game(); } </pre>
49 50 51 52 53 54 55 56 57 58	<pre>} else if (b == JOYSTICK_A) { fire_bullet(); //printf("FIRE BULLET"); } break; case STAGE_END_GAME: if (b == JOYSTICK_SELECT) { clear_screen(); setup_game(); } break; </pre>
49 50 51 52 53 54 55 56 57 58 59	<pre>} else if (b == JOYSTICK_A) { fire_bullet(); //printf("FIRE BULLET"); break; case STAGE_END_GAME: if (b == JOYSTICK_SELECT) { clear_screen(); setup_game(); } break; }</pre>
49 50 51 52 53 55 55 55 55 55 58 59 60	<pre>} else if (b == JOYSTICK_A) { fire_bullet(); //printf("FIRE BULLET"); break; case STAGE_END_GAME: if (b == JOYSTICK_SELECT) { clear_screen(); setup_game(); } break; } </pre>

```
{
              set_map_at(25, 1, PAT_W);
set_map_at(25, 2, PAT_E);
set_map_at(25, 3, PAT_L);
               set_map_at(25, 4, PAT_C);
set_map_at(25, 4, PAT_C);
              set_map_at(25, 5, PAT_0);
set_map_at(25, 6, PAT_0);
set_map_at(25, 6, PAT_M);
set_map_at(25, 7, PAT_E);
            }
            void show_borders() {
                 set_map_at(3, c, PAT_BORDER_TOP);
               }
                  set_map_at(56, c, PAT_BORDER_BOTTOM);
               `}
           `}
           void hide_side() {
              for (int r = 25; r <= 31; r++) {
  for (int c = 50; c <= 55; c++) {</pre>
                     set_map_at(r, c, PAT_BACKGROUND);
                  `}
            }
            void hide_welcome(){
                  for (int c = 0; c <= 8; c++) {
    set_map_at(r, c, PAT_BACKGROUND);</pre>
                   }
            void show_game_over() {
              set_map_at(1, 24, PAT_G);
set_map_at(1, 25, PAT_A);
set_map_at(1, 26, PAT_M);
set_map_at(1, 26, PAT_M);
               set_map_at(1, 27, PAT_E);
               set_map_at(1, 29, PAT_0);
               set_map_at(1, 30, PAT_V);
               set_map_at(1, 31, PAT_E);
set_map_at(1, 32, PAT_R);
            }
111
112
            void show_game_win() {
              set_map_at(1, 25, PAT_Y);
               set_map_at(1, 26, PAT_0);
set_map_at(1, 27, PAT_U);
              set_map_at(1, 29, PAT_W);
set_map_at(1, 30, PAT_I);
set_map_at(1, 31, PAT_N);
118
119
           .
}
```

122	<pre>void show_lives() {</pre>
123	int lives;
124	<pre>set_map_at'(1, 44, PAT_L);</pre>
125	set_map_at(1, 45, PAT_I);
126	set_map_at(1, 46, PAT_V);
127	set_map_at(1, 47, PAT_E);
128	<pre>set_map_at(1, 48, PAT_S);</pre>
129	<pre>lives = check_lives();</pre>
130	if (lives == 3) {
131	<pre>set_map_at(1, 50, PAT_3);</pre>
132	}
133	else if (lives == 2) {
134	<pre>set_map_at(1, 50, PAT_2);</pre>
135	
130	else if (lives == 1) { $ \sum_{i=1}^{n} \sum_{j=1}^{n} \sum_{i=1}^{n} \sum_{i=1}^{n} \sum_{i=1}^{n} \sum_{j=1}^{n} \sum_{i=1}^{n} \sum_{j=1}^{n} \sum_{i=1}^{n} \sum_{i$
137	set_map_at(1, 50, PAT_1);
120	J olco J
140	else $($
140 111	set_map_at(1, 50, PAT_0);
141 1/17	\ \
1/12	<u>1</u>
144	void show score() {
145	int score = 0.
146	int vals[3]:
147	int i = 2
148	set map at(1, 5, PAT S):
149	set map at(1, 6, PAT C):
150	set map at(1, 7, PAT 0);
151	<pre>set_map_at(1, 8, PAT_R);</pre>
152	<pre>set_map_at(1, 9, PAT_E);</pre>
153	<pre>score = check_score();</pre>
154	vals[0] = 0;
155	vals[1] = 0;
156	vals[2] = 0;
157	
158	while(score) {
159	if (i == 2) {
160	<pre>vals[i] = score % 10;</pre>
161	score = score / 10;
162	
163	else 1† (1 == 1) {
164	vals[1] = score % 10;
166	score = score / 10;
167	
168 168	$c_{1} = c_{1}$
160	Vats[1] = Score % 10,
170	, i = i _ 1.
171	}
172	•
173	for (int i=0: i<3: i++) {
174	if (vals[i] == 0) { set map at(1, 11+i, PAT 0); }
175	else if (vals[i] == 1) { set map at(1, 11+i, PAT 1); }
176	<pre>else if (vals[i] == 2) { set_map_at(1, 11+i, PAT_2); }</pre>
177	else if (vals[i] == 3) { set_map_at(1, 11+i, PAT_3); }
178	else if (vals[i] == 4) { set map at(1, 11+i, PAT 4); }
179	else if (vals[i] == 5) { set_map_at(1, 11+i, PAT_5); }
180	else if (vals[i] == 6) { set_map_at(1, 11+i, PAT_6); }
181	else if (vals[i] == 7) { set_map_at(1, 11+i, PAT_7); }
182	else if (vals[i] == 8) { set_map_at(1, 11+i, PAT_8); }
183	else if (vals[i] == 9) { set_map_at(1, 11+i, PAT_9); }
184	else {set_map_at(1, 11+i, PAT_0);
185	}
186	<pre>set_map_at(1, 14, PAT_0);</pre>

189	<pre>void show_reset() {</pre>
190	<pre>set_map_at(7, 18, PAT_P);</pre>
191	<pre>set_map_at(7, 19, PAT_R);</pre>
192	set map at(7, 20, PAT E);
193	<pre>set_map_at(7, 21, PAT_S);</pre>
194	set map at(7, 22, PAT S);
195	
196	set map at(7, 24, PAT S):
197	set map at (7, 25, PAT F):
198	set map_at(7, 26, PAT L);
199	set map at $(7, 27, PAT E)$
200	set map at $(7, 28, PAT C)$
200	set map $at(7, 20, TAT_C)$;
201	set_map_at(7, 29, TAT_T),
202	cot map at (7 21 PAT T)
203	$set_map_at(7, 31, FAT_1);$
204	set_map_at(7, 52, PAT_0);
205	a = 1 $a = 1$ $a = 1$ $a = 1$ $a = 1$
200	$set_map_at(7, 34, PAT_K);$
207	Set_map_at(7, 35, PAT_E);
208	<pre>set_map_at(7, 36, PAI_S); ast map_at(7, 37, PAT_S);</pre>
209	set_map_at(7, 37, PAI_E);
210	<pre>set_map_at(7, 38, PAI_I);</pre>
211	}
212	world chow hold () [
213	
214	c_{1}
215	<pre>static int counter = 0; static int flip = 1;</pre>
210	static int rtip = 1;
21/	$=$ counter = (counter + 1) ≈ 800 :
210	
218	
218 219	<pre>if (counter == 0) flip t= 1;</pre>
218 219 220	if (counter == 0) flip *= -1;
218 219 220 221	<pre>if (counter == 0) flip *= -1; if (flip == 1) {</pre>
218 219 220 221 222	<pre>if (counter == 0) flip *= -1; if (flip == 1) { cot mon of (25 - 51 - BAT - B); </pre>
218 219 220 221 222 223	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_P); </pre>
218 219 220 221 222 223 224 225	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); </pre>
218 219 220 221 222 223 224 225	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 53, PAT_E); </pre>
218 219 220 221 222 223 224 225 226 227	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(25, 54, PAT_S); </pre>
218 219 220 221 222 223 224 225 226 227 228	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(25, 55, PAT_S);</pre>
218 219 220 221 222 223 224 225 226 227 228 228	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(25, 55, PAT_S); set_map_at(27, 51, PAT_S); </pre>
218 219 220 221 222 223 224 225 226 227 228 229 229 220	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(25, 55, PAT_S); set_map_at(27, 51, PAT_S); set_map_at(27, 51, PAT_S); </pre>
218 219 220 221 222 223 224 225 226 227 228 229 230	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(27, 51, PAT_S); set_map_at(27, 52, PAT_T); set_map_at(27, 52, PAT_T); </pre>
218 219 220 221 222 223 224 225 226 227 228 229 230 231 232	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(25, 55, PAT_S); set_map_at(27, 51, PAT_S); set_map_at(27, 52, PAT_T); set_map_at(27, 53, PAT_A); </pre>
218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 232	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 55, PAT_S); set_map_at(25, 55, PAT_S); set_map_at(27, 51, PAT_S); set_map_at(27, 52, PAT_T); set_map_at(27, 53, PAT_R); set_map_at(27, 54, PAT_R); </pre>
218 219 220 221 222 223 224 225 226 227 228 227 228 229 230 231 232 233	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 55, PAT_S); set_map_at(27, 51, PAT_S); set_map_at(27, 52, PAT_T); set_map_at(27, 54, PAT_R); set_map_at(27, 55, PAT_T); </pre>
218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 233 234	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(25, 55, PAT_S); set_map_at(27, 51, PAT_S); set_map_at(27, 53, PAT_A); set_map_at(27, 54, PAT_R); set_map_at(27, 55, PAT_T); </pre>
218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 231 232 233 234 235	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(25, 55, PAT_S); set_map_at(27, 51, PAT_S); set_map_at(27, 52, PAT_T); set_map_at(27, 53, PAT_R); set_map_at(27, 55, PAT_T); set_map_at(27, 55, PAT_T); set_map_at(29, 51, PAT_T); </pre>
218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 232 232 233 234 235 236	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(25, 55, PAT_S); set_map_at(27, 51, PAT_S); set_map_at(27, 52, PAT_T); set_map_at(27, 54, PAT_R); set_map_at(27, 55, PAT_T); set_map_at(29, 51, PAT_T); set_map_at(29, 52, PAT_O); </pre>
218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 233 234 235 236 237 236 237	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(27, 51, PAT_S); set_map_at(27, 52, PAT_T); set_map_at(27, 53, PAT_A); set_map_at(27, 55, PAT_T); set_map_at(27, 55, PAT_T); set_map_at(27, 55, PAT_T); set_map_at(29, 51, PAT_T); set_map_at(29, 51, PAT_C); </pre>
218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 233 234 235 236 237 238 237 238	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(25, 55, PAT_S); set_map_at(27, 51, PAT_S); set_map_at(27, 52, PAT_T); set_map_at(27, 53, PAT_A); set_map_at(27, 55, PAT_T); set_map_at(27, 55, PAT_T); set_map_at(29, 51, PAT_T); set_map_at(29, 51, PAT_T); set_map_at(29, 52, PAT_O); set_map_at(31, 51, PAT_P); </pre>
218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 233 234 235 236 237 238 239 236 237 238 239	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(25, 55, PAT_S); set_map_at(27, 51, PAT_S); set_map_at(27, 52, PAT_T); set_map_at(27, 53, PAT_A); set_map_at(27, 55, PAT_T); set_map_at(27, 55, PAT_T); set_map_at(29, 51, PAT_T); set_map_at(29, 51, PAT_T); set_map_at(31, 51, PAT_P); set_map_at(31, 52, PAT_L); </pre>
218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 233 234 235 236 237 238 239 240 240	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(25, 55, PAT_S); set_map_at(27, 51, PAT_S); set_map_at(27, 52, PAT_T); set_map_at(27, 55, PAT_R); set_map_at(27, 55, PAT_T); set_map_at(29, 51, PAT_T); set_map_at(29, 51, PAT_T); set_map_at(31, 51, PAT_P); set_map_at(31, 52, PAT_L); set_map_at(31, 53, PAT_A); </pre>
218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 231 232 233 234 235 236 237 238 239 240 241	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(25, 55, PAT_S); set_map_at(27, 51, PAT_S); set_map_at(27, 52, PAT_T); set_map_at(27, 54, PAT_R); set_map_at(27, 55, PAT_S); set_map_at(29, 51, PAT_T); set_map_at(29, 51, PAT_T); set_map_at(31, 51, PAT_P); set_map_at(31, 52, PAT_L); set_map_at(31, 53, PAT_A); set_map_at(31, 54, PAT_Y); </pre>
218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 232 233 234 235 236 237 238 239 240 241 242	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(27, 51, PAT_S); set_map_at(27, 52, PAT_T); set_map_at(27, 53, PAT_R); set_map_at(27, 55, PAT_R); set_map_at(27, 55, PAT_R); set_map_at(27, 55, PAT_T); set_map_at(29, 51, PAT_R); set_map_at(29, 51, PAT_T); set_map_at(31, 51, PAT_P); set_map_at(31, 52, PAT_L); set_map_at(31, 53, PAT_A); set_map_at(31, 54, PAT_Y); }</pre>
218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 232 232 233 234 235 236 237 238 239 2240 241 242 242 243	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(27, 51, PAT_S); set_map_at(27, 52, PAT_T); set_map_at(27, 53, PAT_A); set_map_at(27, 54, PAT_R); set_map_at(27, 55, PAT_T); set_map_at(27, 55, PAT_T); set_map_at(27, 55, PAT_T); set_map_at(29, 51, PAT_T); set_map_at(29, 51, PAT_T); set_map_at(29, 52, PAT_O); set_map_at(31, 51, PAT_P); set_map_at(31, 52, PAT_L); set_map_at(31, 54, PAT_Y); } else {</pre>
218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 233 234 235 236 237 238 239 240 241 242 241 242 243 244	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(27, 51, PAT_S); set_map_at(27, 52, PAT_T); set_map_at(27, 53, PAT_A); set_map_at(27, 55, PAT_S); set_map_at(27, 55, PAT_T); set_map_at(27, 55, PAT_T); set_map_at(29, 51, PAT_T); set_map_at(29, 51, PAT_T); set_map_at(31, 51, PAT_P); set_map_at(31, 53, PAT_A); set_map_at(31, 54, PAT_Y); } else { hide_side(); </pre>
218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 233 234 235 236 237 238 239 240 241 242 243 244 245	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(25, 55, PAT_S); set_map_at(27, 51, PAT_S); set_map_at(27, 52, PAT_T); set_map_at(27, 53, PAT_A); set_map_at(27, 55, PAT_T); set_map_at(27, 55, PAT_T); set_map_at(27, 55, PAT_T); set_map_at(29, 51, PAT_T); set_map_at(29, 51, PAT_T); set_map_at(31, 51, PAT_P); set_map_at(31, 51, PAT_P); set_map_at(31, 53, PAT_A); set_map_at(31, 54, PAT_Y); } else { hide_side(); }</pre>
218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 233 234 235 236 237 238 239 240 241 242 243 244 245 244 245 246 247 248 237 238 239 240 241 247 248 249 248 249 249 248 249 249 249 249 249 249 249 249	<pre>if (counter == 0) flip *= -1; if (flip == 1) { set_map_at(25, 51, PAT_P); set_map_at(25, 52, PAT_R); set_map_at(25, 53, PAT_E); set_map_at(25, 54, PAT_S); set_map_at(25, 55, PAT_S); set_map_at(27, 51, PAT_S); set_map_at(27, 52, PAT_T); set_map_at(27, 53, PAT_A); set_map_at(27, 55, PAT_S); set_map_at(29, 51, PAT_T); set_map_at(29, 51, PAT_T); set_map_at(31, 51, PAT_C); set_map_at(31, 51, PAT_L); set_map_at(31, 53, PAT_L); set_map_at(31, 54, PAT_Y); } else { hide_side(); }</pre>

```
int main() {
  int is_clear = 0;
  int is_first_clear = 0;
  clear_screen();
  //setup_map();
  setup_game();
  joystick_set_listener(&gameplay_listener);
  while(1)
  {
   // if (rand() % 4 == 1) { printf("game_stage: %d", get_game_stage()); }
    switch(get_game_stage())
      case STAGE_MENU:
        usleep(1000);
         if (is_first_clear == 0) {
           is_first_clear = 1;
        win = 0;
        show_score();
is_clear = 0;
      case STAGE_IN_GAME:
        usleep(1000);
        //hide_side();
        //hide_welcome();
        if (is_clear == 0){
    clear_screen();
           //create_border();
           is_clear = 1;
        show_lives();
        if(enemy_move_timer())
         {
        }
         if(defender_move_timer())
        break;
      case STAGE_END_GAME:
        usleep(1000);
```

