

# High Frequency Trading acceleration using FPGAs

**Team name:**

**Accelerated**

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High Frequency Trading (HFT) has received a lot of attention over the past years and has become an increasingly important element of financial markets. The term HFT describes a set of techniques within electronic trading of stocks and derivatives, where a large number of orders are injected into the market at sub-millisecond round-trip execution times.

For our project we are considering working on Maxeler's high throughput MPT hardware and utilize their MaxCompilerMPT framework to get market data in through the board, save it to disk and shoot the order data out. Through the project we want to increase the throughput and reduce the latency, which are of critical importance in HFT.

David Lariviere has offered to provide the necessary guidance and support for the project.