

Review for the Midterm

Stephen A. Edwards

Columbia University

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The Midterm

Structure of a Compiler

Scanning

Languages and Regular Expressions

NFAs

Translating REs into NFAs

Building a DFA from an NFA: Subset Construction

Parsing

Resolving Ambiguity

Rightmost and Reverse-Rightmost Derivations

Building the LR(0) Automaton

Building an SLR Parsing Table

Shift/Reduce Parsing

Name, Scope, and Bindings

Activation Records

Static Links for Nested Functions

The Midterm

70 minutes

4–5 problems

Closed book

One double-sided sheet of notes of your own devising

Comprehensive: Anything discussed in class is fair game

Little, if any, programming

Details of O'Caml/C/C++/Java syntax not required

Broad knowledge of languages discussed

Compiling a Simple Program

```
int gcd(int a, int b)
{
    while (a != b) {
        if (a > b) a -= b;
        else b -= a;
    }
    return a;
}
```

What the Compiler Sees

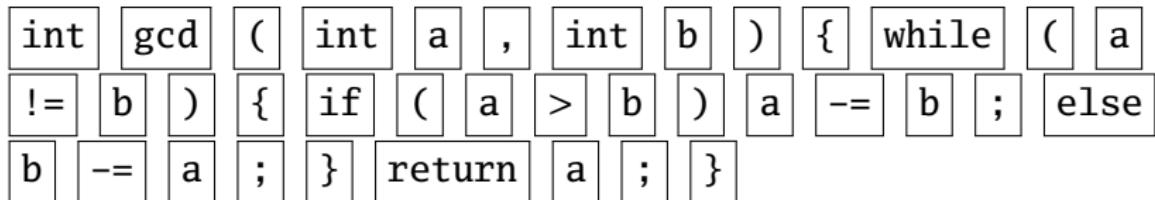
```
int gcd(int a, int b)
{
    while (a != b) {
        if (a > b) a -= b;
        else b -= a;
    }
    return a;
}
```

i n t sp g c d (i n t sp a , sp i
n t sp b) nl { nl sp sp w h i l e sp
(a sp ! = sp b) sp { nl sp sp sp sp i
f sp (a sp > sp b) sp a sp - = sp b
; nl sp sp sp sp e l s e sp b sp - = sp
a ; nl sp sp } nl sp sp r e t u r n sp
a ; nl } nl

Text file is a sequence of characters

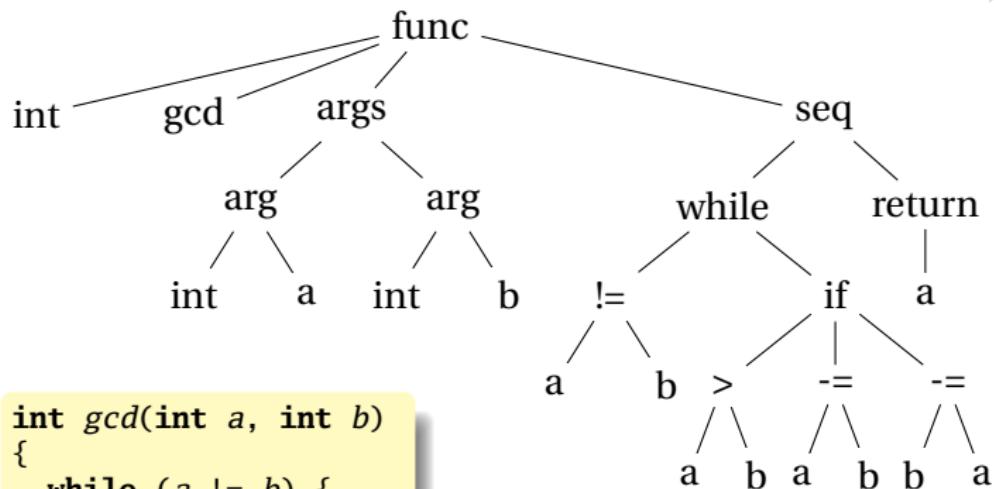
Lexical Analysis Gives Tokens

```
int gcd(int a, int b)
{
    while (a != b) {
        if (a > b) a -= b;
        else b -= a;
    }
    return a;
}
```



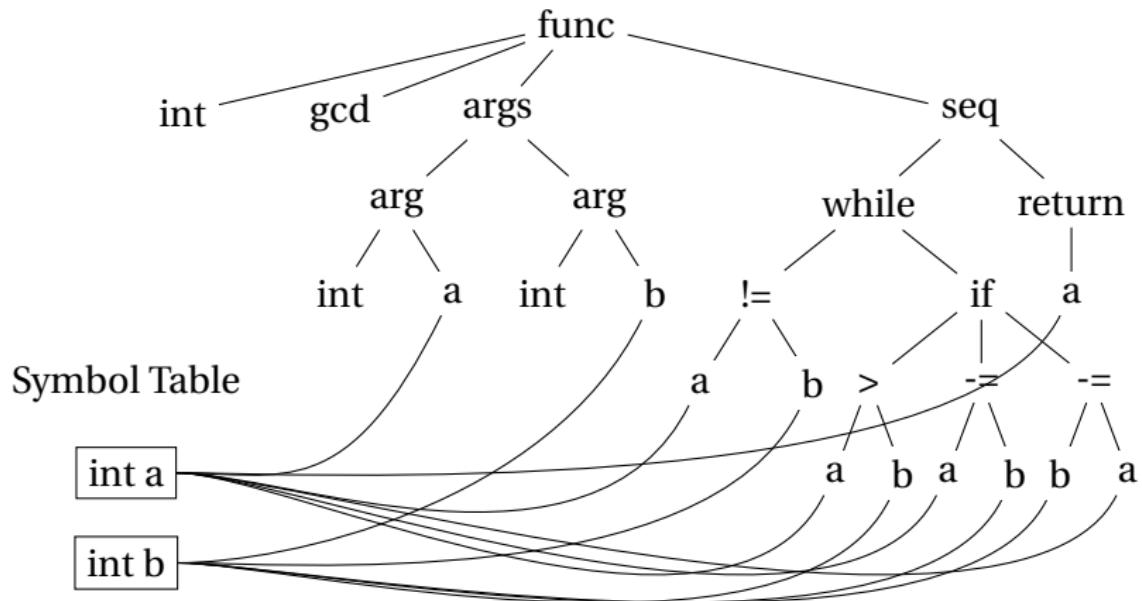
A stream of tokens. Whitespace, comments removed.

Parsing Gives an Abstract Syntax Tree



```
int gcd(int a, int b)
{
    while (a != b) {
        if (a > b) a -= b;
        else b -= a;
    }
    return a;
}
```

Semantic Analysis Resolves Symbols and Checks Types



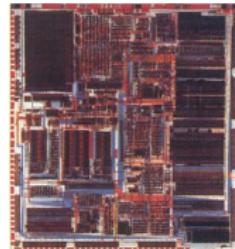
Translation into 3-Address Code

```
L0: sne    $1,  a,  b
     seq    $0,  $1,  0
     btrue $0,  L1      # while (a != b)
     sl     $3,  b,  a
     seq    $2,  $3,  0
     btrue $2,  L4      # if (a < b)
     sub    a,  a,  b # a -= b
     jmp   L5
L4: sub    b,  b,  a # b -= a
L5: jmp   L0
L1: ret    a
```

```
int gcd(int a, int b)
{
    while (a != b) {
        if (a > b) a -= b;
        else b -= a;
    }
    return a;
}
```

Idealized assembly language w/
infinite registers

Generation of 80386 Assembly



```
gcd:  pushl %ebp          # Save BP
      movl %esp,%ebp
      movl 8(%ebp),%eax # Load a from stack
      movl 12(%ebp),%edx # Load b from stack
.L8:  cmpl %edx,%eax
      je   .L3           # while (a != b)
      jle  .L5           # if (a < b)
      subl %edx,%eax    # a -= b
      jmp  .L8
.L5:  subl %eax,%edx    # b -= a
      jmp  .L8
.L3:  leave             # Restore SP, BP
      ret
```

Describing Tokens

Alphabet: A finite set of symbols

Examples: $\{ 0, 1 \}$, $\{ A, B, C, \dots, Z \}$, ASCII, Unicode

String: A finite sequence of symbols from an alphabet

Examples: ϵ (the empty string), Stephen, $\alpha\beta\gamma$

Language: A set of strings over an alphabet

Examples: \emptyset (the empty language), $\{ 1, 11, 111, 1111 \}$, all English words, strings that start with a letter followed by any sequence of letters and digits

Operations on Languages

Let $L = \{ \epsilon, wo \}$, $M = \{ man, men \}$

Concatenation: Strings from one followed by the other

$LM = \{ man, men, woman, women \}$

Union: All strings from each language

$L \cup M = \{ \epsilon, wo, man, men \}$

Kleene Closure: Zero or more concatenations

$M^* = \{ \epsilon \} \cup M \cup MM \cup MMM \dots =$

$\{ \epsilon, man, men, manman, manmen, menman, menmen, manmanman, manmanmen, manmenman, \dots \}$

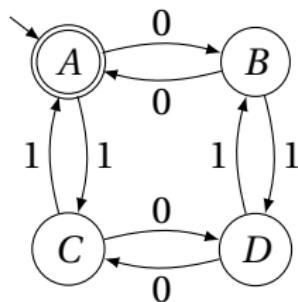
Regular Expressions over an Alphabet Σ

A standard way to express languages for tokens.

1. ϵ is a regular expression that denotes $\{\epsilon\}$
2. If $a \in \Sigma$, a is an RE that denotes $\{a\}$
3. If r and s denote languages $L(r)$ and $L(s)$,
 - ▶ $(r) | (s)$ denotes $L(r) \cup L(s)$
 - ▶ $(r)(s)$ denotes $\{tu : t \in L(r), u \in L(s)\}$
 - ▶ $(r)^*$ denotes $\cup_{i=0}^{\infty} L^i$ ($L^0 = \{\epsilon\}$ and $L^i = LL^{i-1}$)

Nondeterministic Finite Automata

“All strings containing an even number of 0’s and 1’s”



1. Set of states

$$S: \left\{ \textcircled{A}, \textcircled{B}, \textcircled{C}, \textcircled{D} \right\}$$

2. Set of input symbols $\Sigma : \{0, 1\}$

3. Transition function $\sigma : S \times \Sigma_\epsilon \rightarrow 2^S$

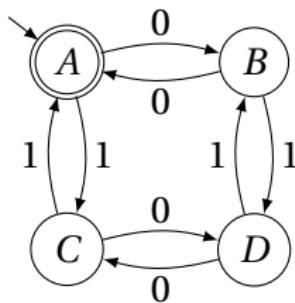
state	ϵ	0	1
A	\emptyset	{B}	{C}
B	\emptyset	{A}	{D}
C	\emptyset	{D}	{A}
D	\emptyset	{C}	{B}

4. Start state $s_0 : \textcircled{A}$

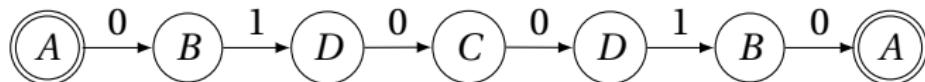
5. Set of accepting states $F : \left\{ \textcircled{A} \right\}$

The Language induced by an NFA

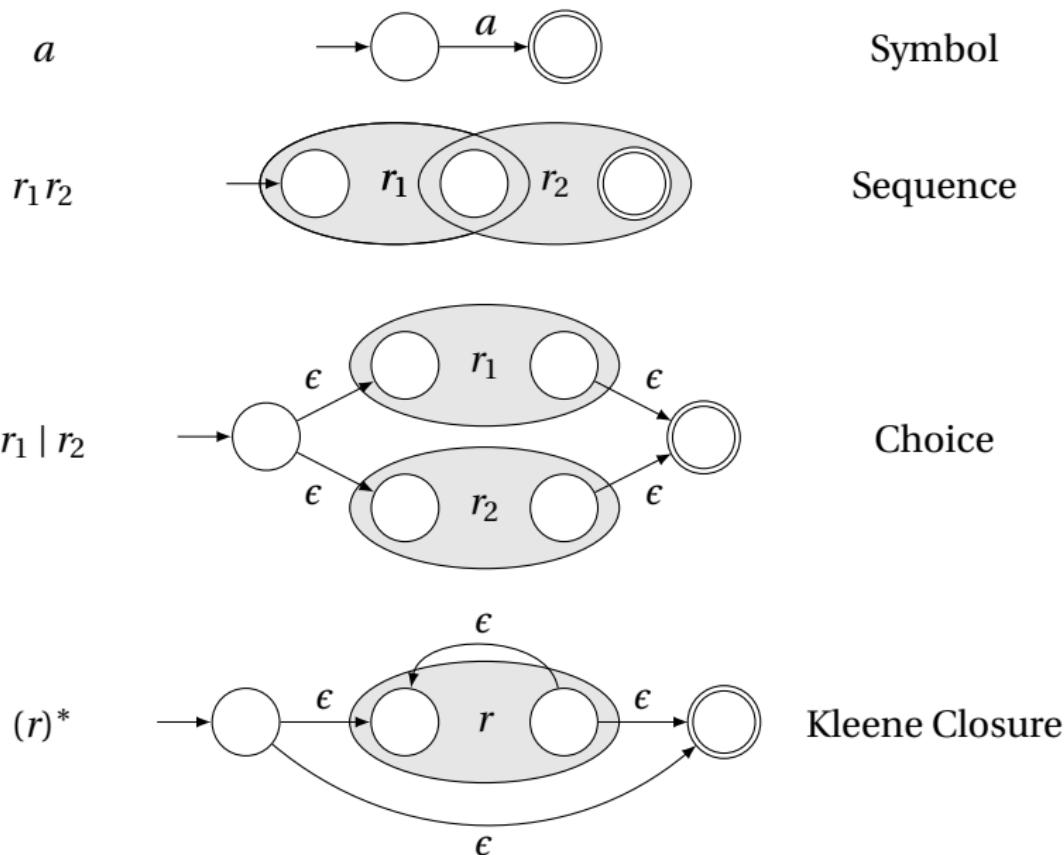
An NFA accepts an input string x iff there is a path from the start state to an accepting state that “spells out” x .



Show that the string “010010” is accepted.

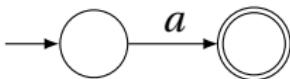


Translating REs into NFAs



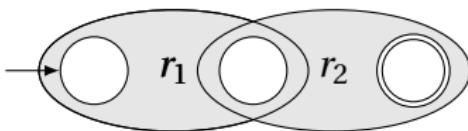
Translating REs into NFAs

a



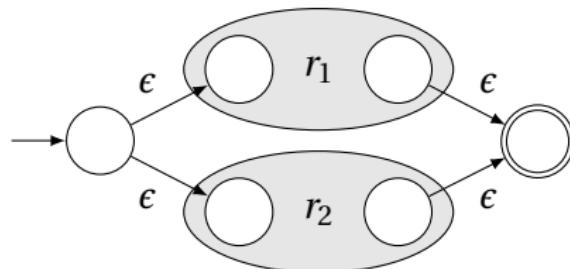
Symbol

$r_1 r_2$



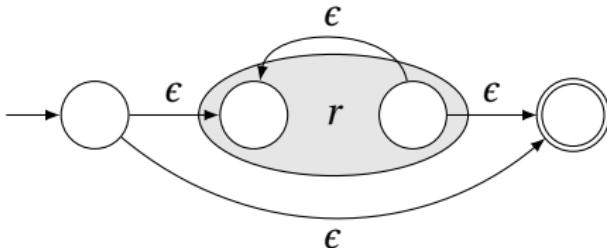
Sequence

$r_1 | r_2$



Choice

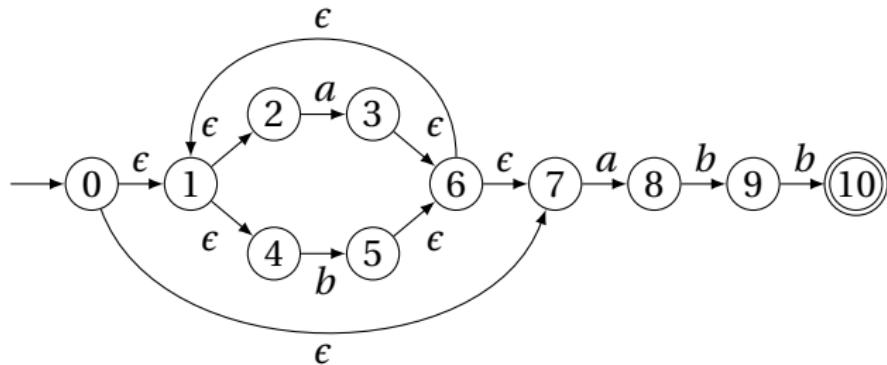
$(r)^*$



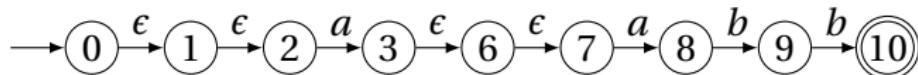
Kleene Closure

Translating REs into NFAs

Example: Translate $(a \mid b)^* abb$ into an NFA. Answer:



Show that the string “*aabb*” is accepted. Answer:



Simulating NFAs

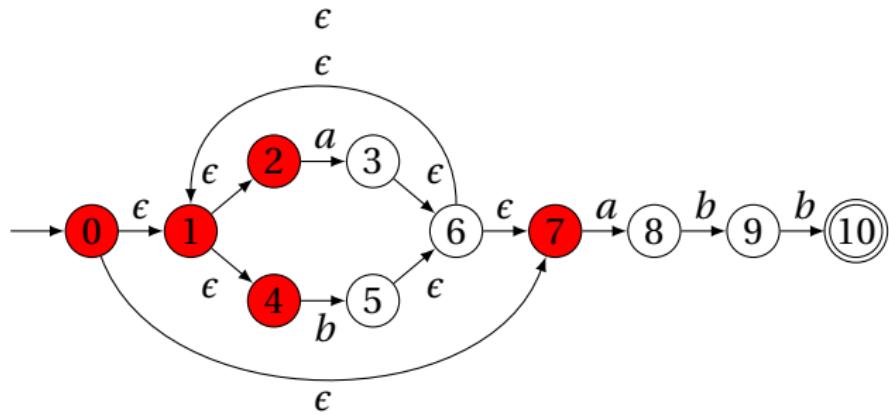
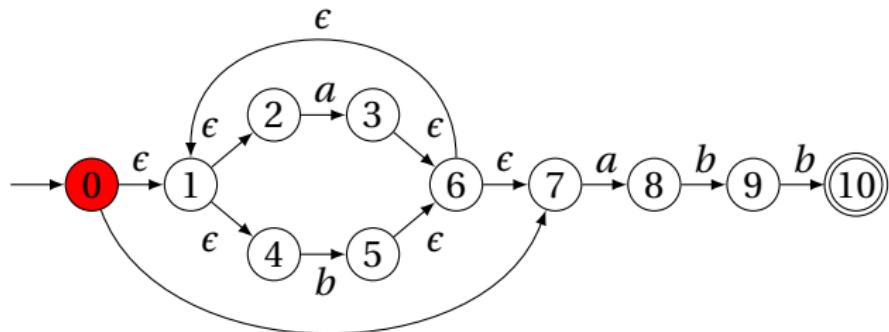
Problem: you must follow the “right” arcs to show that a string is accepted. How do you know which arc is right?

Solution: follow them all and sort it out later.

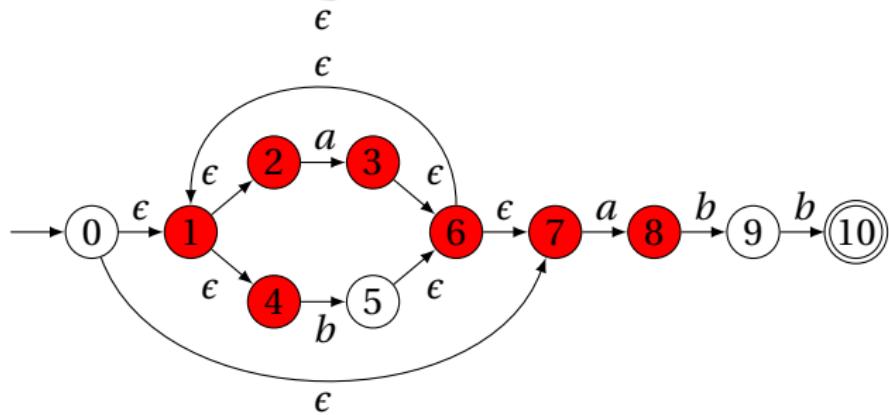
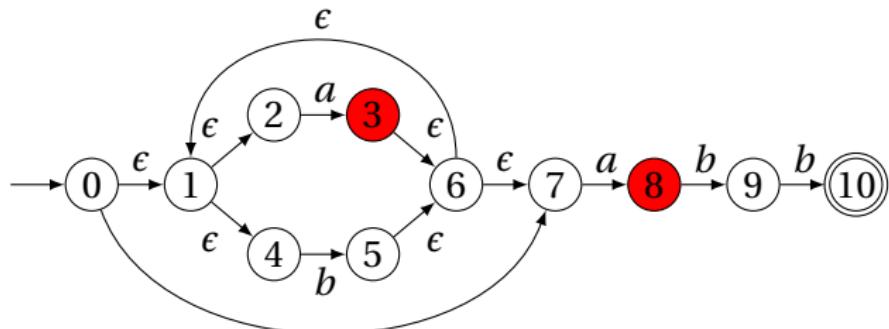
“Two-stack” NFA simulation algorithm:

1. Initial states: the ϵ -closure of the start state
2. For each character c ,
 - ▶ New states: follow all transitions labeled c
 - ▶ Form the ϵ -closure of the current states
3. Accept if any final state is accepting

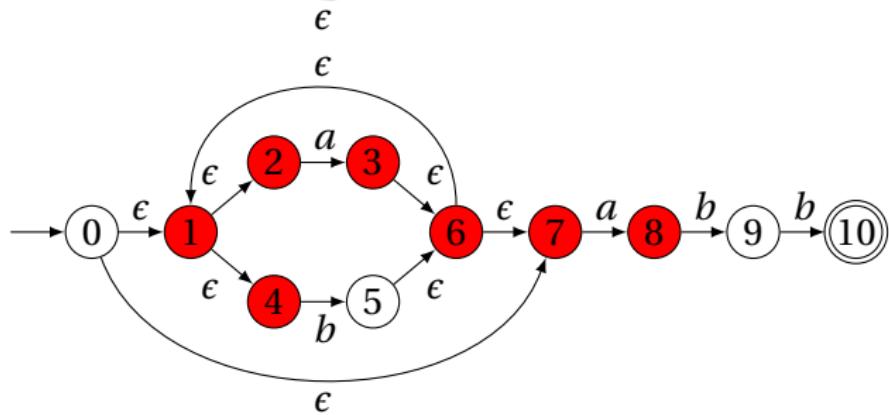
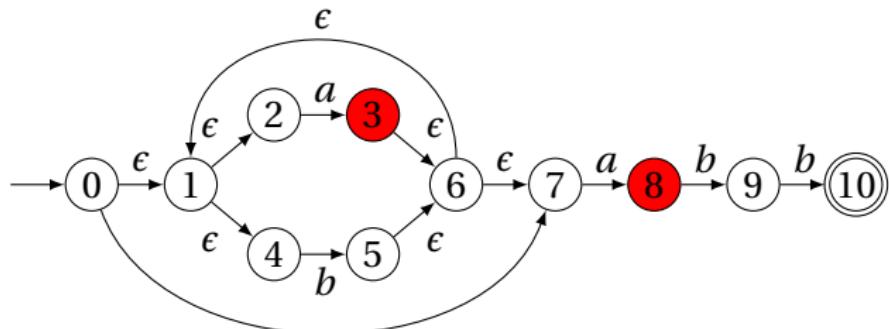
Simulating an NFA: $\cdot aabb$, Start



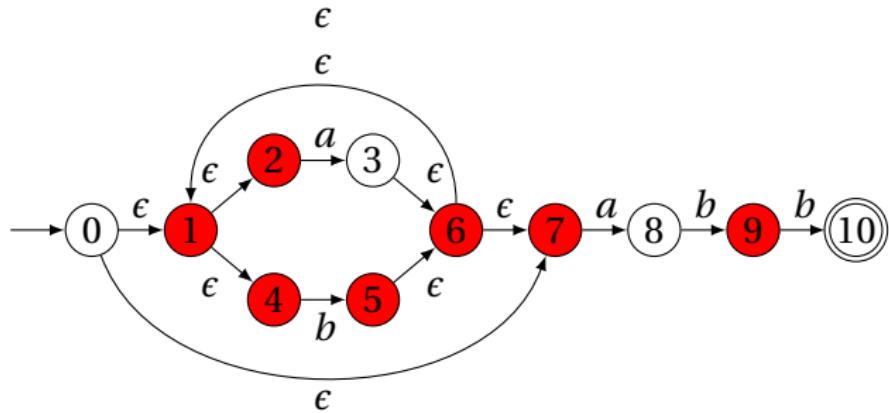
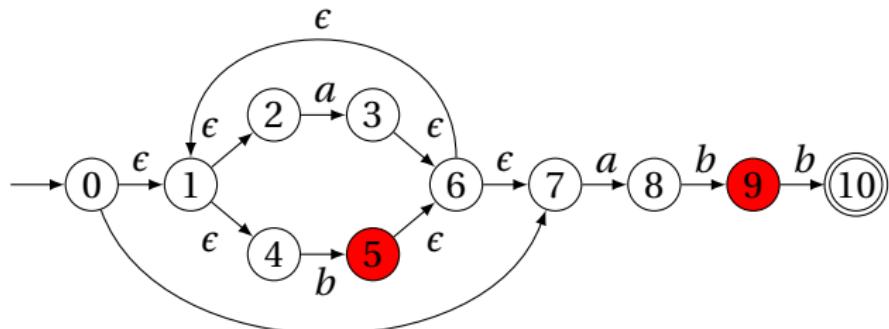
Simulating an NFA: $a \cdot abb$



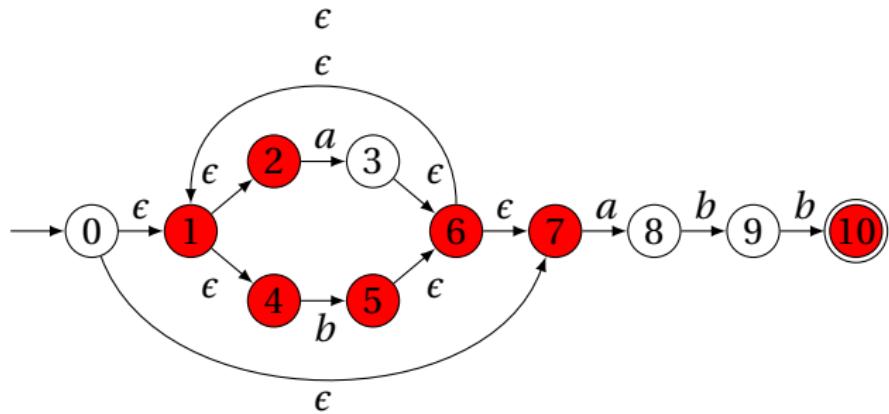
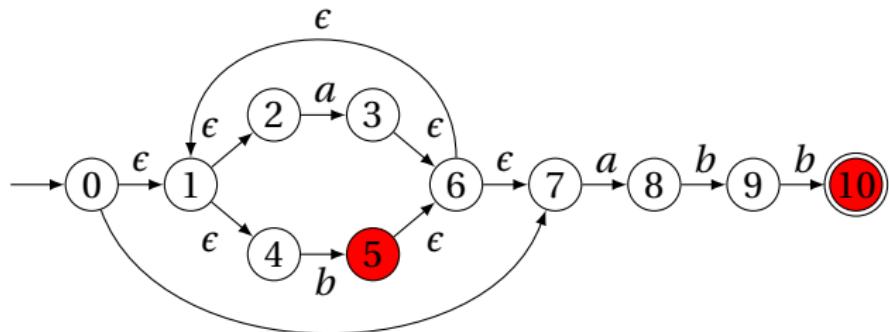
Simulating an NFA: $aa \cdot bb$



Simulating an NFA: $aab \cdot b$



Simulating an NFA: $aabb\cdot$, Done



Deterministic Finite Automata

Restricted form of NFAs:

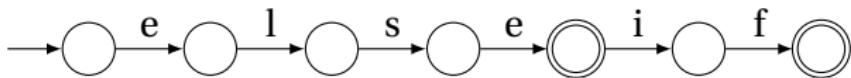
- ▶ No state has a transition on ϵ
- ▶ For each state s and symbol a , there is at most one edge labeled a leaving s .

Differs subtly from the definition used in COMS W3261 (Sipser,
Introduction to the Theory of Computation)

Very easy to check acceptance: simulate by maintaining current state. Accept if you end up on an accepting state. Reject if you end on a non-accepting state or if there is no transition from the current state for the next symbol.

Deterministic Finite Automata

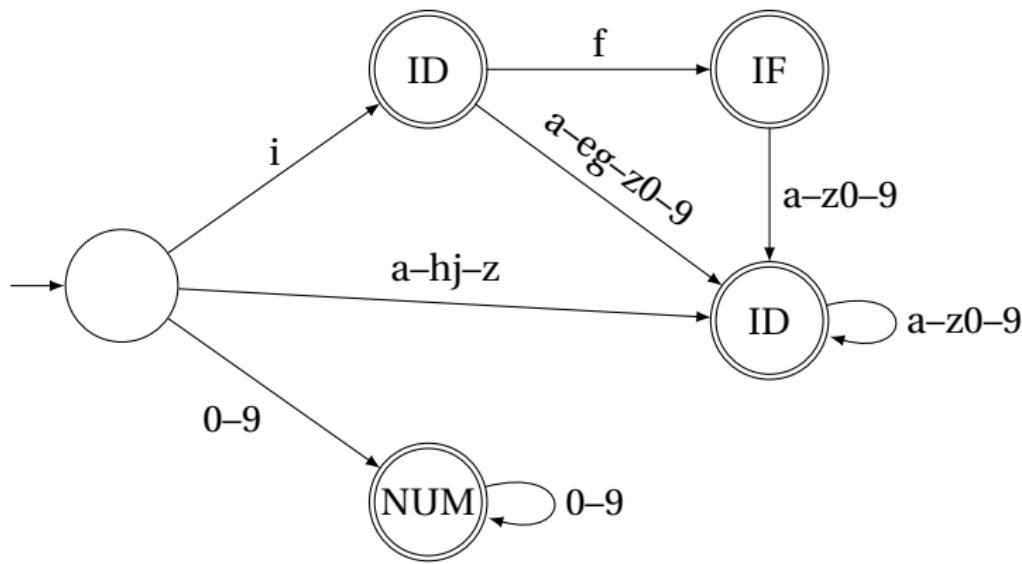
```
{  
    type token = ELSE | ELSEIF  
}  
  
rule token =  
    parse "else" { ELSE }  
        | "elseif" { ELSEIF }
```



Deterministic Finite Automata

```
{ type token = IF | ID of string | NUM of string }

rule token =
  parse "if"
    | ['a'-'z'] ['a'-'z' '0'-'9']* as lit { ID(lit) }
    | ['0'-'9']+ as num { NUM(num) }
```



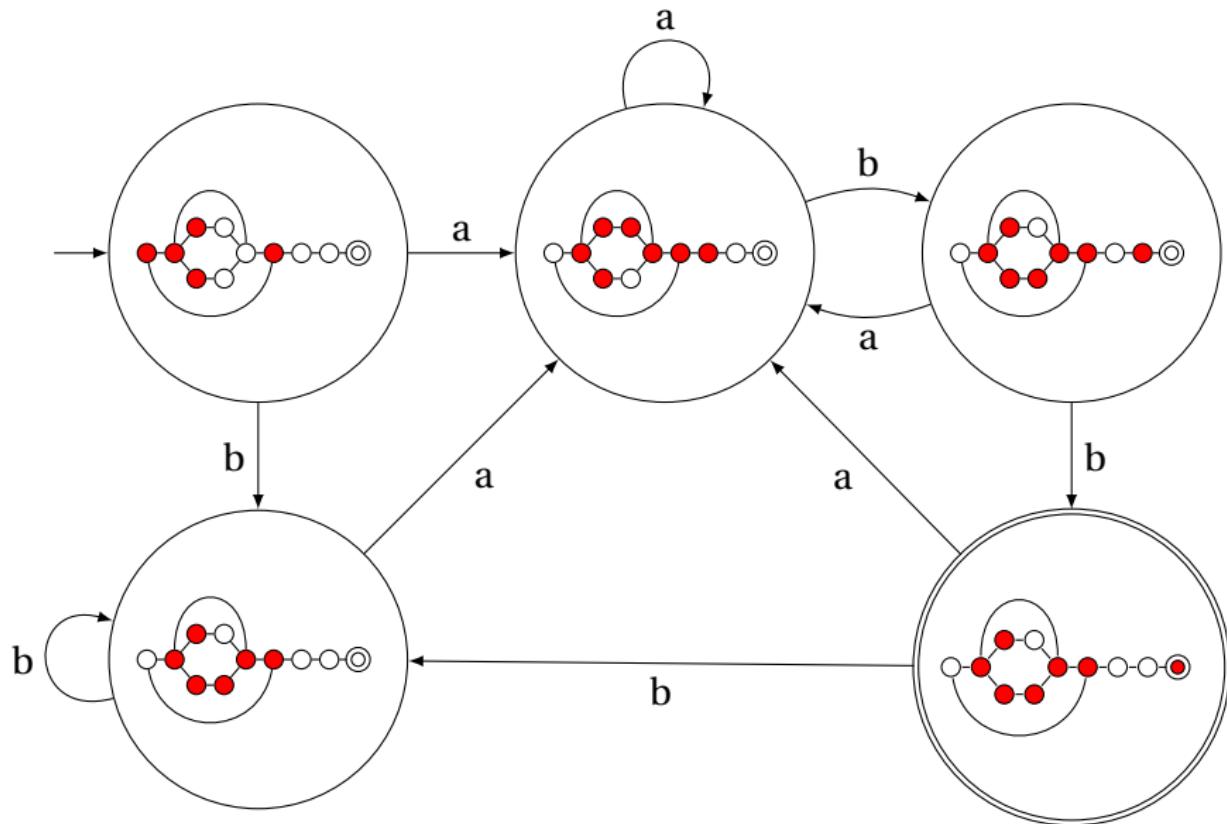
Building a DFA from an NFA

Subset construction algorithm

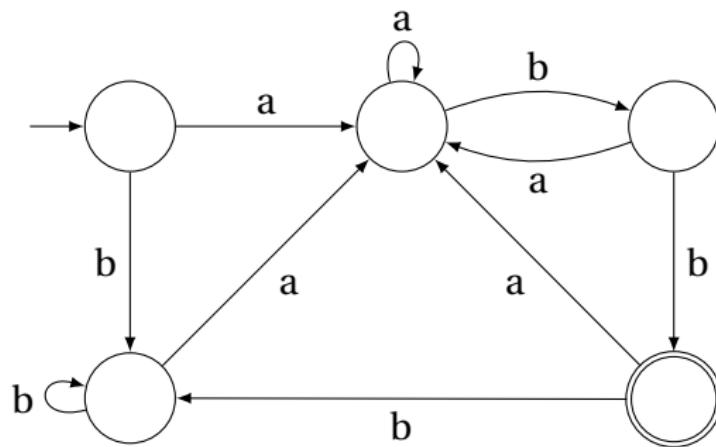
Simulate the NFA for all possible inputs and track the states that appear.

Each unique state during simulation becomes a state in the DFA.

Subset construction for $(a \mid b)^* abb$



Result of subset construction for $(a \mid b)^* abb$



Is this minimal?

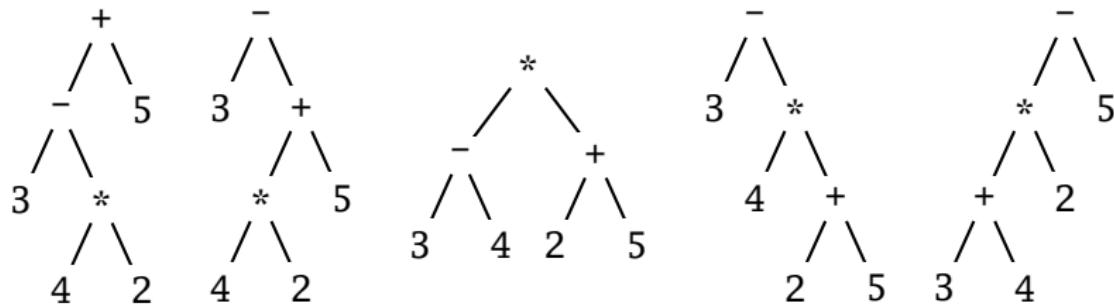
Ambiguous Arithmetic

Ambiguity can be a problem in expressions. Consider parsing

3 - 4 * 2 + 5

with the grammar

$$e \rightarrow e + e \mid e - e \mid e * e \mid e / e \mid N$$



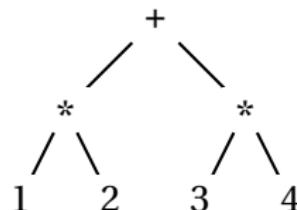
Operator Precedence

Defines how “sticky” an operator is.

$1 * 2 + 3 * 4$

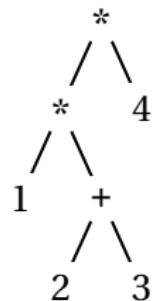
* at higher precedence than +:

$(1 * 2) + (3 * 4)$



+ at higher precedence than *:

$1 * (2 + 3) * 4$

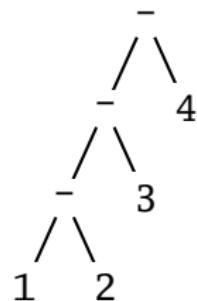


Associativity

Whether to evaluate left-to-right or right-to-left

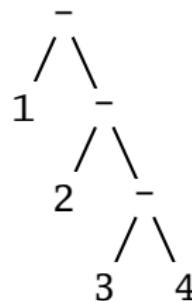
Most operators are left-associative

1 - 2 - 3 - 4



$$((1 - 2) - 3) - 4$$

left associative



$$1 - (2 - (3 - 4))$$

right associative

Fixing Ambiguous Grammars

A grammar specification:

```
expr :  
      expr PLUS expr  
    | expr MINUS expr  
    | expr TIMES expr  
    | expr DIVIDE expr  
    | NUMBER
```

Ambiguous: no precedence or associativity.

Ocamlyacc's complaint: "16 shift/reduce conflicts."

Assigning Precedence Levels

Split into multiple rules, one per level

```
expr : expr PLUS expr
      | expr MINUS expr
      | term

term : term TIMES term
      | term DIVIDE term
      | atom

atom : NUMBER
```

Still ambiguous: associativity not defined

Ocamlyacc's complaint: "8 shift/reduce conflicts."

Assigning Associativity

Make one side the next level of precedence

```
expr : expr PLUS term
      | expr MINUS term
      | term

term : term TIMES atom
      | term DIVIDE atom
      | atom

atom : NUMBER
```

This is left-associative.

No shift/reduce conflicts.

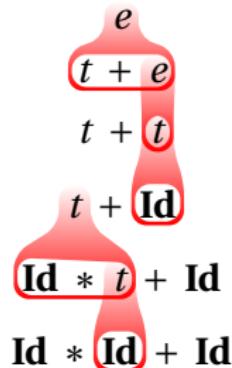
Rightmost Derivation of $\mathbf{Id} * \mathbf{Id} + \mathbf{Id}$

1 : $e \rightarrow t + e$

2 : $e \rightarrow t$

3 : $t \rightarrow \mathbf{Id} * t$

4 : $t \rightarrow \mathbf{Id}$



At each step, expand the *rightmost* nonterminal.

nonterminal

“handle”: The right side of a production

Fun and interesting fact: there is exactly one rightmost expansion if the grammar is unambiguous.

Rightmost Derivation: What to Expand

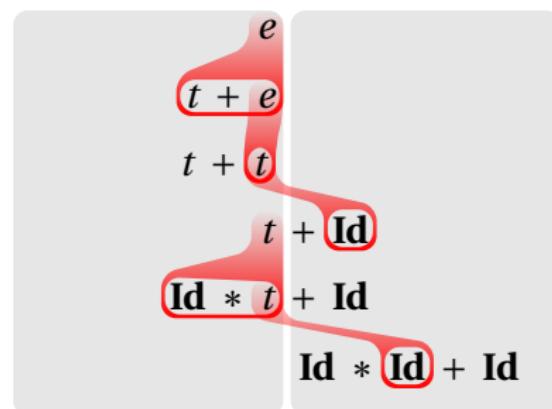
1 : $e \rightarrow t + e$

2 : $e \rightarrow t$

3 : $t \rightarrow \mathbf{Id} * t$

4 : $t \rightarrow \mathbf{Id}$

e
 $t + e$
 $t + t$
 $t + \mathbf{Id}$
 $\mathbf{Id} * t + \mathbf{Id}$
 $\mathbf{Id} * \mathbf{Id} + \mathbf{Id}$



Expand here ↑

Terminals only

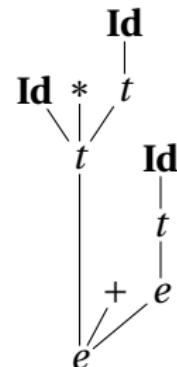
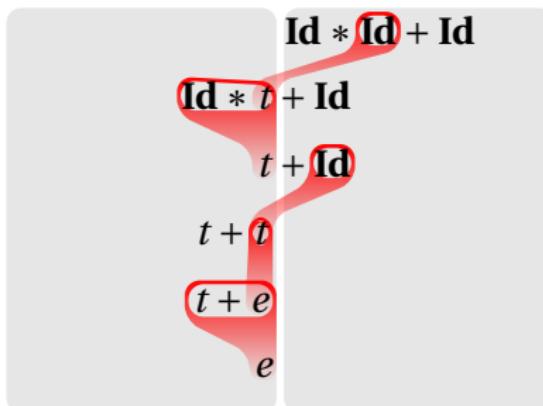
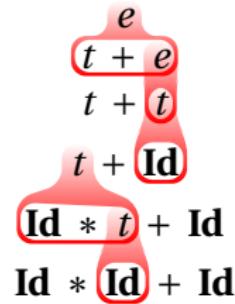
Reverse Rightmost Derivation

1 : $e \rightarrow t + e$

2 : $e \rightarrow t$

3 : $t \rightarrow \mathbf{Id} * t$

4 : $t \rightarrow \mathbf{Id}$



viable prefixes terminals

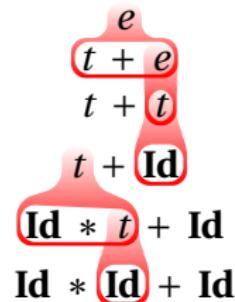
Shift/Reduce Parsing Using an Oracle

1 : $e \rightarrow t + e$

2 : $e \rightarrow t$

3 : $t \rightarrow \text{Id} * t$

4 : $t \rightarrow \text{Id}$



stack	input	
	e	accept
$t + e$		
$t + t$		reduce 2
$t + \text{Id}$		reduce 1
$\text{Id} * \text{Id} + \text{Id}$		
$\text{Id} * \text{Id} + \text{Id}$		reduce 4
$\text{Id} * \text{Id} + \text{Id}$		reduce 3
$t + \text{Id}$		shift
$t + \text{Id}$		shift
$t + t$		shift
$t + e$		shift
e		shift

Handle Hunting

Right Sentential Form: any step in a rightmost derivation

Handle: in a sentential form, a RHS of a rule that, when rewritten, yields the previous step in a rightmost derivation.

The big question in shift/reduce parsing:

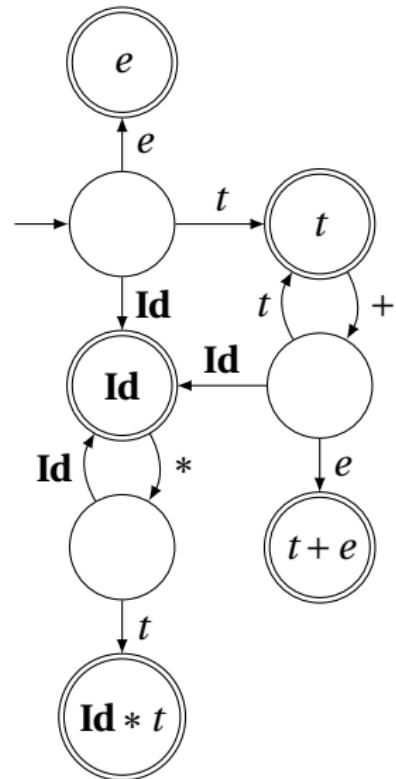
When is there a handle on the top of the stack?

Enumerate all the right-sentential forms and pattern-match against them? *Usually infinite in number, but let's try anyway.*

The Handle-Identifying Automaton

Magical result, due to Knuth: *An automaton suffices to locate a handle in a right-sentential form.*

$\text{Id} * \text{Id} * \dots * \text{Id} * t \dots$
 $\text{Id} * \text{Id} * \dots * \text{Id} \dots$
 $t + t + \dots + t + e$
 $t + t + \dots + t + \text{Id}$
 $t + t + \dots + t + \text{Id} * \text{Id} * \dots * \text{Id} * t$
 $t + t + \dots + t$



Building the Initial State of the LR(0) Automaton

$e' \rightarrow \cdot e$

1 : $e \rightarrow t + e$

2 : $e \rightarrow t$

3 : $t \rightarrow \mathbf{Id} * t$

4 : $t \rightarrow \mathbf{Id}$

Key idea: automata identify viable prefixes of right sentential forms.
Each state is an equivalence class of possible places in productions.

At the beginning, any viable prefix must be at the beginning of a string expanded from e . We write this condition " $e' \rightarrow \cdot e$ "

Building the Initial State of the LR(0) Automaton

1 : $e \rightarrow t + e$

2 : $e \rightarrow t$

3 : $t \rightarrow \mathbf{Id} * t$

4 : $t \rightarrow \mathbf{Id}$

$e' \rightarrow \cdot e$
 $e \rightarrow \cdot t + e$
 $e \rightarrow \cdot t$

Key idea: automata identify viable prefixes of right sentential forms.
Each state is an equivalence class of possible places in productions.

At the beginning, any viable prefix must be at the beginning of a string expanded from e . We write this condition “ $e' \rightarrow \cdot e$ ”

There are two choices for what an e may expand to: $t + e$ and t . So when $e' \rightarrow \cdot e$, $e \rightarrow \cdot t + e$ and $e \rightarrow \cdot t$ are also true, i.e., it must start with a string expanded from t .

Building the Initial State of the LR(0) Automaton

1 : $e \rightarrow t + e$

2 : $e \rightarrow t$

3 : $t \rightarrow \mathbf{Id} * t$

4 : $t \rightarrow \mathbf{Id}$

$e' \rightarrow \cdot e$
$e \rightarrow \cdot t + e$
$e \rightarrow \cdot t$
$t \rightarrow \cdot \mathbf{Id} * t$
$t \rightarrow \cdot \mathbf{Id}$

Key idea: automata identify viable prefixes of right sentential forms.
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Similarly, t must be either $\mathbf{Id} * t$ or \mathbf{Id} , so $t \rightarrow \cdot \mathbf{Id} * t$ and $t \rightarrow \cdot \mathbf{Id}$.

This reasoning is a *closure* operation like ϵ -closure in subset construction.

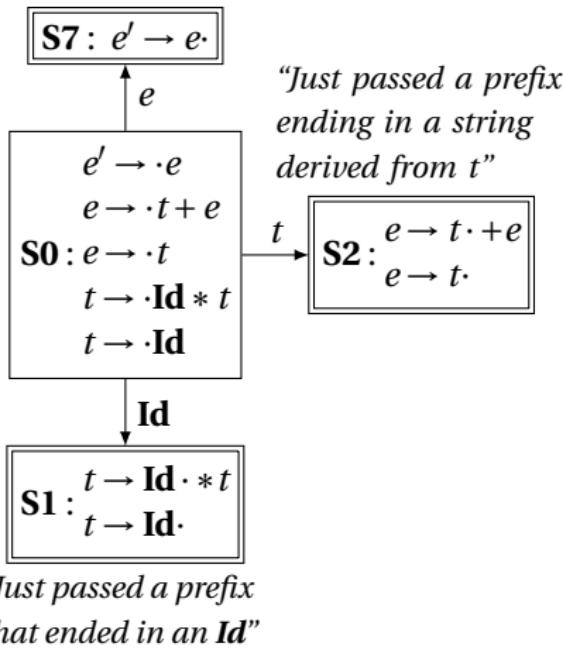
Building the LR(0) Automaton

$e' \rightarrow \cdot e$
$e \rightarrow \cdot t + e$
S0 : $e \rightarrow \cdot t$
$t \rightarrow \cdot \mathbf{Id} * t$
$t \rightarrow \cdot \mathbf{Id}$

The first state suggests a viable prefix can start as any string derived from e , any string derived from t , or **Id**.

Building the LR(0) Automaton

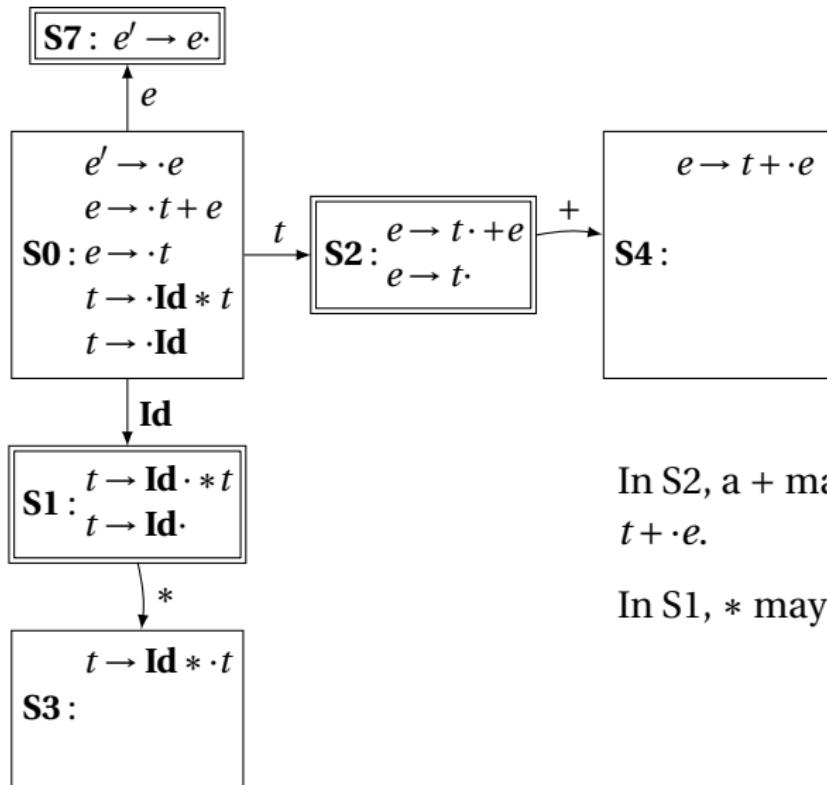
"Just passed a string derived from e"



The first state suggests a viable prefix can start as any string derived from e , any string derived from t , or **Id**.

The items for these three states come from advancing the \cdot across each thing, then performing the closure operation (vacuous here).

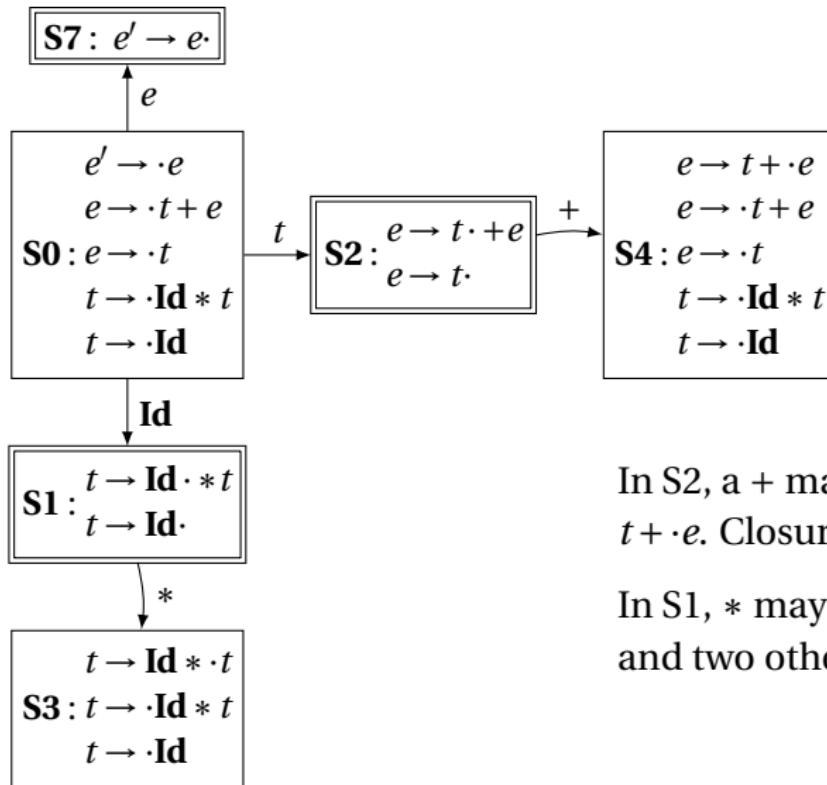
Building the LR(0) Automaton



In S2, a **+** may be next. This gives $t + \cdot e$.

In S1, ***** may be next, giving **Id** $* \cdot t$

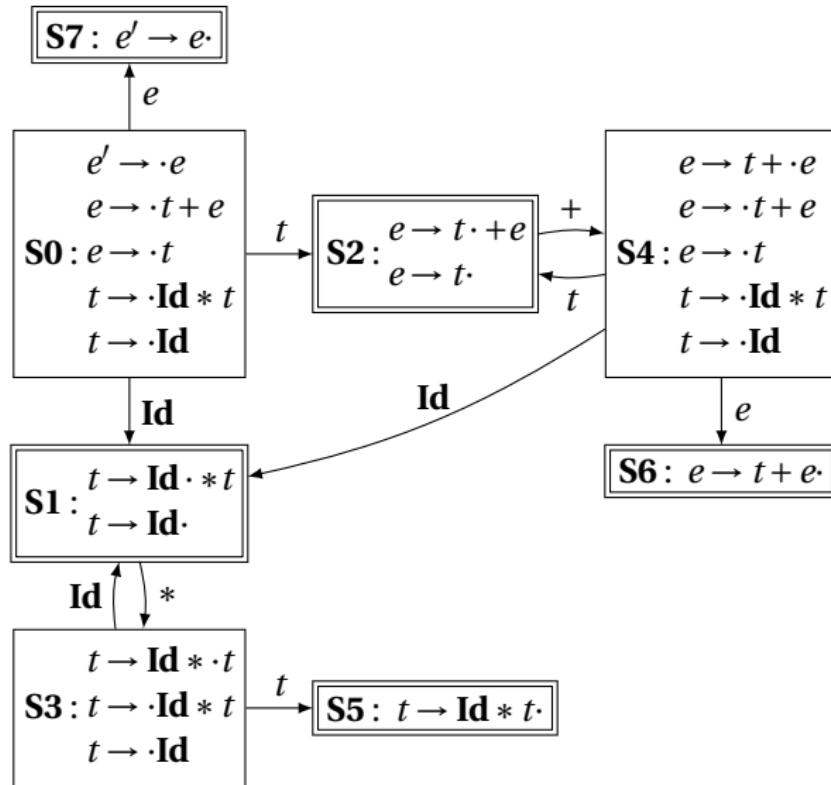
Building the LR(0) Automaton



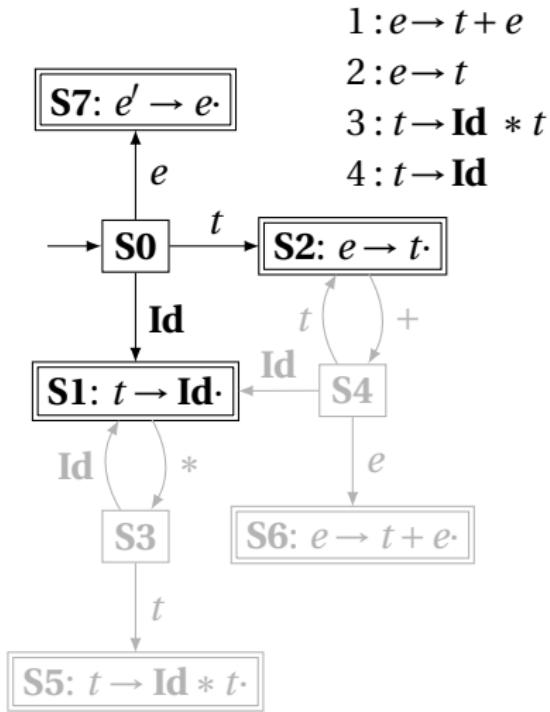
In S2, a $+$ may be next. This gives $t + \cdot e$. Closure adds 4 more items.

In S1, $*$ may be next, giving $\text{Id} * \cdot t$ and two others.

Building the LR(0) Automaton



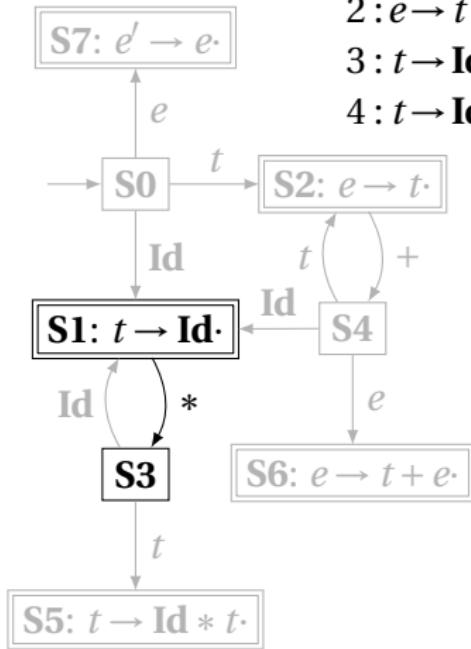
Converting the LR(0) Automaton to an SLR Parsing Table



State	Action				Goto	
	Id	+	*	\$	e	t
0 s1					7	2

From S_0 , shift an **Id** and go to S_1 ; or cross a t and go to S_2 ; or cross an e and go to S_7 .

Converting the LR(0) Automaton to an SLR Parsing Table

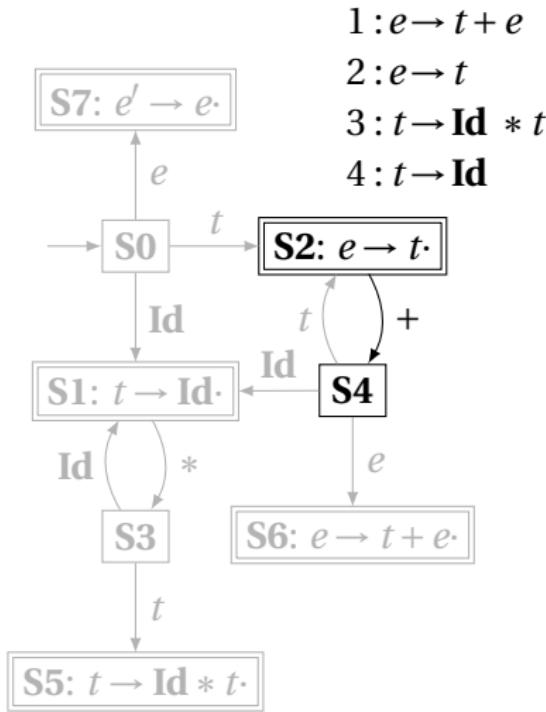


- 1 : $e \rightarrow t + e$
- 2 : $e \rightarrow t$
- 3 : $t \rightarrow Id \cdot * t$
- 4 : $t \rightarrow Id$

State	Action				Goto	
	Id	+	*	\$	e	t
0	s1				7	2
1		r4	s3	r4		

From S_1 , shift a $*$ and go to S_3 ; or, if the next input could follow a t , reduce by rule 4. According to rule 1, $+$ could follow t ; from rule 2, $\$$ could.

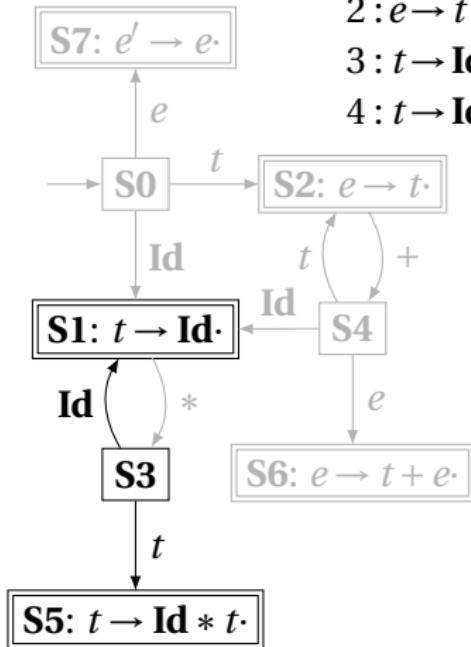
Converting the LR(0) Automaton to an SLR Parsing Table



State	Action				Goto	
	Id	+	*	\$	e	t
0	s1				7	2
1		r4	s3	r4		
2		s4		r2		

From S_2 , shift a $+$ and go to S_4 ; or, if the next input could follow an e (only the end-of-input $$$), reduce by rule 2.

Converting the LR(0) Automaton to an SLR Parsing Table

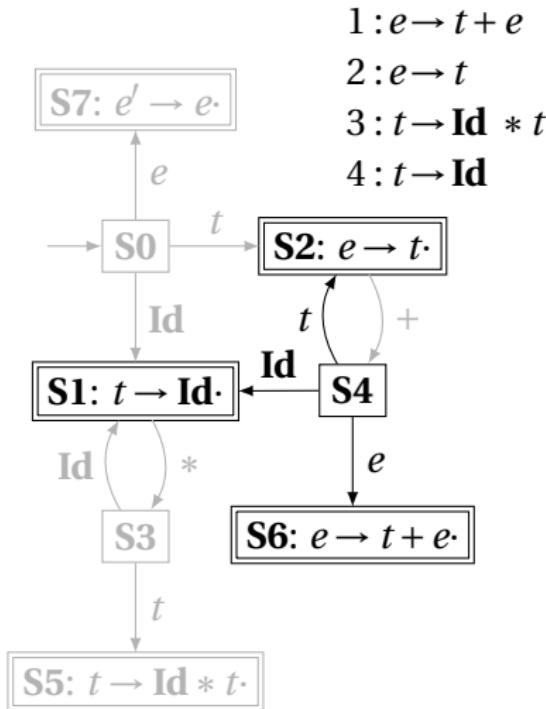


- 1 : $e \rightarrow t + e$
- 2 : $e \rightarrow t$
- 3 : $t \rightarrow \mathbf{Id} * t$
- 4 : $t \rightarrow \mathbf{Id}$

State	Action				Goto	
	Id	+	*	\$	e	t
0	s1				7	2
1		r4	s3	r4		
2		s4		r2		
3	s1					5

From S_3 , shift an **Id** and go to S_1 ; or cross a t and go to S_5 .

Converting the LR(0) Automaton to an SLR Parsing Table

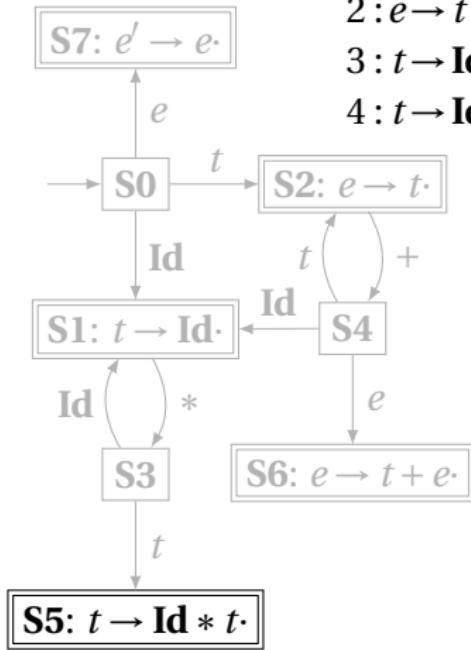


- 1 : $e \rightarrow t + e$
- 2 : $e \rightarrow t$
- 3 : $t \rightarrow \text{Id} * t$
- 4 : $t \rightarrow \text{Id}$

State	Action				Goto	
	Id	+	*	\$	e	t
0	s1				7	2
1		r4	s3	r4		
2		s4		r2		
3	s1					5
4	s1				6	2

From S_4 , shift an **Id** and go to S_1 ; or cross an e or a t .

Converting the LR(0) Automaton to an SLR Parsing Table

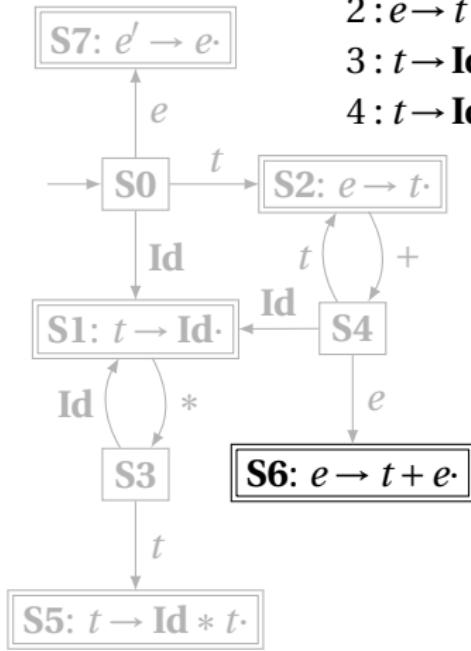


- 1 : $e \rightarrow t + e$
- 2 : $e \rightarrow t$
- 3 : $t \rightarrow \text{Id} * t$
- 4 : $t \rightarrow \text{Id}$

State	Action				Goto	
	Id	+	*	\$	e	t
0	s1				7	2
1		r4	s3	r4		
2		s4		r2		
3	s1					5
4	s1				6	2
5			r3		r3	

From S_5 , reduce using rule 3 if the next symbol could follow a t (again, $+$ and $$$).

Converting the LR(0) Automaton to an SLR Parsing Table

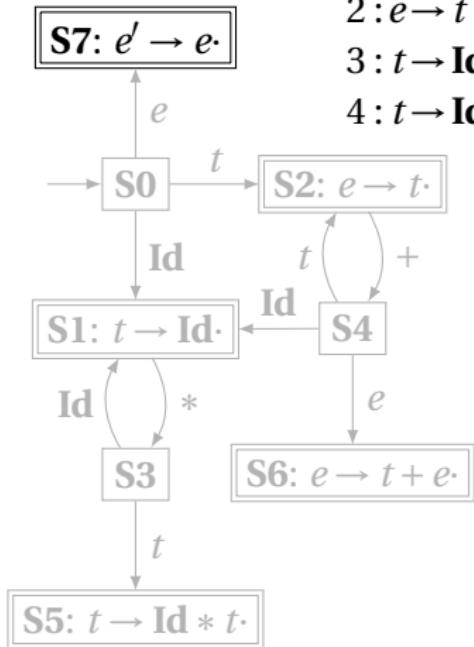


- 1 : $e \rightarrow t + e$
- 2 : $e \rightarrow t$
- 3 : $t \rightarrow Id * t$
- 4 : $t \rightarrow Id$

State	Action				Goto	
	Id	+	*	\$	<i>e</i>	<i>t</i>
0	s1				7	2
1		r4	s3	r4		
2		s4		r2		
3	s1					5
4	s1				6	2
5		r3		r3		
6				r1		

From S6, reduce using rule 1 if the next symbol could follow an *e* (\$) only).

Converting the LR(0) Automaton to an SLR Parsing Table



- 1 : $e \rightarrow t + e$
- 2 : $e \rightarrow t$
- 3 : $t \rightarrow Id * t$
- 4 : $t \rightarrow Id$

State	Action				Goto	
	Id	+	*	\$	<i>e</i>	<i>t</i>
0	s1				7	2
1		r4	s3	r4		
2		s4		r2		
3	s1					5
4	s1				6	2
5		r3		r3		
6			r1			
7				✓		

If, in S7, we just crossed an *e*, accept if we are at the end of the input.

Shift/Reduce Parsing with an SLR Table

Stack	Input	Action
0	Id * Id + Id\$	Shift, goto 1
Look at the state on top of the stack and the next input token.		
Find the action (shift, reduce, or error) in the table.		
In this case, shift the token onto the stack and mark it with state 1.		

State	Action				Goto	
	Id	+	*	\$	e	t
0	s1				7	2
1		r4	s3	r4		
2		s4		r2		
3	s1					5
4	s1				6	2
5		r3		r3		
6				r1		
7				✓		

Shift/Reduce Parsing with an SLR Table

1 : $e \rightarrow t + e$

2 : $e \rightarrow t$

3 : $t \rightarrow \mathbf{Id} * t$

4 : $t \rightarrow \mathbf{Id}$

Stack	Input	Action
0	$\mathbf{Id} * \mathbf{Id} + \mathbf{Id} \$$	Shift, goto 1
0 1	$* \mathbf{Id} + \mathbf{Id} \$$	Shift, goto 3

State	Action				Goto	
	\mathbf{Id}	+	*	\$	e	t
0	s1				7	2
1		r4	s3	r4		
2		s4		r2		
3	s1					5
4	s1				6	2
5		r3		r3		
6				r1		
7				✓		

Here, the state is 1, the next symbol is *, so shift and mark it with state 3.

Shift/Reduce Parsing with an SLR Table

1 : $e \rightarrow t + e$

2 : $e \rightarrow t$

3 : $t \rightarrow \mathbf{Id} * t$

4 : $t \rightarrow \mathbf{Id}$

State	Action				Goto	
	Id	+	*	\$	e	t
0	s1				7	2
1		r4	s3	r4		
2		s4		r2		
3	s1					5
4	s1				6	2
5		r3		r3		
6				r1		
7				✓		

Stack	Input	Action
0	$\mathbf{Id} * \mathbf{Id} + \mathbf{Id} \$$	Shift, goto 1
0 \mathbf{Id}	$* \mathbf{Id} + \mathbf{Id} \$$	Shift, goto 3
0 \mathbf{Id} *	$\mathbf{Id} + \mathbf{Id} \$$	Shift, goto 1
0 \mathbf{Id} * \mathbf{Id}	$+ \mathbf{Id} \$$	Reduce 4

Here, the state is 1, the next symbol is +. The table says reduce using rule 4.

Shift/Reduce Parsing with an SLR Table

1 : $e \rightarrow t + e$

2 : $e \rightarrow t$

3 : $t \rightarrow \mathbf{Id} * t$

4 : $t \rightarrow \mathbf{Id}$

State	Action				Goto	
	Id	+	*	\$	e	t
0	s1				7	2
1		r4	s3	r4		
2		s4		r2		
3	s1					5
4	s1				6	2
5		r3		r3		
6				r1		
7				✓		

Stack	Input	Action
0	$\mathbf{Id} * \mathbf{Id} + \mathbf{Id} \$$	Shift, goto 1
0 \mathbf{Id}	* $\mathbf{Id} + \mathbf{Id} \$$	Shift, goto 3
0 $\mathbf{Id} *$	$\mathbf{Id} + \mathbf{Id} \$$	Shift, goto 1
0 $\mathbf{Id} * \mathbf{Id}$	+ $\mathbf{Id} \$$	Reduce 4
0 $\mathbf{Id} *$	+ $\mathbf{Id} \$$	

Remove the RHS of the rule (here, just **Id**), observe the state on the top of the stack, and consult the “goto” portion of the table.

Shift/Reduce Parsing with an SLR Table

1 : $e \rightarrow t + e$

2 : $e \rightarrow t$

3 : $t \rightarrow \mathbf{Id} * t$

4 : $t \rightarrow \mathbf{Id}$

State	Action				Goto	
	Id	+	*	\$	e	t
0	s1				7	2
1		r4	s3	r4		
2		s4		r2		
3	s1					5
4	s1				6	2
5		r3		r3		
6				r1		
7				✓		

Stack	Input	Action
0	$\mathbf{Id} * \mathbf{Id} + \mathbf{Id} \$$	Shift, goto 1
0 \mathbf{Id}	$* \mathbf{Id} + \mathbf{Id} \$$	Shift, goto 3
0 $\mathbf{Id} *$	$\mathbf{Id} + \mathbf{Id} \$$	Shift, goto 1
0 $\mathbf{Id} * \mathbf{Id}$	$+ \mathbf{Id} \$$	Reduce 4
0 $\mathbf{Id} * t$	$+ \mathbf{Id} \$$	Reduce 3

Here, we push a t with state 5. This effectively “backs up” the LR(0) automaton and runs it over the newly added nonterminal.

In state 5 with an upcoming $+$, the action is “reduce 3.”

Shift/Reduce Parsing with an SLR Table

1 : $e \rightarrow t + e$

2 : $e \rightarrow t$

3 : $t \rightarrow \mathbf{Id} * t$

4 : $t \rightarrow \mathbf{Id}$

State	Action				Goto	
	Id	+	*	\$	e	t
0	s1				7	2
1		r4	s3	r4		
2		s4		r2		
3	s1					5
4	s1				6	2
5		r3		r3		
6				r1		
7				✓		

Stack	Input	Action
0	$\mathbf{Id} * \mathbf{Id} + \mathbf{Id} \$$	Shift, goto 1
0 \mathbf{Id}	* $\mathbf{Id} + \mathbf{Id} \$$	Shift, goto 3
0 $\mathbf{Id} *$	$\mathbf{Id} + \mathbf{Id} \$$	Shift, goto 1
0 $\mathbf{Id} * \mathbf{Id}$	+ $\mathbf{Id} \$$	Reduce 4
0 $\mathbf{Id} * t$	+ $\mathbf{Id} \$$	Reduce 3
0 t	+ $\mathbf{Id} \$$	Shift, goto 4

This time, we strip off the RHS for rule 3, $\mathbf{Id} * t$, exposing state 0, so we push a t with state 2.

Shift/Reduce Parsing with an SLR Table

1 : $e \rightarrow t + e$

2 : $e \rightarrow t$

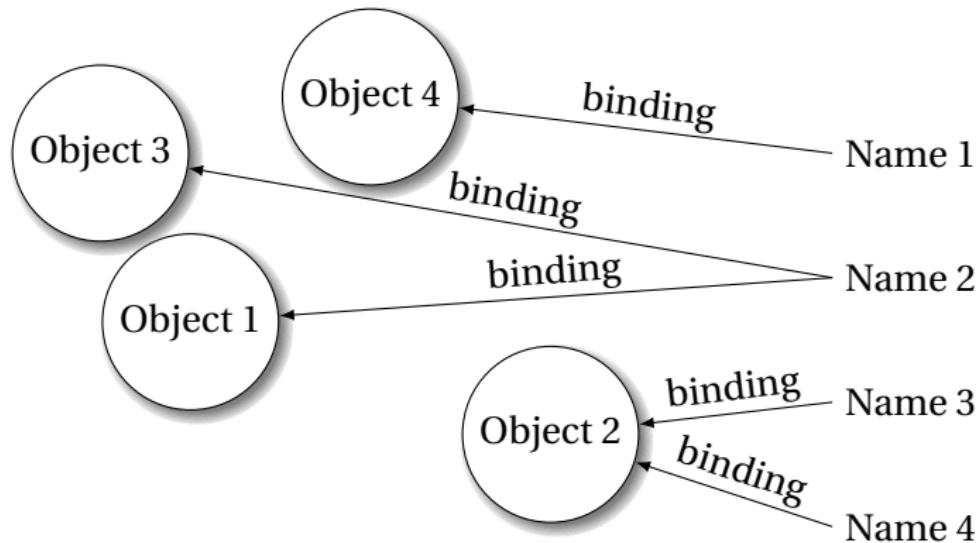
3 : $t \rightarrow \mathbf{Id} * t$

4 : $t \rightarrow \mathbf{Id}$

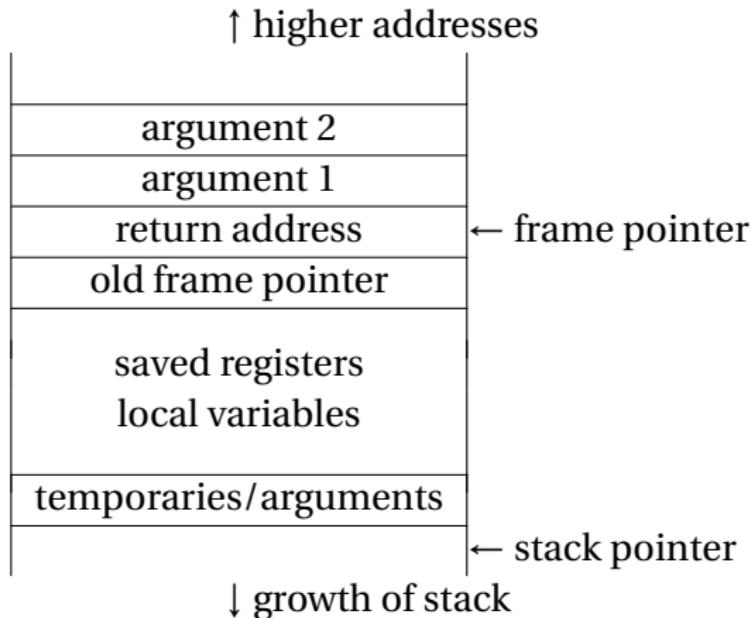
State	Action				Goto	
	Id	+	*	\$	e	t
0	s1				7	2
1		r4	s3	r4		
2		s4		r2		
3	s1				5	
4	s1				6	2
5		r3		r3		
6			r1			
7				✓		

Stack	Input	Action
0	$\mathbf{Id} * \mathbf{Id} + \mathbf{Id} \$$	Shift, goto 1
0 \mathbf{Id}	* $\mathbf{Id} + \mathbf{Id} \$$	Shift, goto 3
0 $\mathbf{Id} *$	$\mathbf{Id} + \mathbf{Id} \$$	Shift, goto 1
0 $\mathbf{Id} * \mathbf{Id}$	+ $\mathbf{Id} \$$	Reduce 4
0 $\mathbf{Id} * t$	+ $\mathbf{Id} \$$	Reduce 3
0 t	+ $\mathbf{Id} \$$	Shift, goto 4
0 $t +$	$\mathbf{Id} \$$	Shift, goto 1
0 $t + \mathbf{Id}$	\$	Reduce 4
0 $t + t$	\$	Reduce 2
0 $t + e$	\$	Reduce 1
0 e	\$	Accept

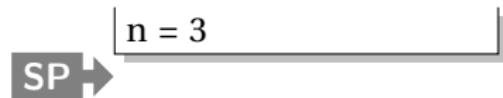
Names, Objects, and Bindings



Typical Stack Layout



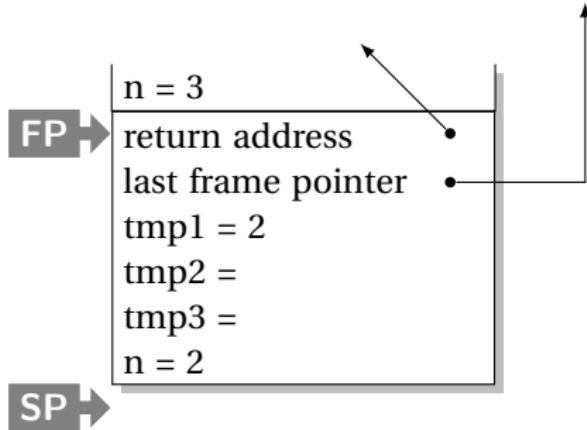
Executing fib(3)



```
int fib(int n) {  
    int tmp1, tmp2, tmp3;  
    tmp1 = n < 2;  
    if (!tmp1) goto L1;  
    return 1;  
L1: tmp1 = n - 1;  
    tmp2 = fib(tmp1);  
L2: tmp1 = n - 2;  
    tmp3 = fib(tmp1);  
L3: tmp1 = tmp2 + tmp3;  
    return tmp1;  
}
```

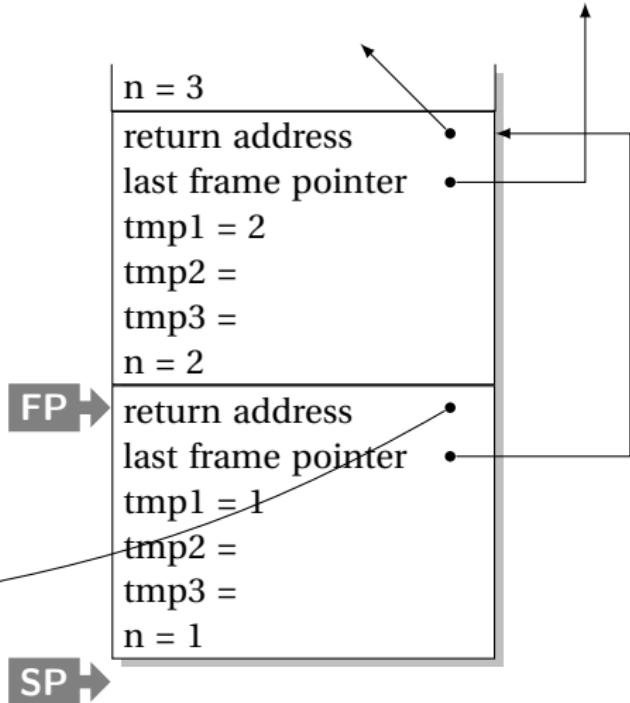
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    tmp2 = fib(tmp1);  
L2: tmp1 = n - 2;  
    tmp3 = fib(tmp1);  
L3: tmp1 = tmp2 + tmp3;  
    return tmp1;  
}
```



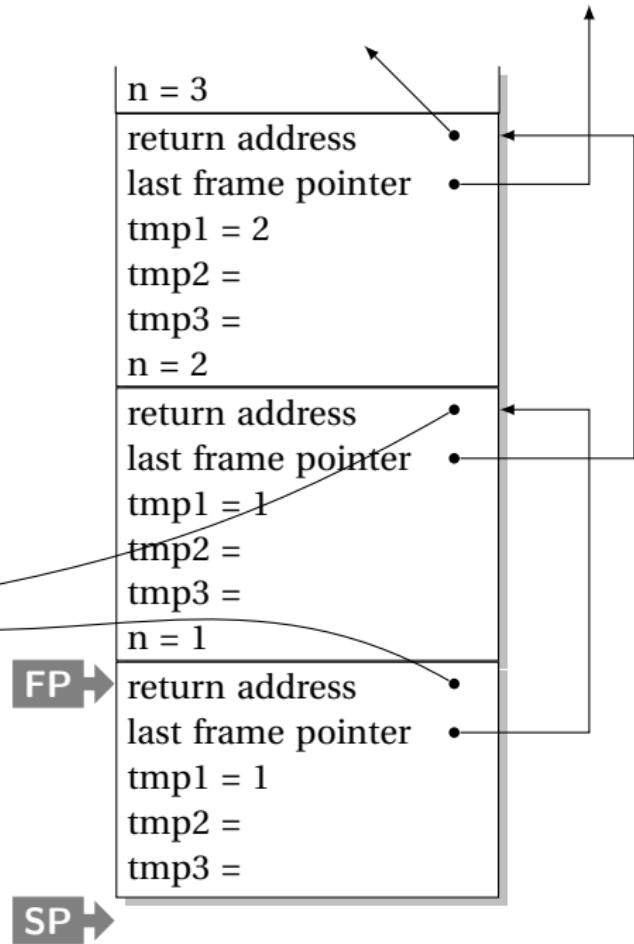
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L2: tmp1 = n - 2;  
    tmp3 = fib(tmp1);  
L3: tmp1 = tmp2 + tmp3;  
    return tmp1;  
}
```



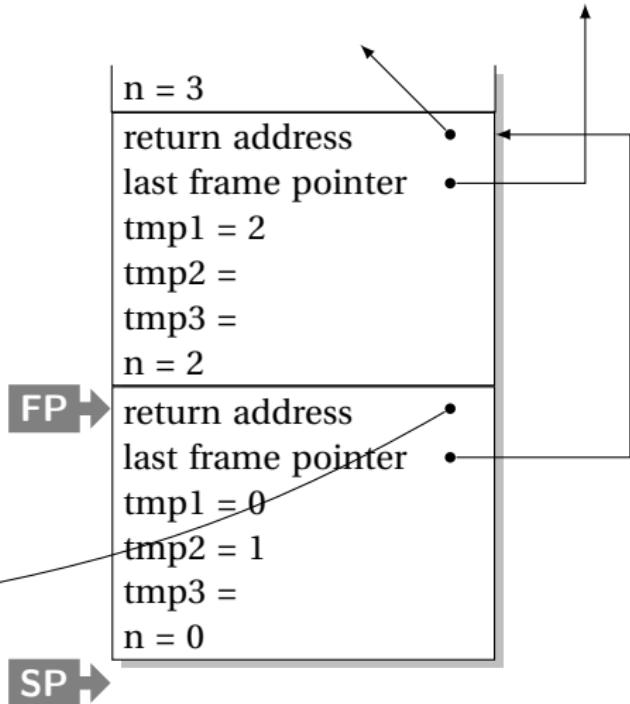
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L1: tmp1 = n - 1;  
    tmp2 = fib(tmp1);  
L2: tmp1 = n - 2;  
    tmp3 = fib(tmp1);  
L3: tmp1 = tmp2 + tmp3;  
    return tmp1;  
}
```



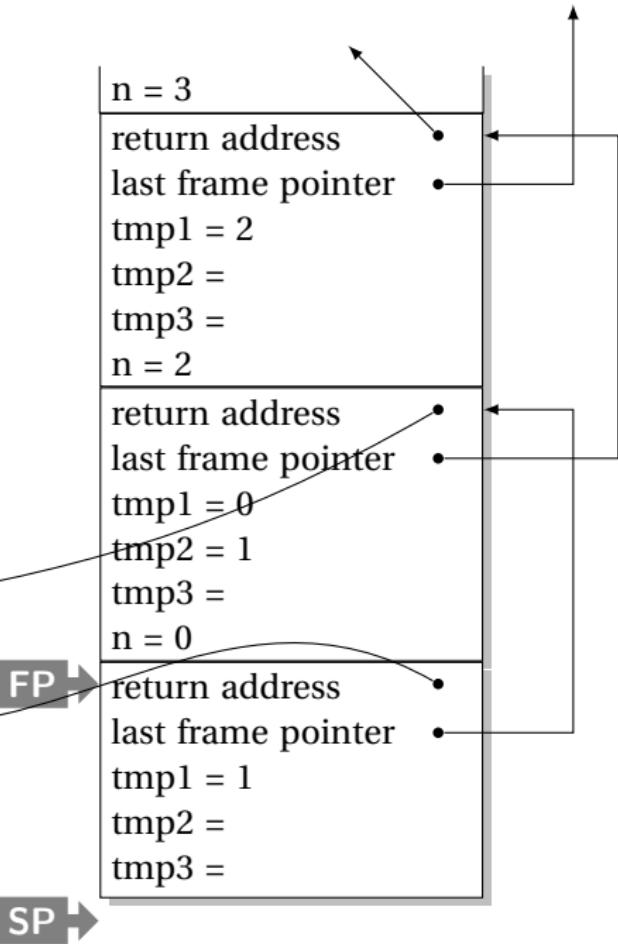
Executing fib(3)

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    tmp2 = fib(tmp1);  
L2: tmp1 = n - 2;  
    tmp3 = fib(tmp1);  
L3: tmp1 = tmp2 + tmp3;  
    return tmp1;  
}
```



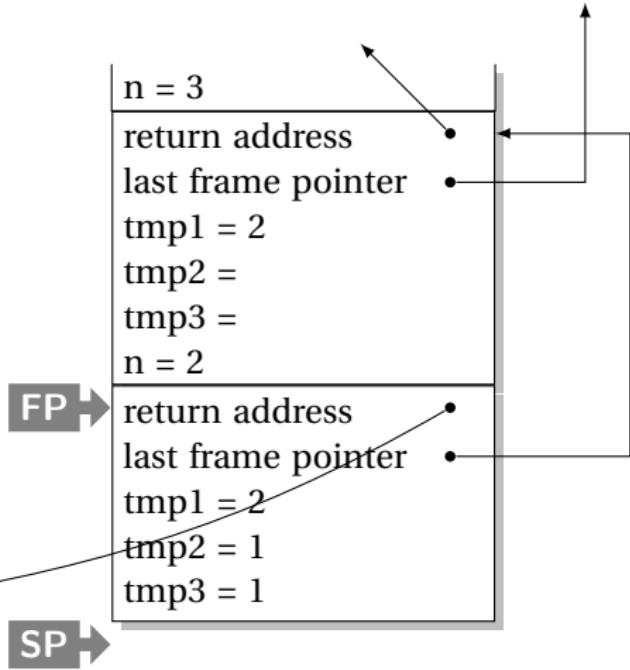
Executing fib(3)

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int fib(int n) {  
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    tmp1 = n < 2;  
    if (!tmp1) goto L1;  
    return 1;  
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    tmp2 = fib(tmp1);  
L2: tmp1 = n - 2;  
    tmp3 = fib(tmp1);  
L3: tmp1 = tmp2 + tmp3;  
    return tmp1;  
}
```



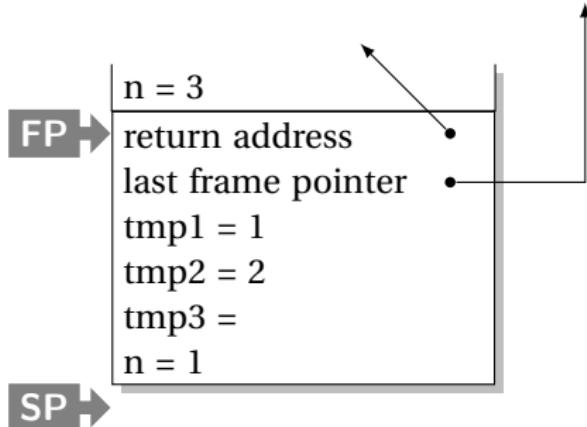
Executing fib(3)

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    int tmp1, tmp2, tmp3;  
    tmp1 = n < 2;  
    if (!tmp1) goto L1;  
    return 1;  
L1: tmp1 = n - 1;  
    tmp2 = fib(tmp1);  
L2: tmp1 = n - 2;  
    tmp3 = fib(tmp1);  
L3: tmp1 = tmp2 + tmp3;  
    return tmp1;  
}
```



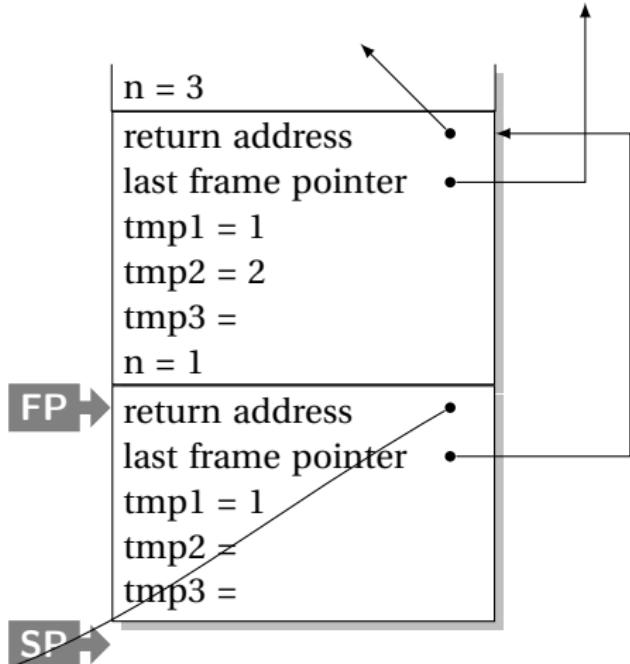
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L2: tmp1 = n - 2;  
    tmp3 = fib(tmp1);  
L3: tmp1 = tmp2 + tmp3;  
    return tmp1;  
}
```



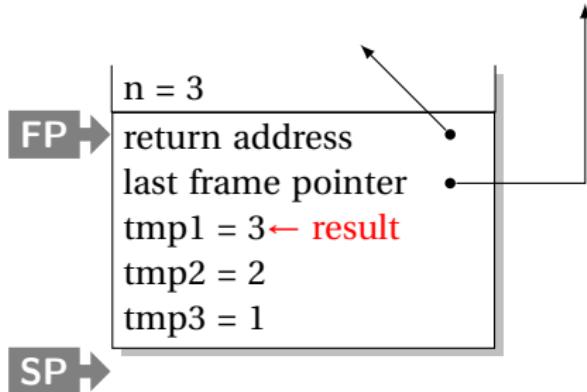
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L2: tmp1 = n - 2;  
    tmp3 = fib(tmp1);  
L3: tmp1 = tmp2 + tmp3;  
    return tmp1;  
}
```



Executing fib(3)

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    return 1;  
L1: tmp1 = n - 1;  
    tmp2 = fib(tmp1);  
L2: tmp1 = n - 2;  
    tmp3 = fib(tmp1);  
L3: tmp1 = tmp2 + tmp3;  
    return tmp1;  
}
```



Implementing Nested Functions with Static Links

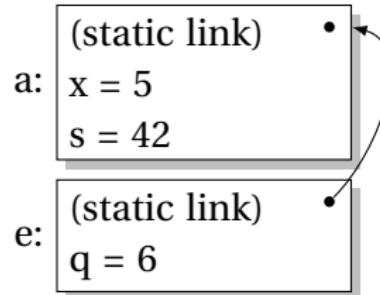
```
let a x s =  
  
  let b y =  
    let c z = z + s in  
    let d w = c (w+1) in  
      d (y+1) in (* b *)  
  
  let e q = b (q+1) in  
    e (x+1) (* a *)
```

(static link) •
a: x = 5
s = 42

What does “a 5 42” evaluate to?

Implementing Nested Functions with Static Links

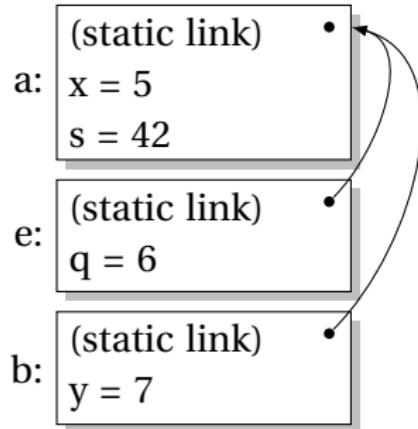
```
let a x s =  
  
  let b y =  
  
    let c z = z + s in  
  
      let d w = c (w+1) in  
  
        d (y+1) in (* b *)  
  
  let e q = b (q+1) in  
  
    e (x+1) (* a *)
```



What does “a 5 42” evaluate to?

Implementing Nested Functions with Static Links

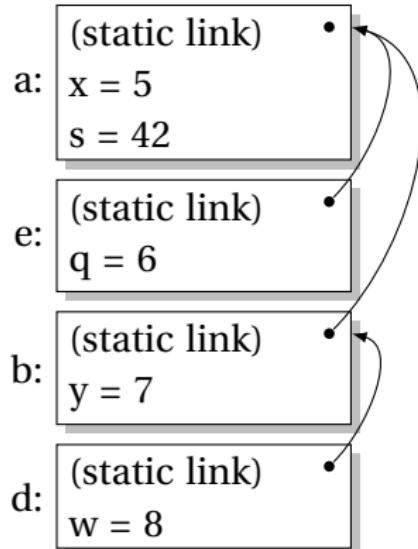
```
let a x s =  
  
  let b y =  
  
    let c z = z + s in  
  
    let d w = c (w+1) in  
  
      d (y+1) in (* b *)  
  
let e q = b (q+1) in  
  
  e (x+1) (* a *)
```



What does “a 5 42” evaluate to?

Implementing Nested Functions with Static Links

```
let a x s =  
  
  let b y =  
  
    let c z = z + s in  
  
    let d w = c (w+1) in  
  
      d (y+1) in (* b *)  
  
  let e q = b (q+1) in  
  
    e (x+1) (* a *)
```

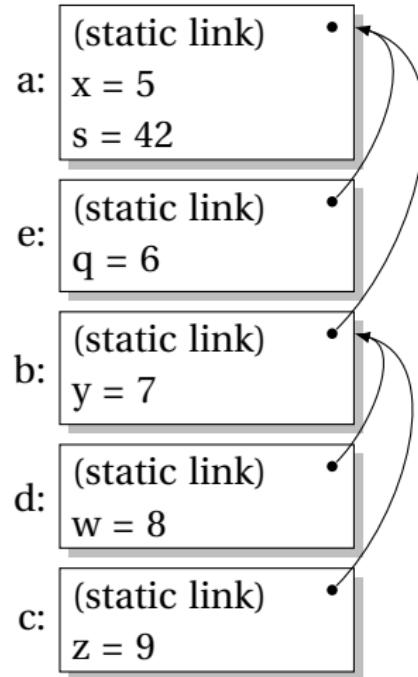


What does “a 5 42” evaluate to?

Implementing Nested Functions with Static Links

```
let a x s =  
  
  let b y =  
  
    let c z = z + s in  
  
    let d w = c (w+1) in  
  
      d (y+1) in (* b *)  
  
    let e q = b (q+1) in  
  
      e (x+1) (* a *)
```

What does “a 5 42” evaluate to?



Static vs. Dynamic Scope

```
program example;
var a : integer; (* Outer a *)

procedure seta;
begin
    a := 1 (* Which a does this change? *)
end

procedure locala;
var a : integer; (* Inner a *)
begin
    seta
end

begin
    a := 2;
    if (readln() = 'b')
        locala
    else
        seta;
    writeln(a)
end
```